

General Description

The NVMFD5875NL use advanced SGT MOSFET

technology to provide low RDS(ON), low gate charge,

fast switching and excellent avalanche characteristics.

This device is specially designed to get better ruggedness and suitable.

General Features

V_{DS} =60V I_D =50 A

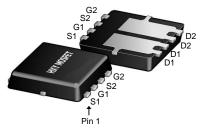
 $R_{DS(ON)}$ < 14m Ω @ V_{GS} =10V

Applications

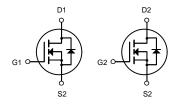
Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications



DFN5X6-8L



Dual N-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
NVMFD5875NL	DFN5X6-8L	HXY MOSFET	5000

Absolute Maximum Ratings (T_C=25 °C unless otherwise specified)

Symbol	Parameter		Max.	Units
V _{DSS}	Drain-Source Voltage		60	V
V _{GSS}	Gate-Source Voltage		±20	V
	Continuous Drain Current	T _C = 25°C	50	Α
l _D		T _C = 100 ℃	29	Α
I _{DM}	Pulsed Drain Current note1		180	Α
E _{AS}	Single Pulsed Avalanche Energy no	te2	36	mJ
P _D	Power Dissipation	T _C = 25°C	60	W
R _{θJC}	Thermal Resistance, Junction to Case		2.5	°C/W
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	$^{\circ}\!\mathbb{C}$



Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	60	-	-	V	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} =0V,	-	-	1.0	μA	
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA	
$V_{GS(th)}$	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250µA	1.0	1.6	2.5	V	
В	Static Drain-Source on-Resistance	V _{GS} =10V, I _D =20A	-	11	14	0	
R _{DS(on)}	note3	V _{GS} =4.5V, I _D =10A	-	14	20	mΩ	
C _{iss}	Input Capacitance	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	-	930	-	pF	
Coss	Output Capacitance	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	230	-	pF	
C _{rss}	Reverse Transfer Capacitance	1 - 1.0IVII 12	-	8	-	pF	
Qg	Total Gate Charge	\/ -20\/ I -20A	-	22	-	nC	
Q _{gs}	Gate-Source Charge	V_{DS} =30V, I_{D} =20A, V_{GS} =10V	-	4.5	-	nC	
Q _{gd}	Gate-Drain("Miller") Charge	VGS-10V	-	3.5	-	nC	
t _{d(on)}	Turn-on Delay Time		-	4.5	-	ns	
t _r	Turn-on Rise Time	V _{DD} =30V, I _D =20A,	-	2.7	-	ns	
t _{d(off)}	Turn-off Delay Time	$R_G=1.6\Omega$, $V_{GS}=10V$	-	13.8	-	ns	
t _f	Turn-off Fall Time		-	2.7	-	ns	
Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	45	Α	
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	180	Α	
V _{SD}	Drain to Source Diode Forward	V _{GS} =0V, I _S =30A	-	-	1.2	V	
V SD	Voltage	VGS-0V, IS-50A					
t _{rr}	Body Diode Reverse Recovery Time	 Tյ=25℃,	-	18	-	ns	
Qrr	Body Diode Reverse Recovery Charge	I _F =20A,dI/dt=100A/μs	-	12	-	nC	

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition: TJ=25 $^{\circ}\text{C}$, VDD=30V, VG=10V, RG=25 Ω , L=0.5mH, IAS=12A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



Typical Performance Characteristics

Figure1: Output Characteristics

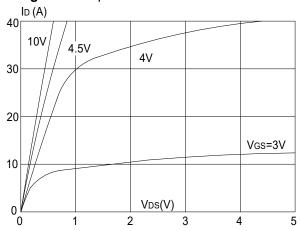


Figure 2: Typical Transfer Characteristics

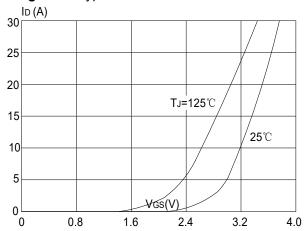


Figure 3:On-resistance vs. Drain Current

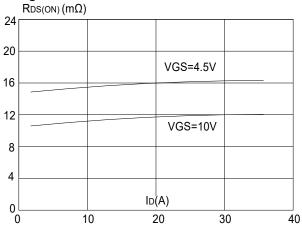


Figure 4: Body Diode Characteristics

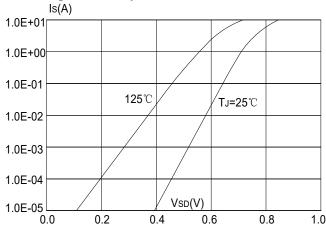


Figure 5: Gate Charge Characteristics

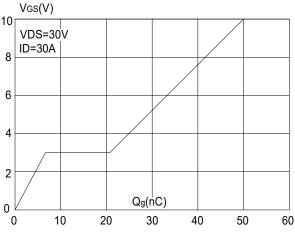


Figure 6: Capacitance Characteristics

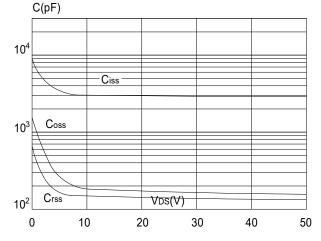




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

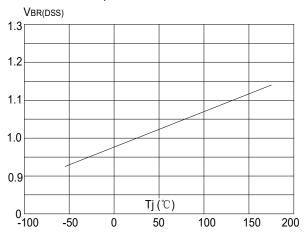


Figure 8: Normalized on Resistance vs. Junction Temperature

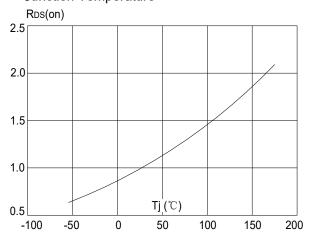


Figure 9: Maximum Safe Operating Area

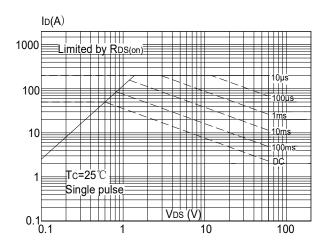


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

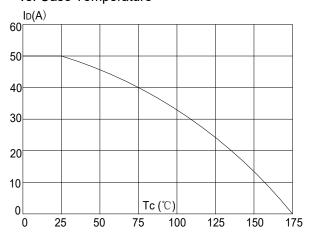
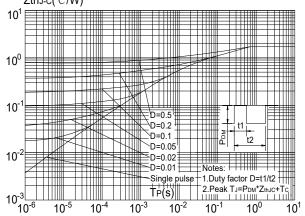
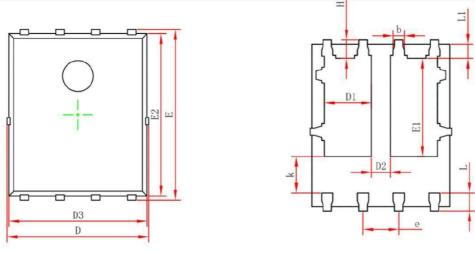


Figure.11: Maximum Effective
Transient Thermal Impedance, Junction-to-Case
ZthJ⋅c(℃/W)



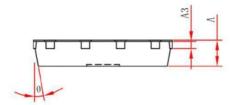


PackageMechanicalData-PDFN5X6-8L



Top View

Bottom View



Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	0.900	1.000	0.035	0.039	
A3	0.154	REF.	0.006REF.		
D	4.944	5.096	0.195	0.201	
E	5.974	6.126	0.235	0.241	
D1	1.470	1.870	0.058	0.074	
D2	0.470	0.870	0.019	0.034	
E1	3.375	3.575	0.133	0.141	
D3	4.824	4.976	0.190	0.196	
E2	5.674	5.826	0.223	0.229	
k	1.190	1.390	0.047	0.055	
b	0.350	0.450	0.014	0.018	
е	1.270TYP.		0.050TYP.		
L	0.559	0.711	0.022	0.028	
L1	0.424	0.576	0.017	0.023	
Н	0.574	0.726	0.023	0.029	
θ	10°	12°	10°	12°	

Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.

 HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc.

 When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.