#### Dual N-SGT Enhancement Mode MOSFET

### **General Description**

The IPG20N06S2L-50A use advanced SGT MOSFET

technology to provide low RDS(ON), low gate charge,

fast switching and excellent avalanche characteristics.

This device is specially designed to get better ruggedness and suitable.

#### **General Features**

V<sub>DS</sub> =60V I<sub>D</sub> =50 A

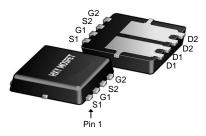
 $R_{DS(ON)}$  < 14m $\Omega$  @  $V_{GS}$ =10V

### **Applications**

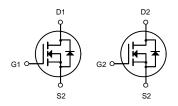
Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications



DFN5X6-8L



**Dual N-Channel MOSFET** 

**Package Marking and Ordering Information** 

Product ID	Pack	Brand	Qty(PCS)
IPG20N06S2L-50A	DFN5X6-8L	HXY MOSFET	5000

### **Absolute Maximum Ratings** (T<sub>C</sub>=25 ℃ unless otherwise specified)

Symbol	Parameter		Max.	Units
V <sub>DSS</sub>	Drain-Source Voltage		60	V
V <sub>GSS</sub>	Gate-Source Voltage		±20	V
	Continuous Dusin Cumant	T <sub>C</sub> = 25°C	50	Α
l <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> = 100 ℃	29	Α
I <sub>DM</sub>	Pulsed Drain Current note1		180	Α
E <sub>AS</sub>	Single Pulsed Avalanche Energy no	te2	36	mJ
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25°C	60	W
R <sub>θJC</sub>	Thermal Resistance, Junction to Case		2.5	°C/W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	$^{\circ}\!\mathbb{C}$



## **Electrical Characteristics** (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units	
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA	60	_	-	V	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V,	-	-	1.0	μA	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	-	-	±100	nA	
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	1.0	1.6	2.5	V	
В	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	11	14	m0	
R <sub>DS(on)</sub>	note3	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	14	20	mΩ	
C <sub>iss</sub>	Input Capacitance	)/ O5)/ )/ O)/	-	930	-	pF	
Coss	Output Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz	-	230	-	pF	
Crss	Reverse Transfer Capacitance	1-1.0101112	-	8	-	pF	
Qg	Total Gate Charge	\/ -20\/ I -20\	-	22	-	nC	
Q <sub>gs</sub>	Gate-Source Charge	$V_{DS}$ =30V, $I_{D}$ =20A, $V_{GS}$ =10V	-	4.5	-	nC	
Q <sub>gd</sub>	Gate-Drain("Miller") Charge	VGS-10V	-	3.5	-	nC	
t <sub>d(on)</sub>	Turn-on Delay Time		-	4.5	-	ns	
t <sub>r</sub>	Turn-on Rise Time	V <sub>DD</sub> =30V, I <sub>D</sub> =20A,	-	2.7	-	ns	
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =1.6Ω, V <sub>GS</sub> =10V	-	13.8	-	ns	
t <sub>f</sub>	Turn-off Fall Time		-	2.7	-	ns	
Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	45	Α	
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current			-	180	Α	
V	Drain to Source Diode Forward	\/=0\/_L=20A	-	-	1.2	V	
V <sub>SD</sub>	Voltage	$V_{GS}$ =0V, $I_{S}$ =30A					
t <sub>rr</sub>	Body Diode Reverse Recovery Time	Tյ=25℃,	-	18	-	ns	
Qrr	Body Diode Reverse Recovery Charge	I <sub>F</sub> =20A,dI/dt=100A/μs	-	12	-	nC	

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition: TJ=25  $^{\circ}\text{C}$  , VDD=30V, VG=10V, RG=25 $\Omega$ , L=0.5mH, IAS=12A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



# **Typical Performance Characteristics**

Figure1: Output Characteristics

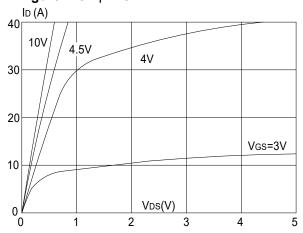


Figure 2: Typical Transfer Characteristics

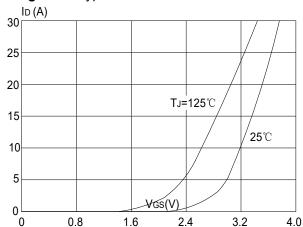


Figure 3:On-resistance vs. Drain Current

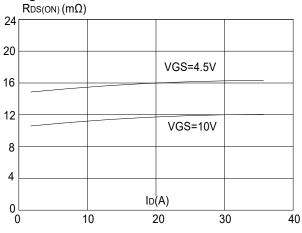


Figure 4: Body Diode Characteristics

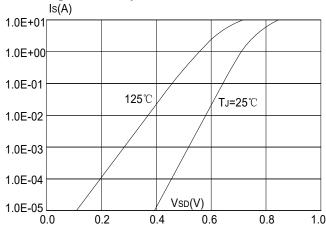


Figure 5: Gate Charge Characteristics

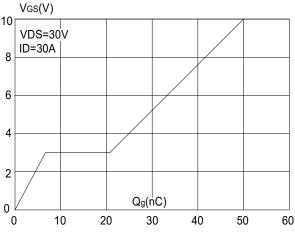
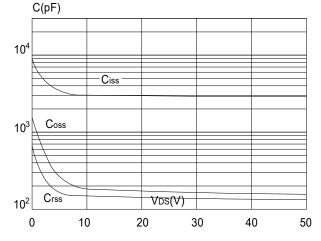
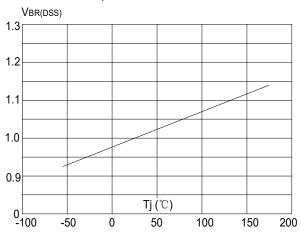


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



**Figure 8:** Normalized on Resistance vs. Junction Temperature

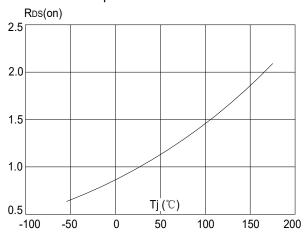
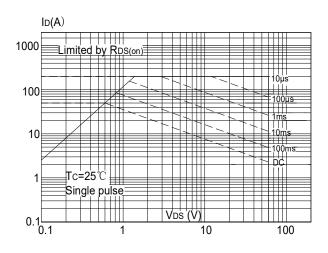


Figure 9: Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature

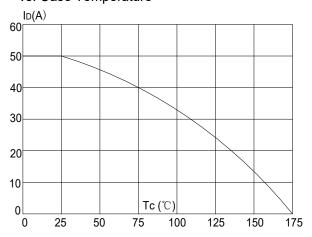
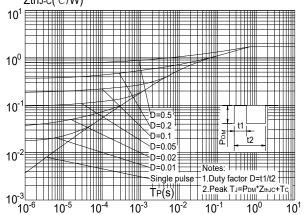
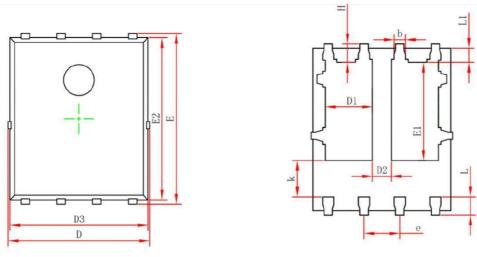


Figure.11: Maximum Effective
Transient Thermal Impedance, Junction-to-Case
ZthJ⋅c(℃/W)



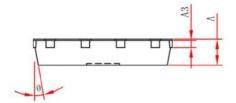


## PackageMechanicalData-PDFN5X6-8L



Top View

**Bottom View** 



Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	0.900	1.000	0.035	0.039	
A3	0.154	AREF.	0.006REF.		
D	4.944	5.096	0.195	0.201	
E	5.974	6.126	0.235	0.241	
D1	1.470	1.870	0.058	0.074	
D2	0.470	0.870	0.019	0.034	
E1	3.375	3.575	0.133	0.141	
D3	4.824	4.976	0.190	0.196	
E2	5.674	5.826	0.223	0.229	
k	1.190	1.390	0.047	0.055	
b	0.350	0.450	0.014	0.018	
е	1.270TYP.		0.050TYP.		
L	0.559	0.711	0.022	0.028	
L1	0.424	0.576	0.017	0.023	
Н	0.574	0.726	0.023	0.029	
θ	10°	12°	10°	12°	

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