

## Description

The DMN4026SSD uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

## **General Features**

 $V_{DS} = 40V I_D = 12A$   $R_{DS(ON)} < 16m\Omega @ V_{GS} = 10 V$  $R_{DS(ON)} < 24m\Omega @ V_{GS} = 4.5V$ 

# Application

Battery protection Load switch Uninterruptible power supply

## Package Marking and Ordering Information

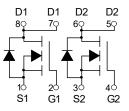
Product ID	Pack	Brand	Qty(PCS)
DMN4026SSD	SOP-8	HXY MOSFET	3000

## Absolute Maximum Ratings@Tj=25°C(unless otherwise specified)

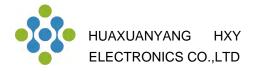
Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	40	V
V <sub>GS</sub>	Gate-Source Voltage	<u>+</u> 20	V
I₀@T₄=25℃	Drain Current, V <sub>GS</sub> @ 4.5V <sup>3</sup>	12	А
ID@T <sub>A</sub> =70°C	Drain Current, V <sub>GS</sub> @ 4.5V <sup>3</sup>	7	A
Ідм	Pulsed Drain Current <sup>1</sup>	40	А
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation	2.9	W
Тята	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
Rthj-a	Maximum Thermal Resistance, Junction- ambient <sup>3</sup>	65	°C/W



SOP-8



#### **Dual N-Channel MOSFET**



Parameter	Symbol	Condition	Min	Тур	Мах	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	BV <sub>DSS</sub> V <sub>GS</sub> =0V I <sub>D</sub> =250μA		-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	I <sub>DSS</sub> V <sub>DS</sub> =40V,V <sub>GS</sub> =0V		-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250µA	1	1.5	2.0	V
Drain-Source On-State Resistance	Р	V <sub>GS</sub> =10V, I <sub>D</sub> =8A	-	12.0	16	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	-	18.9	24	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =5V,I <sub>D</sub> =8A	33	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C <sub>lss</sub>		-	964	-	PF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V, F=1.0MHz	-	109	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	96	-	PF
Switching Characteristics (Note 4)	· · ·					
Turn-on Delay Time	t <sub>d(on)</sub>		-	5.5	-	nS
Turn-on Rise Time	tr	$V_{DD}$ =20V, R <sub>L</sub> =2.5 $\Omega$	-	14	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{GEN}$ =3 $\Omega$	-	24	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Qg	)/ _20)// _04	-	22.9	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =20V,I <sub>D</sub> =8A, V <sub>GS</sub> =10V	-	3.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> -10V	-	5.3	-	nC
Drain-Source Diode Characteristics				•		•
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =9A	-	0.8	1.2	V

## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)



# **Typical Electrical and Thermal Characteristics (Curves)**

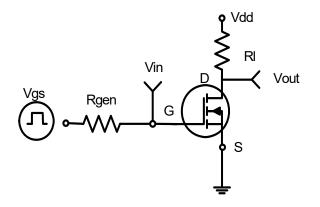


Figure 1:Switching Test Circuit

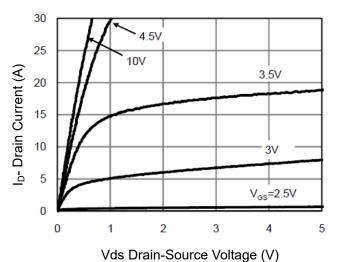
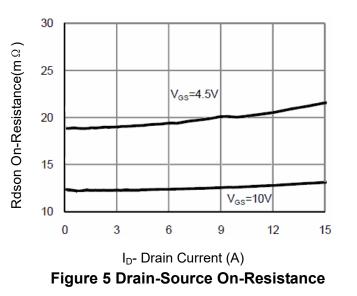


Figure 3 Output Characteristics



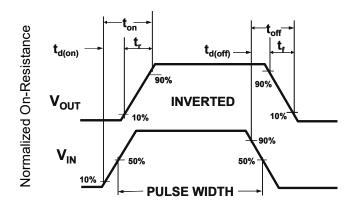


Figure 2:Switching Waveforms

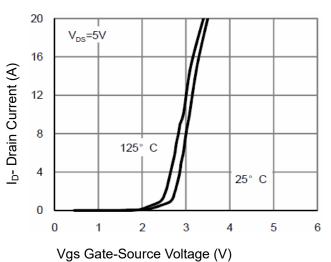


Figure 4 Transfer Characteristics

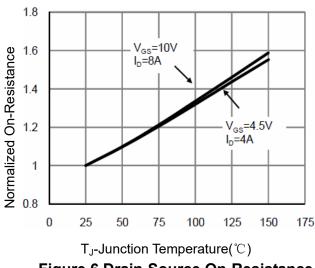
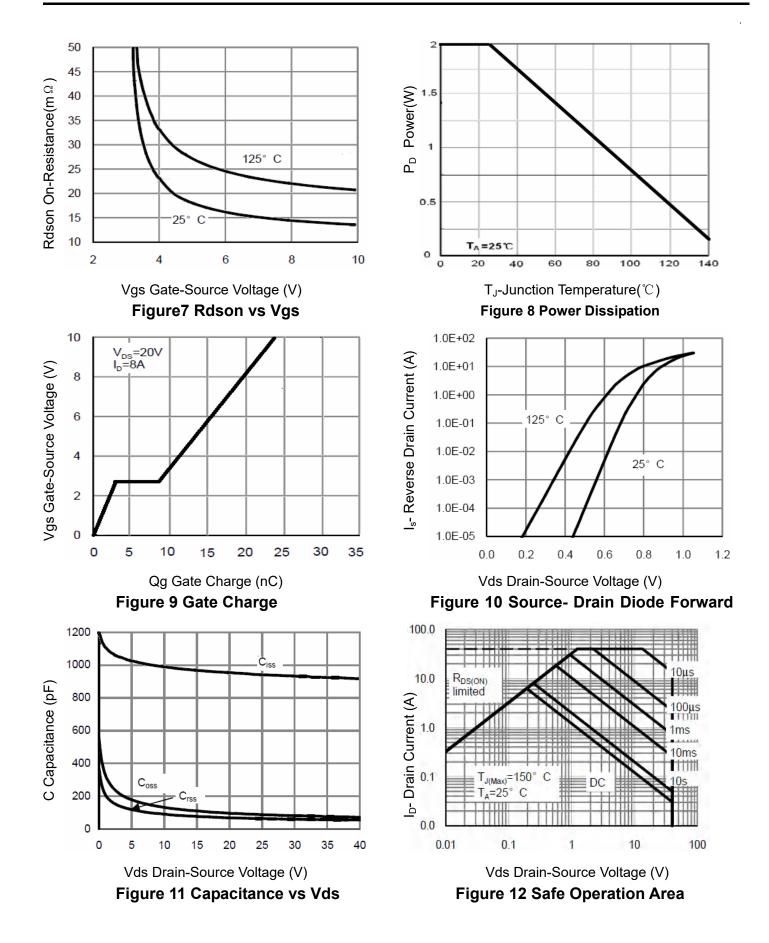


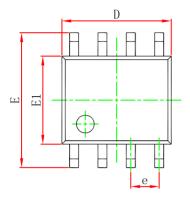
Figure 6 Drain-Source On-Resistance

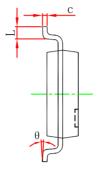


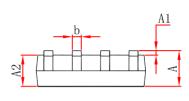




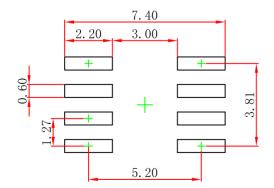
# **SOP-8 Package Outline Dimensions**







Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
с	0.170	0.250	0.007	0.010	
D	4.800	5.000	0.189	0.197	
e	1.270 (BSC)		0.050 (BSC)		
E	5.800	6.200	0.228	0.244	
E1	3.800	4.000	0.150	0.157	
L	0.400	1.270	0.016	0.050	
θ	0 °	8°	0 °	8°	



Note: 1.Controlling dimension: in millimeters.

2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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