

#### **Description**

The STD12NF06LT4 uses advanced trench technology

to provide excellent  $R_{DS(ON)}$ , low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.



TO-252-2L

#### **General Features**

 $V_{DS} = 60V I_{D} = 15 A$ 

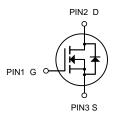
 $R_{DS(ON)}$  < 49m $\Omega$  @  $V_{GS}$ =10V

#### **Application**

Battery protection

Load switch

Uninterruptible power supply



N-Channel MOSFET

## **Package Marking and Ordering Information**

Product ID	Pack	Brand	Qty(PCS)
STD12NF06LT4	TO-252-2L	HXY MOSFET	2500

## Absolute Maximum Ratings (T<sub>C</sub>=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units		
V <sub>DS</sub>	Drain-Source Voltage	60	V		
Vgs	Gate-Source Voltage	Gate-Source Voltage ±20			
Ip@T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	ntinuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup> 15			
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup> 9.8			
Ідм	Pulsed Drain Current <sup>2</sup>	Pulsed Drain Current <sup>2</sup> 60			
EAS	Single Pulse Avalanche Energy <sup>3</sup>	Single Pulse Avalanche Energy <sup>3</sup> 9.3			
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	Power Dissipation <sup>4</sup> 24			
Тѕтс	Storage Temperature Range	-55 to 175	°C		
TJ	Operating Junction Temperature Range	-55 to 175	°C		



## **Electrical Characteristics** (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units	
Off Charac	Off Characteristic						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	60	-	-	V	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V,	-	-	1.0	μA	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA	
On Charac	On Characteristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1	1.6	2.5	V	
П	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =15A	-	38	49	mΩ	
R <sub>DS(on)</sub>	note3	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	45	63		
Dynamic C	Characteristics		•				
C <sub>iss</sub>	Input Capacitance	)/ OF)/ )/ O)/	-	825	-	pF	
Coss	Output Capacitance	$V_{DS}$ =25V, $V_{GS}$ =0V, f=1.0MHz	-	49	-	pF	
Crss	Reverse Transfer Capacitance	1 I – I .UIVIMZ	-	41	-	pF	
Qg	Total Gate Charge	\/ -20\/ I -4.5A	-	14	-	nC	
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =30V, I <sub>D</sub> =4.5A,	-	2.9	-	nC	
Q <sub>gd</sub>	Gate-Drain("Miller") Charge	V <sub>GS</sub> =10V	-	5.2	-	nC	
Switching	Characteristics						
t <sub>d(on)</sub>	Turn-on Delay Time	.,	-	5	-	ns	
t <sub>r</sub>	Turn-on Rise Time	V <sub>DS</sub> =30V,I <sub>D</sub> =2A,	-	2.6	-	ns	
t <sub>d(off)</sub>	Turn-off Delay Time	$R_L=6.7\Omega, R_G=3\Omega,$ $V_{GS}=10V$	-	16.1	-	ns	
t <sub>f</sub>	Turn-off Fall Time	V <sub>GS</sub> -10V	-	2.3	-	ns	
Drain-Soul	rce Diode Characteristics and Maxim	um Ratings	•				
	Maximum Continuous Drain to Source	Diode Forward			15	Λ	
I <sub>S</sub>	Current		-	-	15	A	
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	60	Α	
V <sub>SD</sub>	Drain to Source Diode Forward	V <sub>GS</sub> =0V, I <sub>S</sub> =15A	_		1.2	V	
V 3D	Voltage	VG3 VV, 13 10/1			1.2	•	
trr	Body Diode Reverse Recovery Time	   T <sub>J</sub> =25℃,I <sub>F</sub> =15A,	-	35	-	ns	
Qrr	Body Diode Reverse Recovery Charge	dl/dt=100A/µs	-	53	-	nC	
	Charge					L	

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

<sup>2.</sup> EAS condition : T<sub>J</sub>=25  $^{\circ}$ C ,V<sub>DD</sub>=30V,V<sub>G</sub>=10V,L=0.5mH,Rg=25 $\Omega$ ,I<sub>AS</sub>=6.1A

<sup>3.</sup> Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



# **Typical Performance Characteristics**

Figure1: Output Characteristics

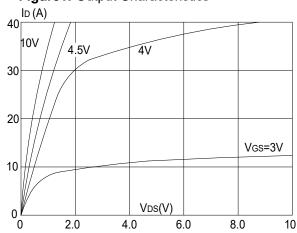


Figure 2: Typical Transfer Characteristics

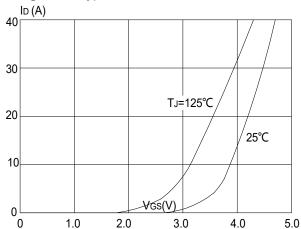


Figure 3:On-resistance vs. Drain Current

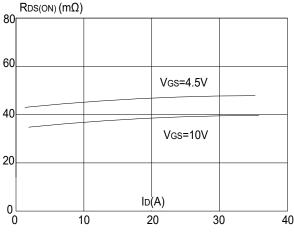


Figure 4: Body Diode Characteristics

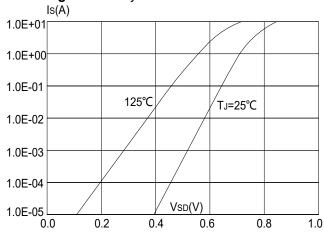


Figure 5: Gate Charge Characteristics

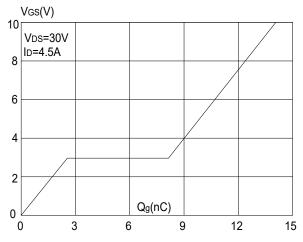
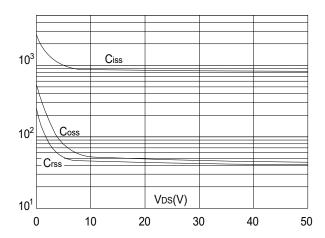


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

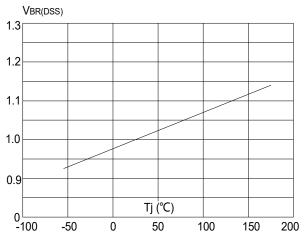


Figure 9: Maximum Safe Operating Area

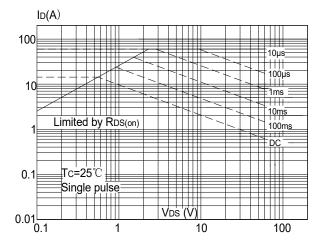
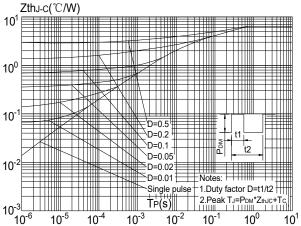
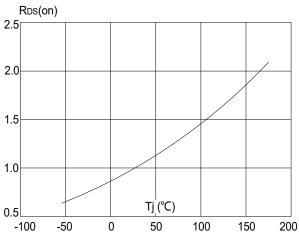


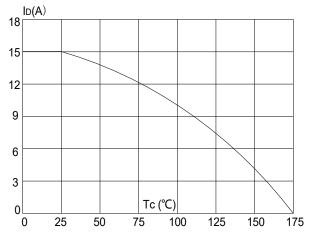
Figure.11: Maximum Effective
Transient Thermal Impedance, Junction-to-Case



**Figure 8:** Normalized on Resistance vs. Junction Temperature

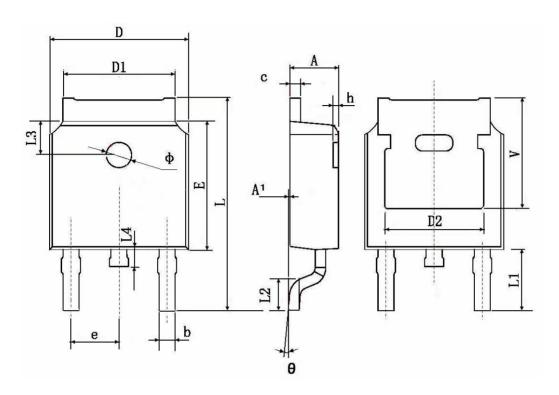


**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature

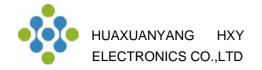




# **TO-252-2L Package Information**



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	0.483	0.483 TYP.		0.190 TYP.	
Е	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600	TYP.	0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211 TYP.		



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