

Description

The FDMC8884 uses advanced trench technology

to provide excellent $R_{DS(ON)}$, low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

General Features

V_{DS} = 30V I_D = 20 A

 $R_{DS(ON)} < 20m\Omega @ V_{GS}=10V$

Application

Battery protection

Load switch

Uninterruptible power supply

Package Marking and Ordering Information

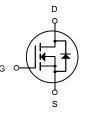
Product ID	Pack	Brand	Qty(PCS)
FDMC8884	DFN3X3-8L	HXY MOSFET	5000

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	30	V
VGS	Gate-Source Voltage	±20	V
I₀@Tc=25°C	Continuous Drain Current, V _{GS} @ 10V ¹	20	А
I₀@Tc=100°C	Continuous Drain Current, V _{GS} @ 10V ¹	8	А
IDM	Pulsed Drain Current ²	38	А
EAS	Single Pulse Avalanche Energy ³	28	mJ
IAS	Avalanche Current	13.8	А
P₀@Tc=25°C	Total Power Dissipation ⁴	5.5	W
TSTG	Storage Temperature Range	-55 to 175	°C
TJ	Operating Junction Temperature Range	-55 to 175	°C
R₀JC	Thermal Resistance Junction-Case ¹	36	°C/W







N-Channel MOSFET



Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250µA	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V_{DS} =30V, V_{GS} = 0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V_{DS} =0V, V_{GS} = ±20V	-	-	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250µA	1.0	1.5	2.5	V
D	Static Drain-Source on-Resistance	V _{GS} =10V, I _D =5A	-	15	20	mΩ
$R_{DS(on)}$		V _{GS} =4.5V, I _D =3A	-	21	29	
Ciss	Input Capacitance		-	490	-	pF
Coss	Output Capacitance	V _{DS} =15V, V _{GS} =0V,	-	79	-	pF
Crss	Reverse Transfer Capacitance	f=1.0MHz	-	61	-	pF
Qg	Total Gate Charge	V _{DS} =15V, I _D =5.8A, V _{GS} =10V	-	10	-	nC
Q _{gs}	Gate-Source Charge		-	1.7	-	nC
Q_gd	Gate-Drain("Miller") Charge		-	2.5	-	nC
t _{d(on)}	Turn-on Delay Time		-	6	-	ns
tr	Turn-on Rise Time	V _{DS} =15V, I _D =3A, V _{GS} =10V, R _{REN} =3Ω	-	15	-	ns
t _{d(off)}	Turn-off Delay Time		-	17	-	ns
t _f	Turn-off Fall Time		-	17	-	ns
ls	Maximum Continuous Drain to Source Diode Forward Current		-	-	9	А
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	36	А
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =9A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	7	-	ns
Qrr	Body Diode Reverse Recovery Charge	l _F =5A, dl/dt=100A/µs	-	2	-	nC

Electrical Characteristics (T_J=25°C unless otherwise specified)

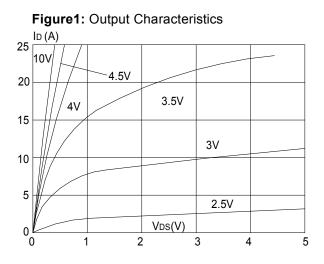
Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

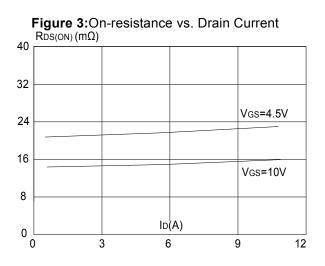
2. EAS condition : T_J=25 $^\circ \!\! C$,V_DD=15V,V_G=10V,L=0.5mH,Rg=25\Omega,I_{AS}=6A

3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



Typical Performance Characteristics





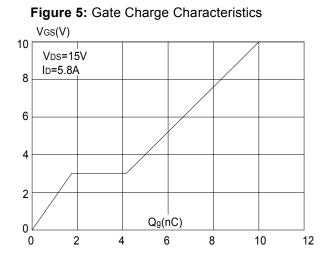
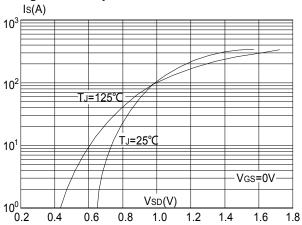
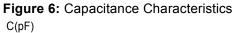


Figure 2: Typical Transfer Characteristics 25 (A) 20 15 12⁵℃ 10 ′25℃ 5 √gs(V) 0 3.0 0 2.0 4.0 5.0 1.0 6.0







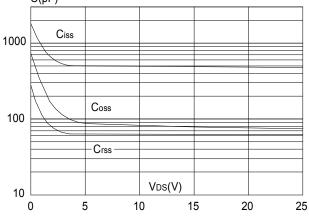
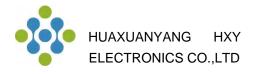


Figure 4: Body Diode Characteristics



FDMC8884 N-Channel Enhancement Mode MOSFET

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

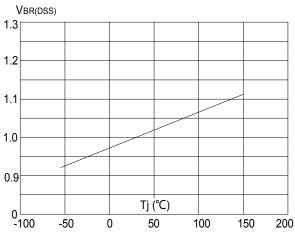


Figure 9: Maximum Safe Operating Area

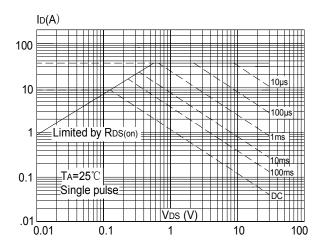


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

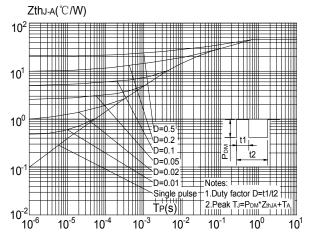


Figure 8: Normalized on Resistance vs. Junction Temperature

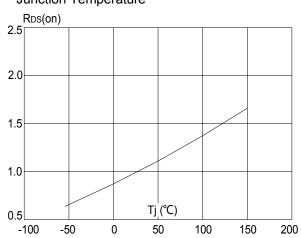
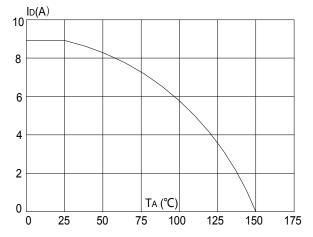
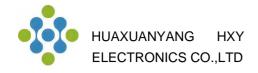


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature





Test Circuit

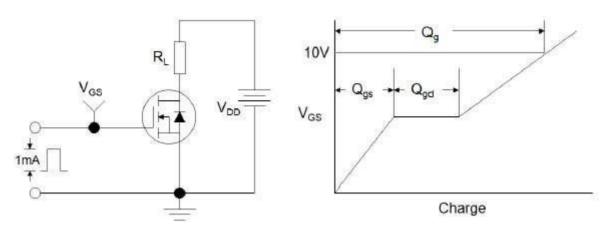


Figure1:Gate Charge Test Circuit & Waveform

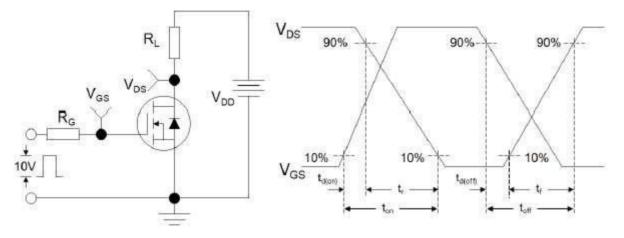


Figure 2: Resistive Switching Test Circuit & Waveforms

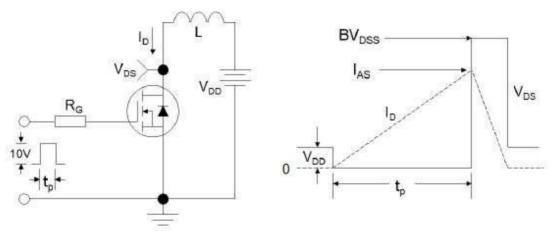
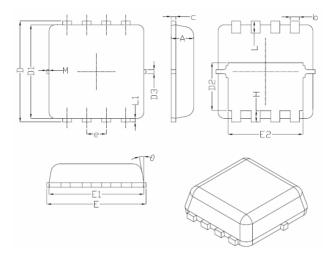


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



DFN3X3-8L Package Information



Sumbal	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
с	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
e	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
Μ	*	*	0.15	
θ		10 [°]	12 [°]	



Attention

Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.

• HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.

• Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

• HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could

give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

■ In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

• No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.

Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production.
HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.