

#### Description

The DMP3036SFV uses advanced trench technology

to provide excellent R<sub>DS(ON)</sub>, low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> =-30V I<sub>D</sub> =-25A

 $R_{DS(ON)} < 20m\Omega @ V_{GS}=-10V$ 

## Application

Battery protection

Load switch

Uninterruptible power supply

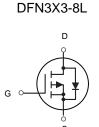
#### Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
DMP3036SFV	SOP-8	HXY MOSFET	3000

## Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-30	V
VGS	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-25	A
I <sub>D</sub> @Tc=100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-20	A
IDM	Pulsed Drain Current <sup>2</sup>	-65	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	72.2	mJ
P₀@Tc=25°C	Total Power Dissipation <sup>4</sup>	29	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JC	Thermal Resistance Junction-Case <sup>1</sup>	2.8	°C/W





P-Channel MOSFET

## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-30			V	
$\bigtriangleup BV_{\text{DSS}} \bigtriangleup T_J$	BV <sub>DSS</sub> Temperature Coefficient	Reference to $25^{\circ}$ C $, I_{D}$ =-1mA		-0.022		V/°C	
R <sub>DS(ON)</sub>	Statis Drain Source On Desistance <sup>2</sup>	V <sub>GS</sub> =-10V , I <sub>D</sub> =-15A		16	20	mΩ	
	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-10A		22	32		
V <sub>GS(th)</sub>	Gate Threshold Voltage		-1.0		-2.5	V	
$\bigtriangleup V_{\text{GS(th)}}$	V <sub>GS(th)</sub> Temperature Coefficient	──V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA		4.6		mV/°C	
I	I <sub>DSS</sub> Drain-Source Leakage Current	V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			-1	uA	
IDSS		V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			-5		
lgss	Gate-Source Leakage Current	$V_{GS} = \pm 25V$ , $V_{DS} = 0V$			±100	nA	
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		13		Ω	
Qg	Total Gate Charge (-4.5V)			52			
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =-15V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-15A		9.8		nC	
Q <sub>gd</sub>	Gate-Drain Charge			8.3			
T <sub>d(on)</sub>	Turn-On Delay Time			13			
Tr	Rise Time	$V_{DD}$ =-15V , $V_{GS}$ =-10V , $R_{G}$ =3.3 $\Omega$ ,		15			
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =-15A		198		ns	
T <sub>f</sub>	Fall Time			98			
Ciss	Input Capacitance			1150			
Coss	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz		150		pF	
Crss	Reverse Transfer Capacitance			134			
ls	Continuous Source Current <sup>1,5</sup>	−−−V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-32	А	
Ism	Pulsed Source Current <sup>2,5</sup>				-65	А	
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25°C			-1.2	V	

Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper. 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%

3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =-25V,  $V_{GS}$ =-10V, L=0.1mH,  $I_{AS}$ =-38A

4. The power dissipation is limited by 150°C junction temperature

5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications , should be limited by total power dissipation.



## **Typical Characteristics**

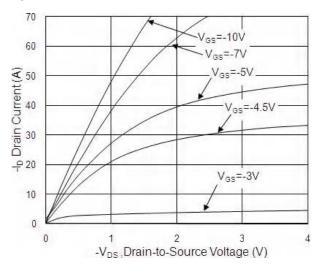


Fig.1 Typical Output Characteristics

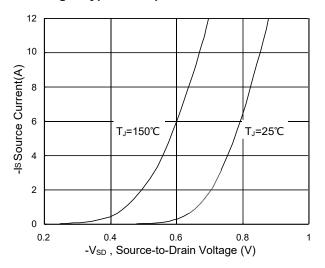
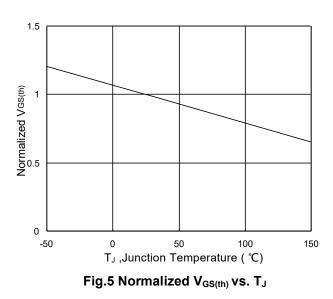


Fig.3 Forward Characteristics of Reverse



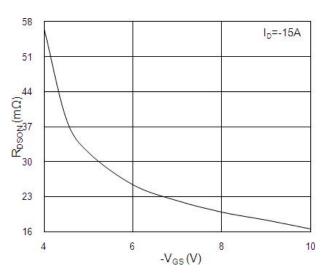


Fig.2 On-Resistance v.s Gate-Source

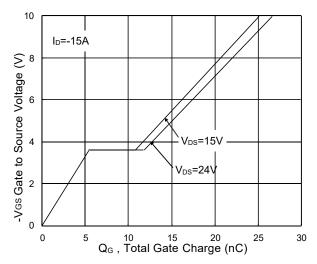


Fig.4 Gate-Charge Characteristics

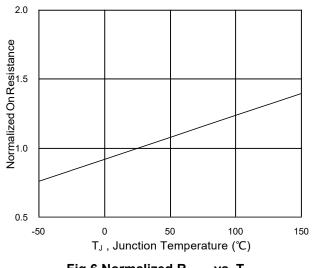
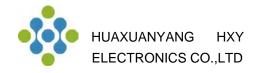


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>



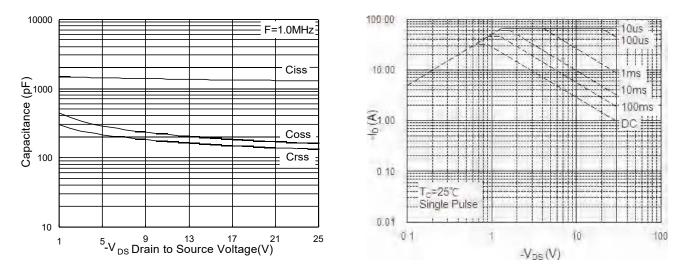
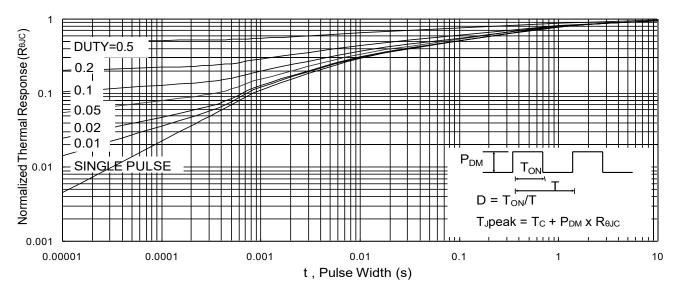


Fig.7 Capacitance

Fig.8 Safe Operating Area





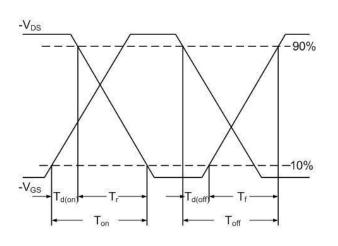
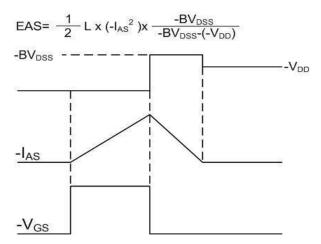


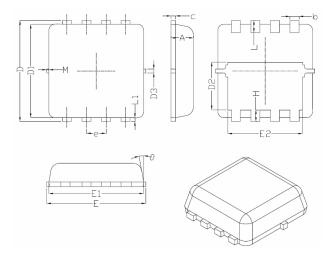
Fig.10 Switching Time Waveform







# DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
С	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
Н	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
М	*	*	0.15
θ		10 <sup>°</sup>	12 <sup>°</sup>



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