

Description

The BUK9M53-60EX uses advanced trench technology

to provide excellent R_{DS(ON)}, low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

General Features

 $V_{DS} = 60V I_{D} = 20 A$

 $R_{DS(ON)}$ < 40m Ω @ V_{GS} =10V

Application

Battery protection

Load switch

Uninterruptible power supply

Package Marking and Ordering Information

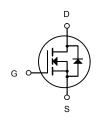
| Product ID | Pack | Brand | Qty(PCS) |
|--------------|-----------|------------|----------|
| BUK9M53-60EX | DFN3X3-8L | HXY MOSFET | 5000 |

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

| Symbol | Parameter | Rating | Units |
|--------------------------------------|--|------------|-------|
| Vos | Drain-Source Voltage | 60 | V |
| Vgs | Gate-Source Voltage | ±20 | V |
| I _D @T _A =25°C | Continuous Drain Current, V _{GS} @ 10V ¹ | 20 | А |
| I _D @T _A =70°C | Continuous Drain Current, V _{GS} @ 10V ¹ | 10 | А |
| Ідм | Pulsed Drain Current ² | 46 | А |
| EAS | Single Pulse Avalanche Energy ³ | 25.5 | mJ |
| las | Avalanche Current | 20 | А |
| P _D @T _C =25°C | Total Power Dissipation ⁴ | 34.7 | W |
| Тѕтс | Storage Temperature Range | -55 to 175 | °C |
| TJ | Operating Junction Temperature Range | -55 to 175 | °C |
| R _θ JA | Thermal Resistance Junction-Ambient ¹ | 62 | °C/W |



DFN3X3-8L



N-Channel MOSFET

N-Channel Enhancement Mode MOSFET

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

| Symbol | Parameter | Test Condition | Min. | Тур. | Max. | Units |
|----------------------|--|---|------|------|------|-------|
| V _{(BR)DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =250μA | 60 | - | - | V |
| I _{DSS} | Zero Gate Voltage Drain Current | V_{DS} =60V, V_{GS} = 0V, | - | - | 1.0 | μΑ |
| I _{GSS} | Gate to Body Leakage Current | V_{DS} =0V, V_{GS} = ±20V | - | - | ±100 | nA |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} =V _{GS} , I _D =250µA | 1.0 | 1.6 | 2.5 | V |
| П | Static Drain-Source on-Resistance | V _{GS} =10V, I _D =5A | - | 28 | 40 | mΩ |
| $R_{DS(on)}$ | note3 | V _{GS} =4.5V, I _D =3A | - | 36 | 50 | |
| C _{iss} | Input Capacitance | \\ -05\\ \\ -0\\ | - | 1148 | - | pF |
| Coss | Output Capacitance | V _{DS} =25V, V _{GS} =0V, f=1.0MHz | - | 58.5 | - | pF |
| C _{rss} | Reverse Transfer Capacitance | | - | 49.4 | - | pF |
| Qg | Total Gate Charge | V _{DS} =30V, I _D =2.5A, V _{GS} =10V | - | 20.3 | - | nC |
| Q _{gs} | Gate-Source Charge | | - | 3.7 | - | nC |
| Q_{gd} | Gate-Drain("Miller") Charge | | - | 5.3 | - | nC |
| t _{d(on)} | Turn-on Delay Time | | - | 7.6 | _ | ns |
| t _r | Turn-on Rise Time | V_{DS} =30V, I_{D} =5A, R_{G} =1.8 Ω , V_{GS} =10V | - | 20 | - | ns |
| t _{d(off)} | Turn-off Delay Time | | - | 15 | - | ns |
| t f | Turn-off Fall Time | | - | 24 | - | ns |
| Is | Maximum Continuous Drain to Source Diode Forward Current | | _ | - | 5 | Α |
| I _{SM} | Maximum Pulsed Drain to Source Diode Forward Current | | - | - | 20 | Α |
| M | Drain to Source Diode Forward | V _{GS} =0V, I _S =5A | | | 1.2 | V |
| V _{SD} | Voltage | VGS-UV, IS-DA | _ | - | 1.2 | V |
| trr | Body Diode Reverse Recovery Time | | - | 29 | - | ns |
| Qrr | Body Diode Reverse Recovery Charge | I _F =5A, dl/dt=100A/μs | - | 43 | - | nC |

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition : T_J=25 $^{\circ}$ C,V_{DD}=30V,V_G=10V,L=0.5mH,Rg=25 Ω ,I_{AS}=8.7A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



Typical Characteristics

Figure1: Output Characteristics

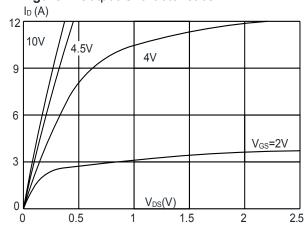


Figure 2: Typical Transfer Characteristics

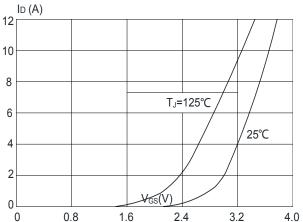


Figure 3:On-resistance vs. Drain Current RDS(ON) $(m\Omega)$

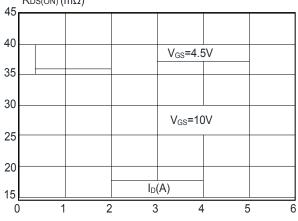


Figure 4: Body Diode Characteristics

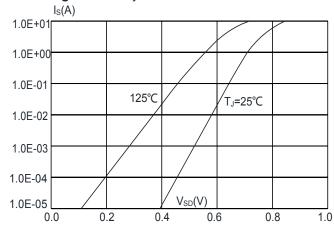


Figure 5: Gate Charge Characteristics

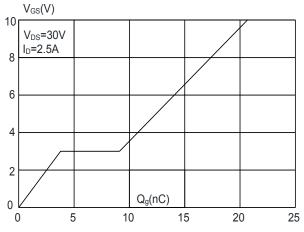


Figure 6: Capacitance Characteristics

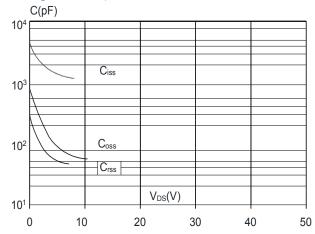




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

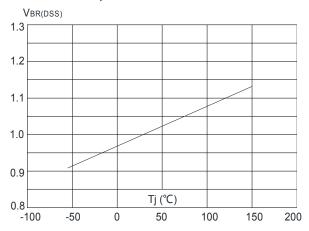


Figure 9: Maximum Safe Operating Area

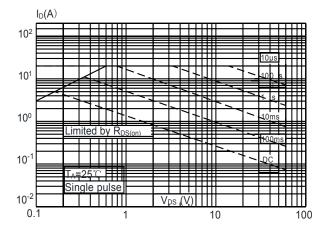


Figure.11: Maximum Effective
Transient Thermal Impedance, Junction-to-Ambient
7th (*C/W)

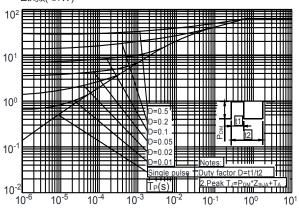


Figure 8: Normalized on Resistance vs. Junction Temperature

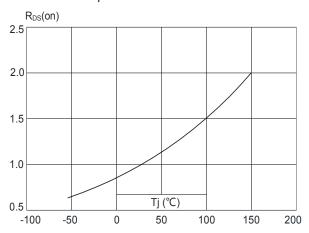
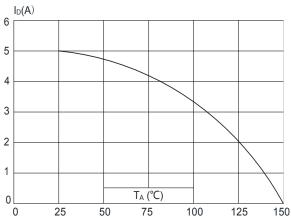


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature



Test Circuit

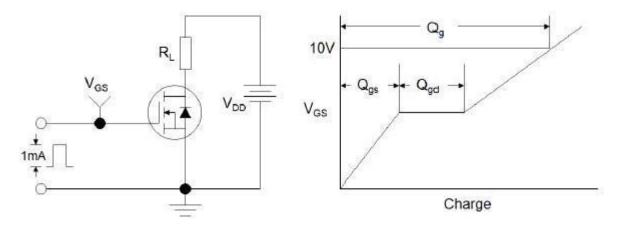


Figure1:Gate Charge Test Circuit & Waveform

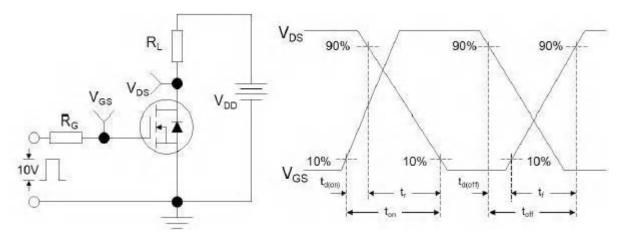


Figure 2: Resistive Switching Test Circuit & Waveforms

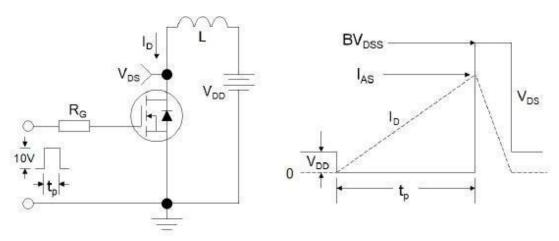
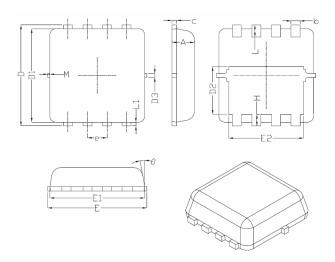


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

DFN3X3-8L Package Information



| Complete I | Dimensions In Millimeters | | | |
|------------|---------------------------|------|-----------------|--|
| Symbol | Min. | Nom. | Max. | |
| A | 0.70 | 0.75 | 0.80 | |
| b | 0.25 | 0.30 | 0.35 | |
| С | 0.10 | 0.15 | 0.25 | |
| D | 3.25 | 3.35 | 3.45 | |
| D1 | 3.00 | 3.10 | 3.20 | |
| D2 | 1.48 | 1.58 | 1.68 | |
| D3 | - | 0.13 | - | |
| E | 3.20 | 3.30 | 3.40 | |
| E1 | 3.00 | 3.15 | 3.20 | |
| E2 | 2.39 | 2.49 | 2.59 | |
| е | 0.65BSC | | | |
| Н | 0.30 | 0.39 | 0.50 | |
| L | 0.30 | 0.40 | 0.50 | |
| L1 | - | 0.13 | - | |
| M | * | * | 0.15 | |
| θ | | 10° | 12 [°] | |



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