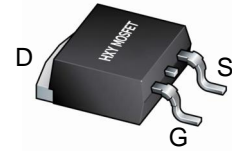




## Description

The DMN6040SK3 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



TO-252-2L

## General Features

$V_{DS} = 60V$   $I_D = 20A$

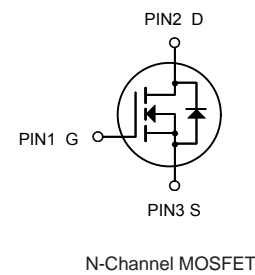
$R_{DS(ON)} < 32m\Omega$  @  $V_{GS} = 10V$

## Application

Battery protection

Load switch

Uninterruptible power supply



## Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
DMN6040SK3	TO-252-2L	HXY MOSFET	2500

## Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	20	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	10	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	80	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	38	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation <sup>4</sup>	34.7	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$



**Electrical Characteristics (T<sub>J</sub> = 25°C, unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	<b>V<sub>(BR)DSS</sub></b>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	60	-	-	V
Gate-Body Leakage Current	<b>I<sub>GSS</sub></b>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T <sub>J</sub> =25°C	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	1	μA
	T <sub>J</sub> =100°C		-	-	100	
Gate-Threshold Voltage	<b>V<sub>GS(th)</sub></b>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.2	1.7	2.5	V
Drain-Source on-Resistance <sup>4</sup>	<b>R<sub>DS(on)</sub></b>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A	-	25	32	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5A	-	31.5	40	
Forward Transconductance <sup>4</sup>	<b>g<sub>fs</sub></b>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 10A	-	15.5	-	S
<b>Dynamic Characteristics<sup>5</sup></b>						
Input Capacitance	<b>C<sub>iss</sub></b>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz	-	1355	-	pF
Output Capacitance	<b>C<sub>oss</sub></b>		-	60	-	
Reverse Transfer Capacitance	<b>C<sub>rss</sub></b>		-	49	-	
Gate Resistance	<b>R<sub>G</sub></b>	f = 1MHz	-	1.2	-	Ω
<b>Switching Characteristics<sup>5</sup></b>						
Total Gate Charge	<b>Q<sub>g</sub></b>	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 30V, I <sub>D</sub> = 10A	-	22	-	nC
Gate-Source Charge	<b>Q<sub>gs</sub></b>		-	4.2	-	
Gate-Drain Charge	<b>Q<sub>gd</sub></b>		-	6.9	-	
Turn-on Delay Time	<b>t<sub>d(on)</sub></b>	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 30V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = 10A	-	6.4	-	ns
Rise Time	<b>t<sub>r</sub></b>		-	15.3	-	
Turn-off Delay Time	<b>t<sub>d(off)</sub></b>		-	25	-	
Fall Time	<b>t<sub>f</sub></b>		-	7.6	-	
Body Diode Reverse Recovery Time	<b>t<sub>rr</sub></b>	I <sub>F</sub> = 10A, dI <sub>F</sub> /dt = 100A/μs	-	26	-	ns
Body Diode Reverse Recovery Charge	<b>Q<sub>rr</sub></b>		-	45	-	nC
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	<b>V<sub>SD</sub></b>	I <sub>S</sub> = 10A, V <sub>GS</sub> = 0V	-	-	1.2	V
Continuous Source Current	T <sub>C</sub> =25°C	<b>I<sub>S</sub></b>	-	-	20	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub> = 150°C
2. The EAS data shows Max. rating. The test condition is V<sub>DD</sub> = 25V, V<sub>GS</sub> = 10V, L = 0.4mH, I<sub>AS</sub> = 14A
3. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.



## Typical Characteristics

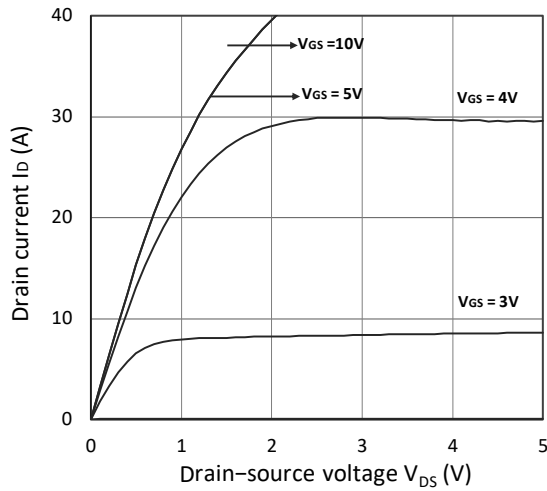


Figure 1. Output Characteristics

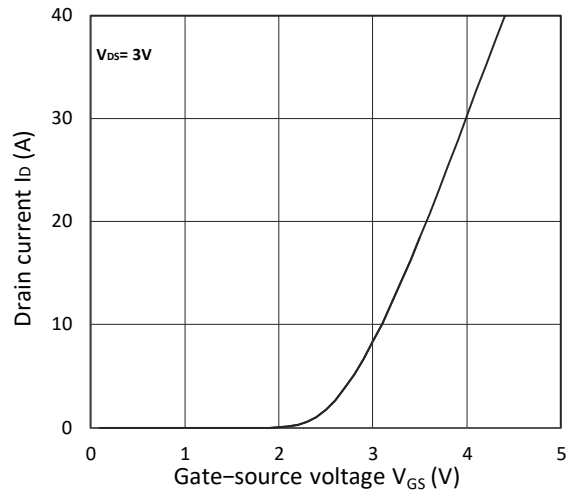


Figure 2. Transfer Characteristics

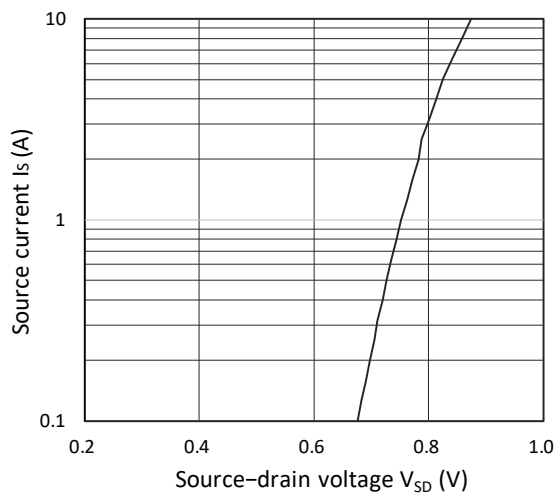


Figure 3. Forward Characteristics of Reverse

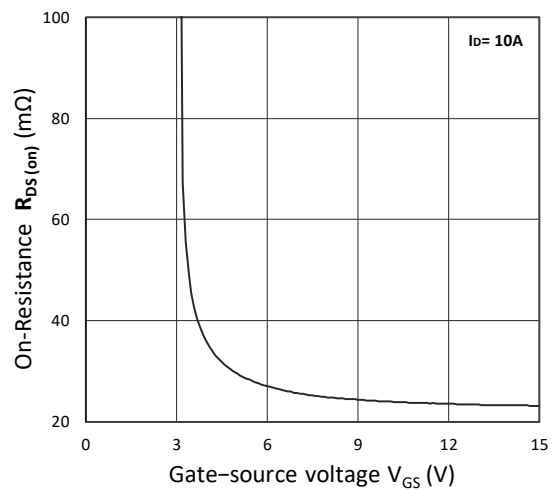


Figure 4.  $R_{DS(ON)}$  vs.  $V_{GS}$

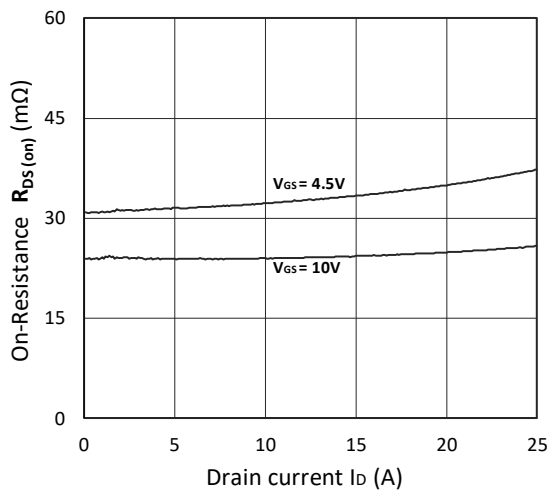


Figure 5.  $R_{DS(ON)}$  vs.  $I_D$

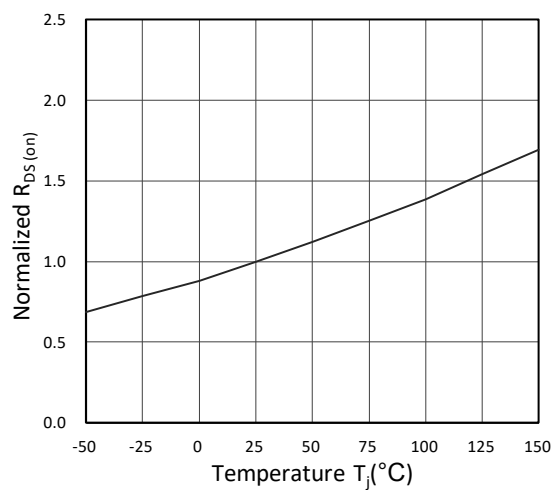


Figure 6. Normalized  $R_{DS(ON)}$  vs. Temperature

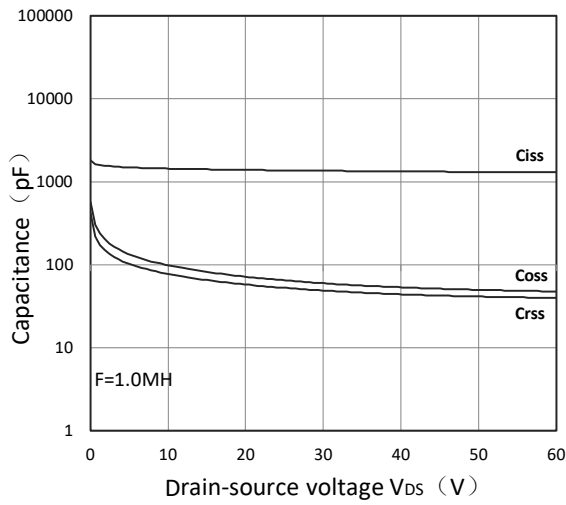


Figure 7. Capacitance Characteristics

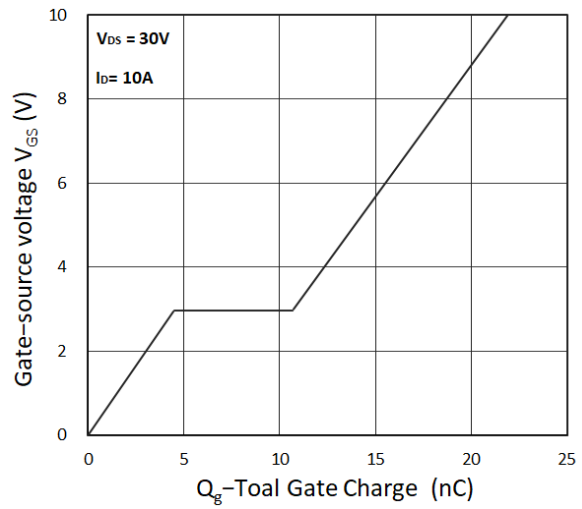


Figure 8. Gate Charge Characteristics

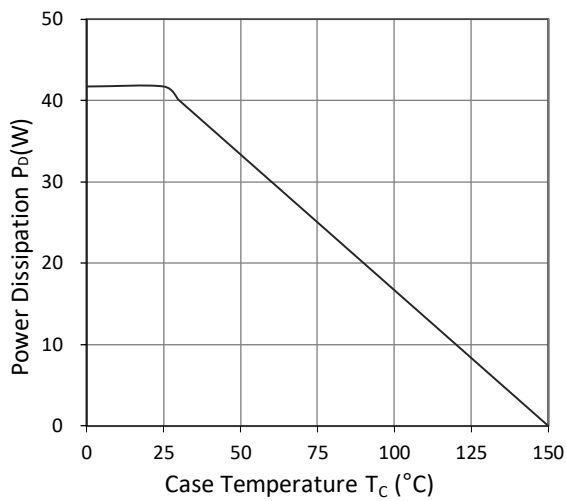


Figure 9. Power Dissipation

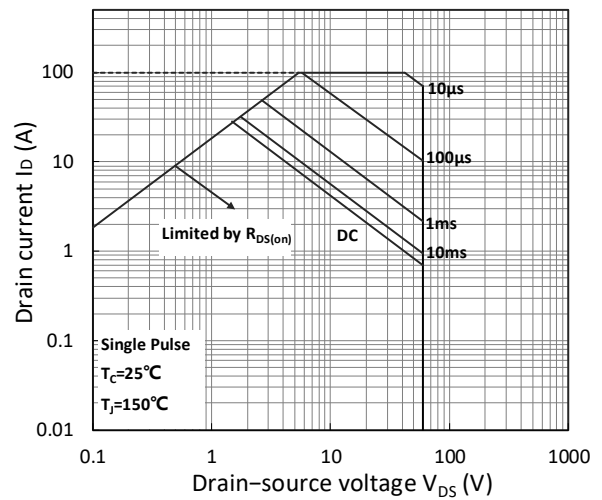


Figure 10. Safe Operating Area

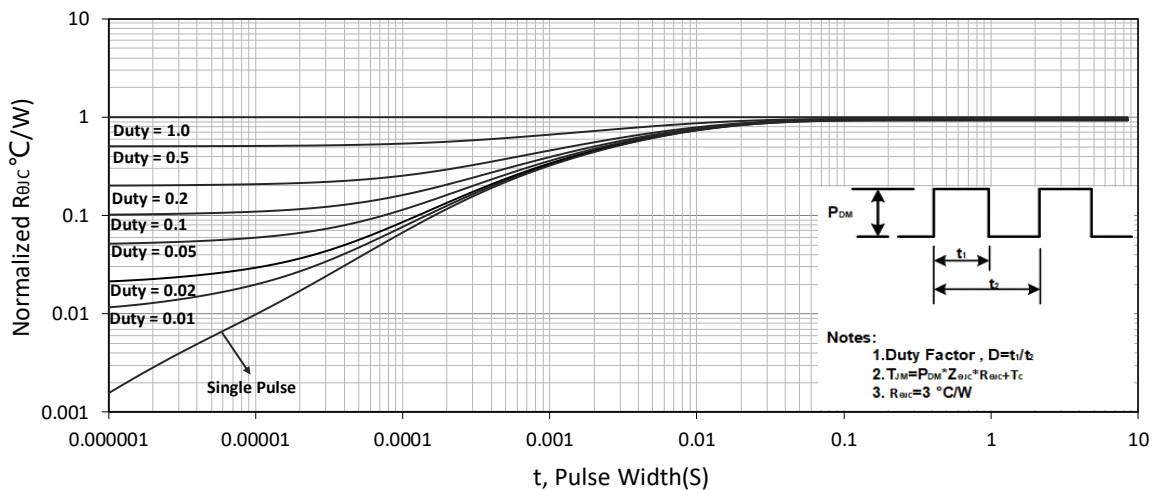


Figure 11. Normalized Maximum Transient Thermal Impedance





### Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.