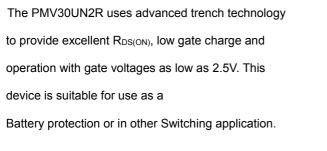


Description



General Features

V_{DS} = 20V I_D =6 A

 $R_{DS(ON)} < 27m\Omega @ V_{GS}=4.5V$

Application

Battery protection

Load switch Uninterruptible power supply

Package Marking and Ordering Information

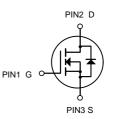
Product ID	Pack	Brand	Qty(PCS)
PMV30UN2R	SOT-23	HXY MOSFET	3000

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter		Limit	Unit
VDS	Drain-Source Voltage		20	V
V _{GS}	Gate-Source Voltage		±12	V
		T _A =25 ℃	6	
ID	Continuous Drain Current	T _A =70°C	3.6	A
Ырм	Drain Current-Pulsed (Note 1)		15	А
PD	Maximum Power Dissipation		1.25	W
Tj,Tstg	Operating Junction and Storage Temperature Range		-55 To 150	°C
Reja	Thermal Resistance, Junction-to-Ambient (Note 2)		100	°C /W







N-Channel MOSFET



Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	20	22.5	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =20V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	lgss	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	VGS(th)	V _{DS} =V _{GS} ,I _D =250µA	0.5	0.65	1.0	V
		V _{GS} =4.5V, I _D =4.0 A	-	22	27	mΩ
Drain-Source On-State Resistance	RDS(ON)	V _{GS} =2.5V, I _D =4.5A	-	28	40	mΩ
Forward Transconductance	gfs	V _{DS} =10V,I _D =4A	-	10	-	S
Input Capacitance	Clss		-	500	-	PF
Output Capacitance	Coss	V _{DS} =8V,V _{GS} =0V,	-	295	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	96	-	PF
Turn-on Delay Time	td(on)		-	11	-	nS
Turn-on Rise Time	tr	V _{DD} =10V,I _D =1A	-	30	-	nS
Turn-Off Delay Time	td(off)	V_{GS} =4.5V,R _{GEN} =6 Ω	-	35	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Qg		-	10	15	nC
Gate-Source Charge	Qgs	V _{DS} =10V,I _D =3A,V _{GS} =4.5V	-	2.3	-	nC
Gate-Drain Charge	Q _{gd}		-	2.9	-	nC
Diode Forward Voltage (Note 3)	Vsd	V _{GS} =0V,I _S =1A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	4.5	А

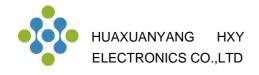
Notes:

1. Repetitive rating: pulse width limited by maximum junction temperature.

2. Surface mounted on FR4 Board, t \leq 10 sec.

3. Pulse test: pulse width \leq 300µs, duty cycle \leq 2%.

4. Guaranteed by design, not subject to production



Typical Electrical and Thermal Characteristics

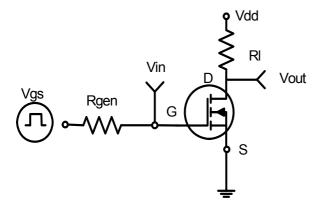


Figure 1:Switching Test Circuit

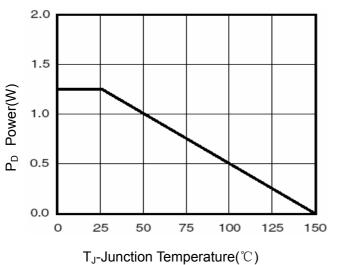
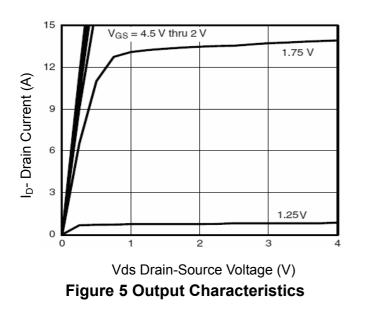


Figure 3 Power Dissipation



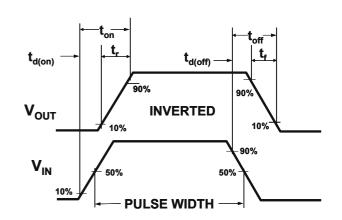
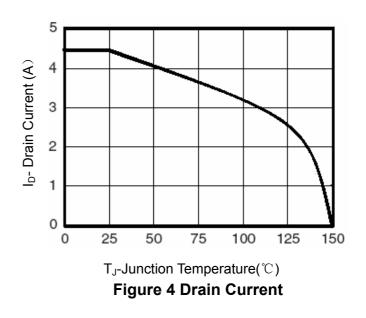
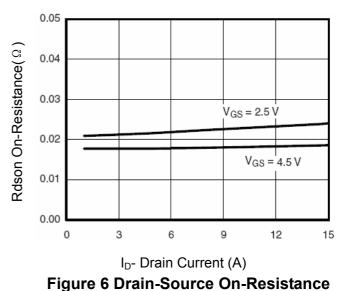
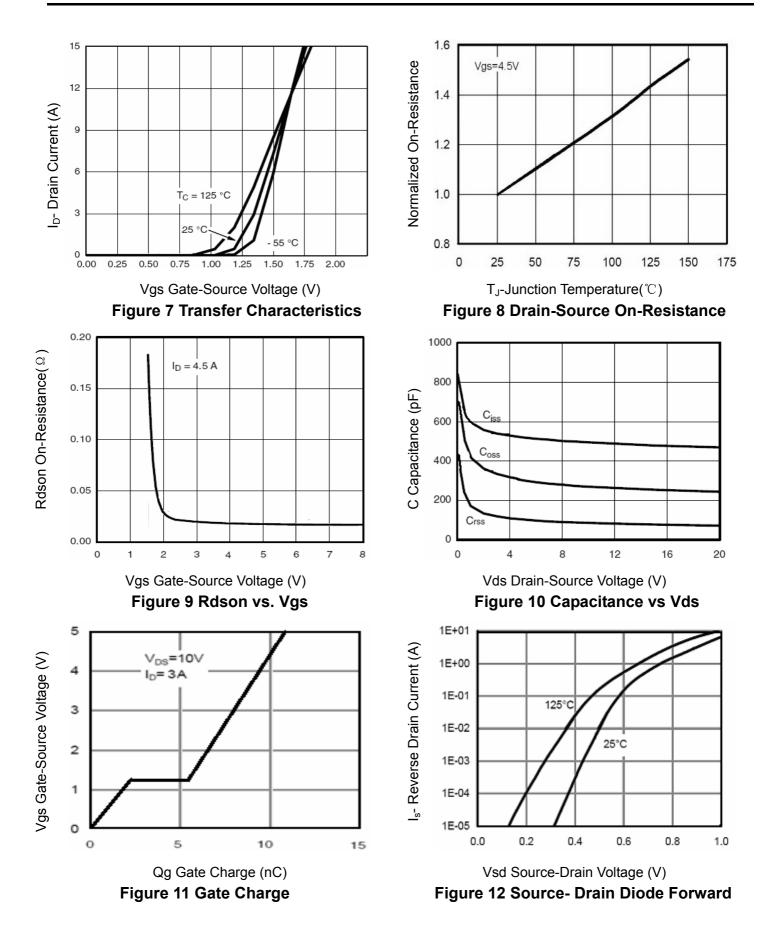


Figure 2:Switching Waveforms











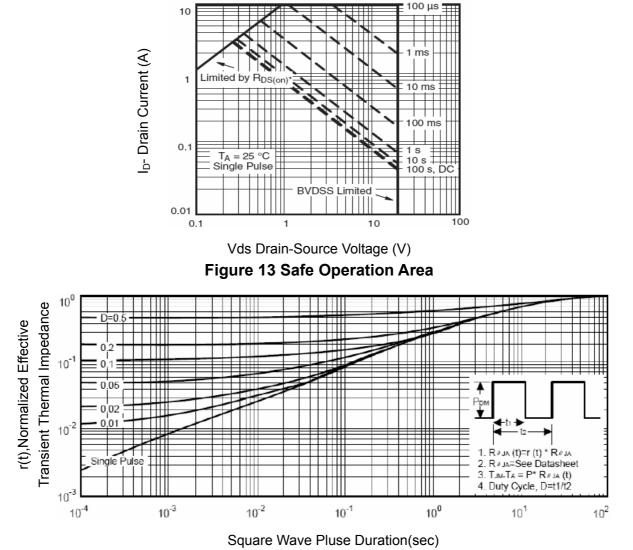
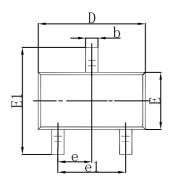
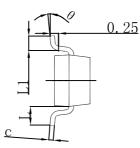


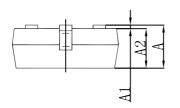
Figure 14 Normalized Maximum Transient Thermal Impedance



SOT-23 Package Outline Dimensions

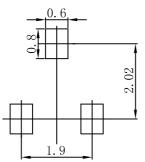






Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
Е	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
e	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

SOT-23 Suggested Pad Layout



Note: 1.Controlling dimension:in millimeters.

2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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