

Description

The FDMC8878 uses advanced trench

technology to provide excellent R_{DS(ON)}, low gate

charge and operation with gate voltages as low

as 4.5V. This device is suitable for use as a

Battery protection or in other Switching application.

General Features

 $V_{DS} = 30V I_{D} = 30 A$

 $R_{DS(ON)}$ < 13m Ω @ V_{GS} =10V

Application

Battery protection

Load switch

Uninterruptible power supply

Package Marking and Ordering Information

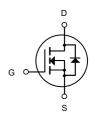
Product ID	Pack	Brand	Qty(PCS)
FDMC8878	DFN3X3-8L	HXY MOSFET	5000

Absolute Maximum Ratings (T_C=25 ℃ unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	30	V
VGS	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	30	А
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	18	Α
IDM	Pulsed Drain Current ²	55	А
EAS	Single Pulse Avalanche Energy ³	22.1	mJ
IAS	Avalanche Current	21	А
P _D @T _C =25°C	Total Power Dissipation ⁴	20	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
ReJA	Thermal Resistance Junction-ambient ¹	75	°C/W
R₀JC	Thermal Resistance Junction-Case ¹	6	°C/W



DFN3X3-8L



N-Channel MOSFET



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30			V
△BVpss/△TJ	BVDSS Temperature Coefficient	Reference to 25°C , I _D =1mA		0.022		V/°C
RDS(ON)	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =10A		8	13	
		V _{GS} =4.5V , I _D =5A		12	20	mΩ
V _G S(th)	Gate Threshold Voltage		1.0		2.5	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=250uA$		-5.1		mV/°C
loss	Dunin Course Leglery Courset	V _{DS} =24V , V _{GS} =0V , T _J =25°C			1	
	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =55°C			5	uA
Igss	Gate-Source Leakage Current	V_{GS} = $\pm 20V$, V_{DS} = $0V$			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =1A		4.5		S
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		2.5		Ω
Qg	Total Gate Charge (4.5V)	V _{DS} =20V , V _{GS} =4.5V , I _D =10A		7.2		nC
Qgs	Gate-Source Charge			1.4		
Qgd	Gate-Drain Charge			2.2		
T _{d(on)}	Turn-On Delay Time	-V _{DD} =12V , V _{GS} =10V , -R _G =3.3 -I _D =5A		4.1		
Tr	Rise Time			9.8		ns
T _d (off)	Turn-Off Delay Time			15.5		
T _f	Fall Time			6.0		
Ciss	Input Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz		572		
Coss	Output Capacitance			81		pF
Crss	Reverse Transfer Capacitance			65		
ls	Continuous Source Current ^{1,5}	\/ =\/ =0\/ Force Current			28	Α
Іѕм	Pulsed Source Current ^{2,5}	─V _G =V _D =0V , Force Current			55	Α
Vsp	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C			1.2	V

Note:

- 2.The data tested by pulsed , pulse width $\leq 300 us$, duty cycle $\leq 2\%$
- 3. The EAS data shows Max. rating . The test condition is V_{DD} =25V, V_{GS} =10V,L=0.1mH, I_{AS} =21A
- 4 .The power dissipation is limited by 150 $^{\circ}$ C junction temperature 5.The data is theoretically the same as I_{D} and I_{DM} , in real applications, should be limited by total power dissipation.

^{1.}The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.



Typical Characteristics

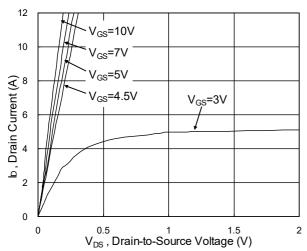


Fig.1 Typical Output Characteristics

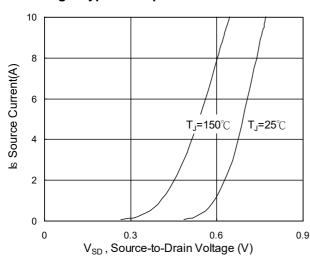


Fig.3 Forward Characteristics Of Reverse

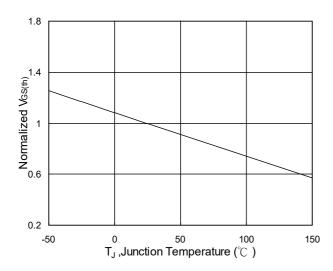


Fig.5 Normalized $V_{\text{GS(th)}}$ vs. T_J

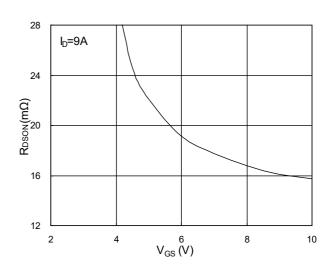


Fig.2 On-Resistance vs. Gate-Source

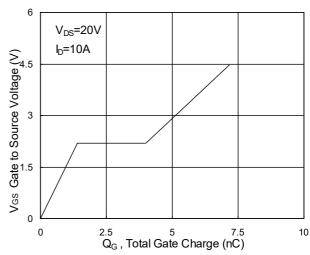


Fig.4 Gate-Charge Characteristics

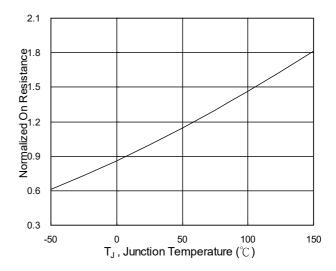
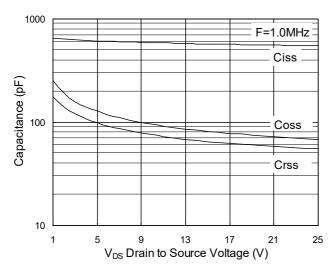


Fig.6 Normalized R_{DSON} vs T_J



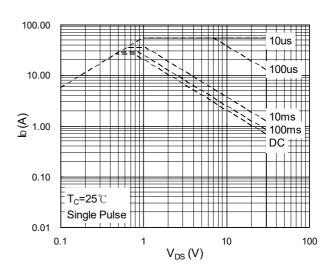


Fig.7 Capacitance

Fig.8 Safe Operating Area

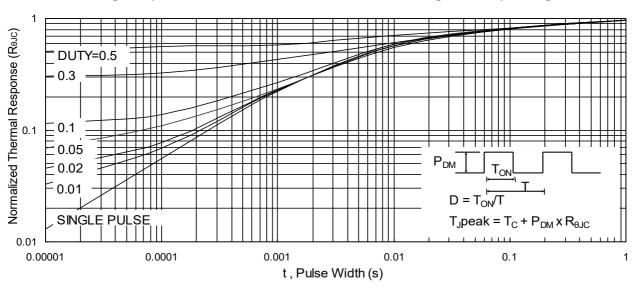


Fig.9 Normalized Maximum Transient Thermal Impedance

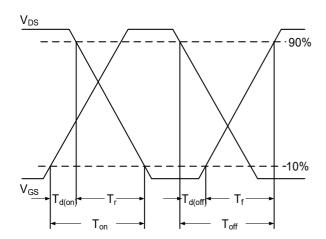


Fig.10 Switching Time Waveform

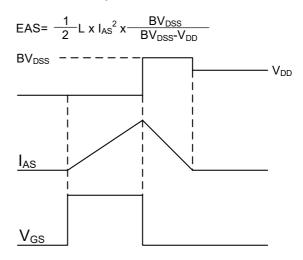
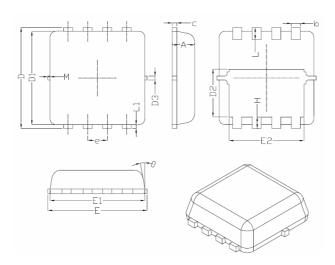


Fig.11 Unclamped Inductive Waveform



DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters			
	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
M	*	*	0.15	
θ		10 [°]	12 [°]	

N-Channel Enhancement Mode MOSFET

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