

Description

The RZR025P01TL uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

General Features

$$\begin{split} V_{DS} &= -20 V, I_D = -4.2 A \\ R_{DS(ON)} < 55 m \Omega @ V_{GS} = -4.5 V \\ R_{DS(ON)} < 75 m \Omega @ V_{GS} = -2.5 V \end{split}$$

Application

PWM applications Load switch Power management

Package Marking and Ordering Information

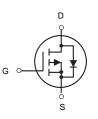
Product ID	Pack	Brand	Qty(PCS)
RZR025P01TL	SOT-23	HXY MOSFET	3000

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Limit	Unit
VDS	Drain-Source Voltage	-20	V
Vgs	Gate-Source Voltage	±12	V
ID	Drain Current-Continuous	-4.2	A
Ідм	Drain Current-Pulsed (Note 1)	-15	A
P₀	Maximum Power Dissipation	1.7	W
Тј,Тѕтб	Operating Junction and Storage Temperature Range	-55 To 150	°C
Reja	Thermal Resistance, Junction-to-Ambient (Note 2)	74	°C/W







P-Channel MOSFET



Electrical Characteristics (T_A=25[°]Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics			•				
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-20	-	-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V,V _{GS} =0V	-	-	-1	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)						•	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250µA	-0.45	-0.7	-1.0	V	
Drain October Or Otata Dagistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-4A	-	48	55		
Drain-Source On-State Resistance		V _{GS} =-2.5V, I _D =-3A	-	60	75	mΩ	
Forward Transconductance	g fs	V _{DS} =-5V,I _D =-4.2A	-	6	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C _{lss}		-	740	-	PF	
Output Capacitance	C _{oss}	V_{DS} =-4V, V_{GS} =0V, F=1.0MHz	-	290	-	PF	
Reverse Transfer Capacitance	C _{rss}		-	190	-	PF	
Switching Characteristics (Note 4)			•				
Turn-on Delay Time	t _{d(on)}		-	12	-	nS	
Turn-on Rise Time	tr	V _{DD} =-4V, ,R _L =-1.2Ω,	-	35	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GEN} =-4.5V,Rg=1 Ω	-	30	-	nS	
Turn-Off Fall Time	t _f		-	10	-	nS	
Total Gate Charge	Qg		-	7.8	-	nC	
Gate-Source Charge	Q _{gs}	V _{DS} =-4V,I _D =-4.1A,V _{GS} =-4.5V	-	1.2	-	nC	
Gate-Drain Charge	Q _{gd}	1	-	1.6	-	nC	
Drain-Source Diode Characteristics	·						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-4.1A	-	-	-1.2	V	
Diode Forward Current (Note 2)	Is		-	-	-4.1	Α	

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production



Typical Electrical and Thermal Characteristics

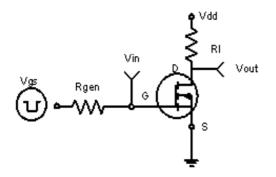
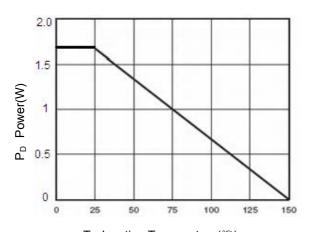
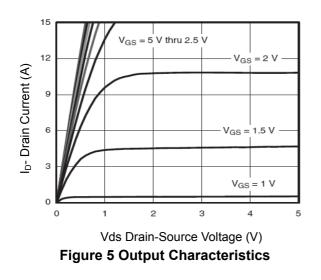


Figure 1:Switching Test Circuit



T_J-Junction Temperature(℃) **Figure 3 Power Dissipation**



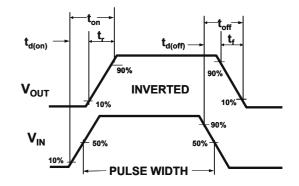
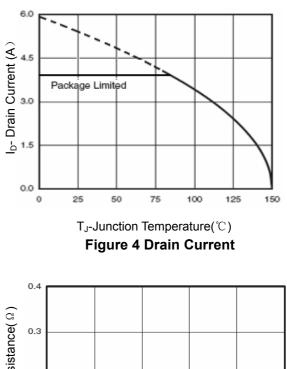


Figure 2:Switching Waveforms



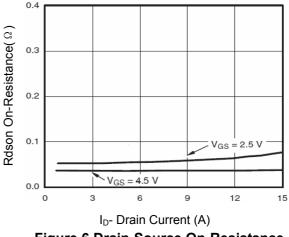
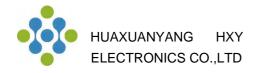
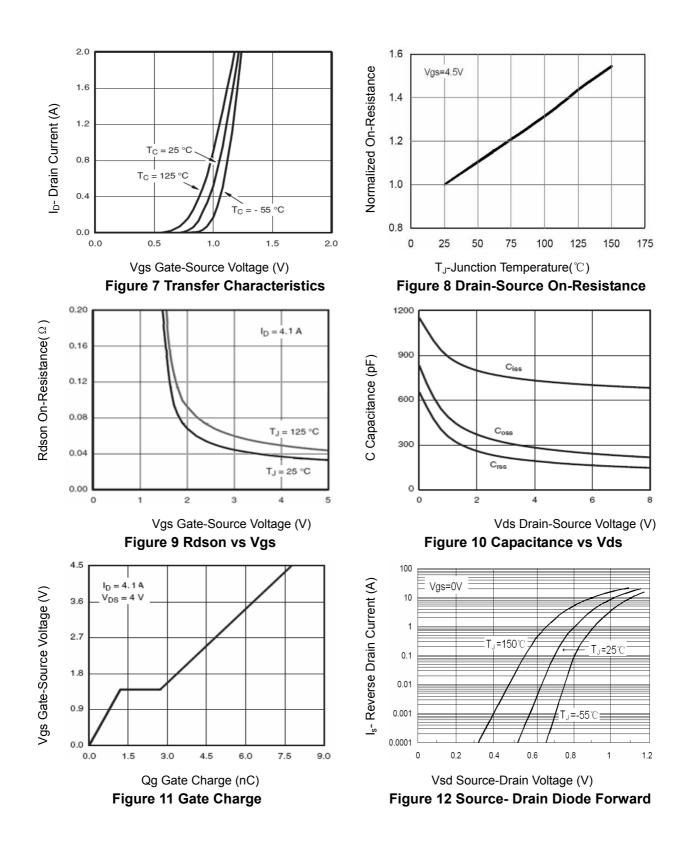
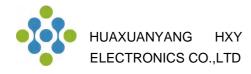


Figure 6 Drain-Source On-Resistance







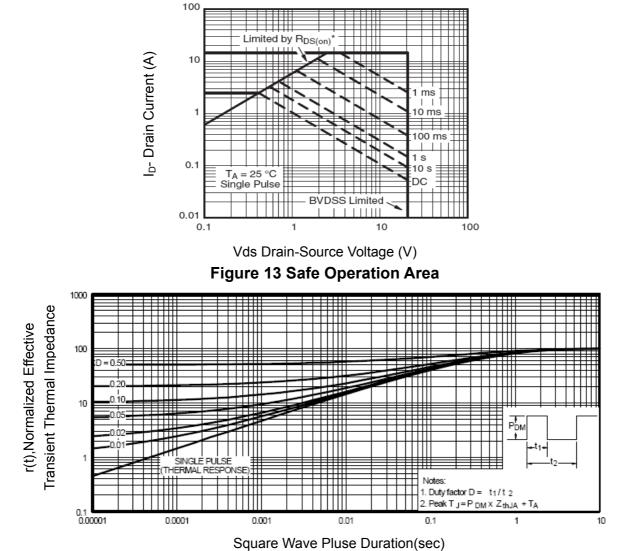
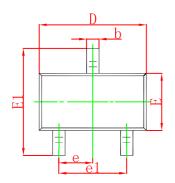
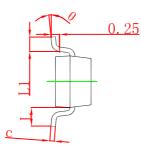


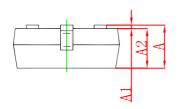
Figure 14 Normalized Maximum Transient Thermal Impedance



SOT-23 Package Outline Dimensions

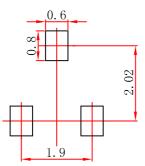






Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
Е	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
e	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

SOT-23 Suggested Pad Layout



Note: 1.Controlling dimension:in millimeters.

2.General tolerance:± 0.05mm.
 3.The pad layout is for reference purposes only.



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