

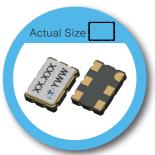
VT Type 7.0 x 5.0 mm SMD Voltage Controlled Crystal Oscillator

FEATURE

- Typical 7.0 x 5.0 x 1.75 mm 6 pads ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- Tri-state enable/disable

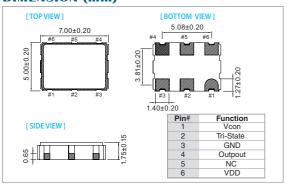
TYPICAL APPLICATION

- Set-top Box, HDTV
- WiMAX/WLAN
- xDSL/ VoIP, Cable modem

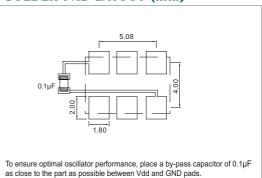


RoHS Compliant

DIMENSION (mm)



SOLDER PAD LAYOUT (mm)



ELECTRICAL SPECIFICATION

Parameter	3.3 V		Unit	
	Min.	Max.	Ullit	
Supply Voltage Variation (VDD)	VDD-5%	VDD+5%	V	
Frequency Range	1.5	170	MHz	
Standard Frequency	10,20,25,27,32.768,35.328,38.88,61.44,122.88, 153.6			
Absolute Pulling Range (APR)	±50	_	ppm	
Control Voltage Range	0.3	3.0	V	
Supply Current 1.5 MHz ≤ Fo < 20 MHz	_	10		
20 MHz ≤ Fo < 50 MHz	_	20	mA	
50 MHz ≤ Fo ≤ 80 MHz	_	30		
80 MHz < Fo < 160 MHz	_	40		
160 MHz ≤ Fo ≤ 170 MHz	_	50		
Output Level (CMOS)				
Output High (Logic "1")	2.97		V	
Output Low (Logic "0")	_	0.33		
Transition Time: Rise/Fall Time+				
1.5 MHz ≦ Fo < 20 MHz 20 MHz ≦ Fo < 50 MHz		5	nSec	
20 MHZ ≦ FO < 50 MHZ 50 MHZ ≤ Fo ≤ 80 MHZ		4		
80 MHz < Fo ≤ 170 MHz		3		
Start Time	_	<u>2</u> 5	_	
Tri-State(Input to Pin 2)		5	mSec	
Enable (High voltage or floating)	2.31	_		
Disable (Low voltage or GND)		0.99	- v	
Period Jitter (Pk-Pk)		40	pSec	
RMS Phase Jitter (Integrated 12kHz~20MHz)		1	pSec	
Linearity	_	10	%	
Modulation Bandwidth (BW)		10	/0	
1.5 MHz ≤ Fo ≤ 170 MHz	15	_		
Input Impedance			kHz	
1.5 MHz ≦ Fo ≦ 170 MHz	10000	_	kΩ	
	-1	15	KSZ	
T Hase Noise@30.72MHz				
1 kHz	-135		dBc/Hz	
10 kHz	-150			
Aging (@ 25°C 1st year)	<u>-</u>	±3	ppm	
Storage Temp. Range	-55	125	°C	

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	±25	±50
-10 ~ +60	0	0
-20 ~ +70	0	0
-40 ~ +85	\triangle	0

^{*} \bigcirc : Available \triangle :Conditional X: Not available

Note: not all combination of options are available. Other specifications may be available upon request.

⁺ Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.

 $^{^*}$ Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration