

■ PRODUCT CHARACTERISTICS

VDSS	500V
R _{DS(on)} max(@V _{GS} =10 V)	0.2Ω
ID	28A

■ APPLICATIONS

- * High efficiency switch mode power supplies
- * Electronic lamp ballasts based on half bridge
- * LED power supplies

■ FEATURES

- * R_{DS(ON)} ≤ 0.2 Ω @ V_{GS}=10V
- * High Switching Speed
- * Improved dv/dt capability

■ ORDER INFORMATION

Order codes		Package	Packing
Halogen-Free	Halogen	TO-220F	50 pieces/Tube

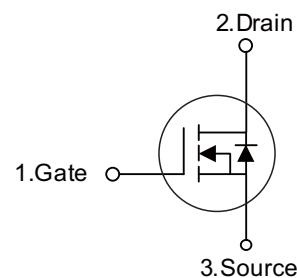
■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Units
Drain-source voltage	V _{DSS}	500	V
Gate-source voltage	V _{GSS}	±30	V
Drain current continuous	I _D	28	A
Drain current pulsed (note1)	I _{DP}	112	A
Avalanche energy	Repetitive E _{AR}	43	mJ
	Single pulse E _{AS}	1960	mJ
Peak diode recovery dv/dt (note 3)	dv/dt	4.5	V/ns
Total power dissipation	T _C =25°C	479	W
	derate above 25°C	3.83	W/°C
Junction temperature	T _J	+150	°C
Storage temperature	T _{STG}	-55~+150	°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	62.5	°C/W
Junction to Case	θ _{JC}	0.625	°C/W

Symbol



TO-220F

■ ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Off characteristics						
Drain-source breakdown voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	500	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}}=500\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
		$V_{\text{DS}}=400\text{V}, T_c=125^\circ\text{C}$	-	-	10	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}}=30\text{V}, V_{\text{DS}}=0\text{V}$	-	-	100	nA
		$V_{\text{GS}}=-30\text{V}, V_{\text{DS}}=0\text{V}$	-	-	-100	nA
On characteristics						
Gate threshold voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.0	-	4.0	V
Static drain-source on-resistance	$R_{\text{DS(on)}}$	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=14\text{A}$	-	0.16	0.2	Ω
Forward transconductance	g_{FS}	$V_{\text{DS}}=40\text{V}, I_{\text{D}}=14\text{A}$ (note4)	-	26	-	S
Dynamic characteristics						
Input capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	4085	-	pF
Output capacitance	C_{oss}		-	474	-	pF
Reverse transfer capacitance	C_{rss}		-	60	-	pF
Switching characteristics						
Turn-on delay time	$t_{\text{d(on)}}$	$V_{\text{DD}}=250\text{V}, I_{\text{D}}=28\text{A}, R_{\text{G}}=25\Omega$ (note4,5)	-	45	-	ns
Rise time	t_r		-	87	-	ns
Turn-off delay time	$t_{\text{d(off)}}$		-	355	-	ns
Fall time	t_f		-	130	-	ns
Total gate charge	Q_g	$V_{\text{DS}}=400\text{V}, I_{\text{D}}=28\text{A}, V_{\text{GS}}=10\text{V}$ (note4,5)	-	102	-	nC
Gate-source charge	Q_{gs}		-	43	-	nC
Gate-drain charge	Q_{gd}		-	20	-	nC
Drain-source diode characteristics						
Drain-source diode forward voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=28\text{A}$	-	-	1.4	V
Continuous drain-source current	I_{SD}		-	-	28	A
Pulsed drain-source current	I_{SM}		-	-	112	A
Reverse recovery time	t_{rr}	$I_{\text{SD}}=28\text{A}$ $dI_{\text{SD}}/dt=100\text{A}/\mu\text{s}$ (note4)	-	656	-	ns
Reverse recovery charge	Q_{rr}		-	11.5	-	μC

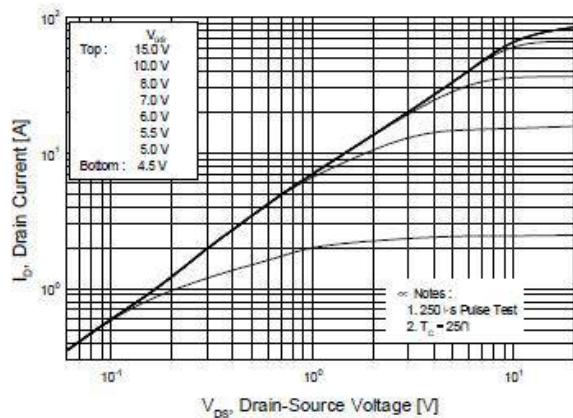
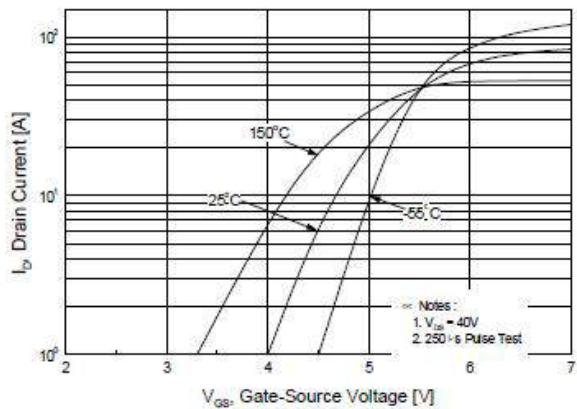
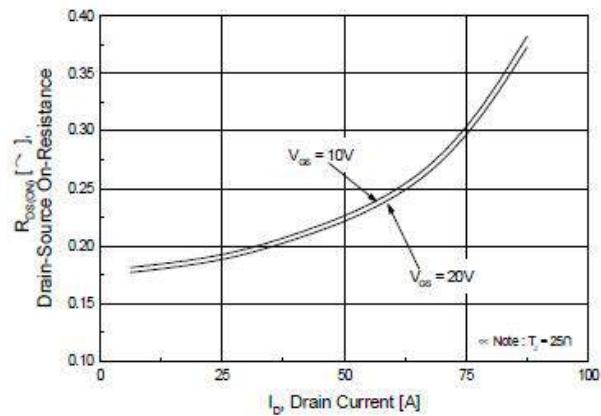
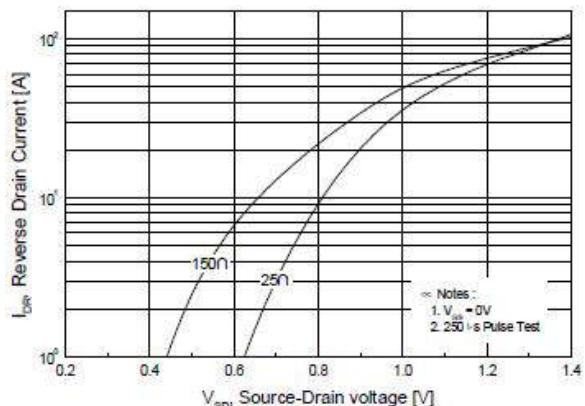
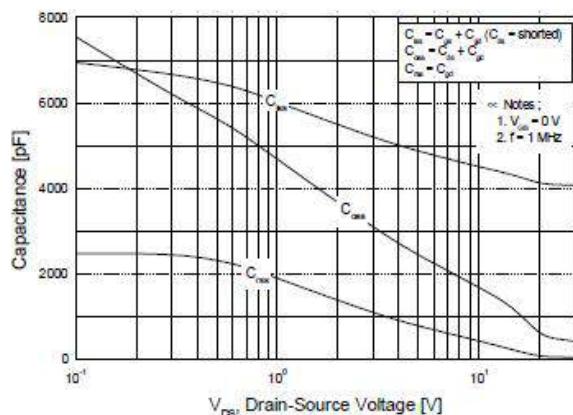
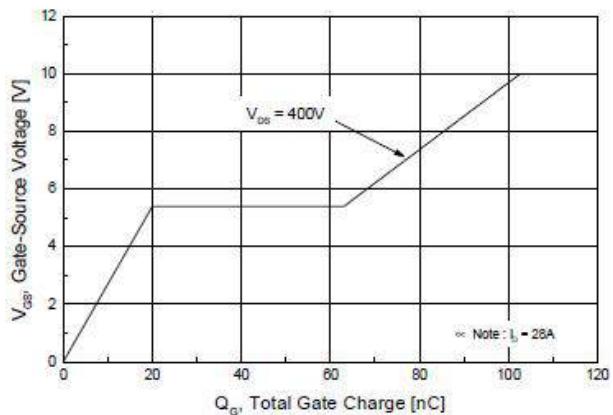
Note:1 Repetitive rating: pulse width limited by maximum junction temperature

2. $L=5\text{mH}, I_{\text{AS}}=28\text{A}, V_{\text{DD}}=50\text{V}, R_{\text{G}}=25\Omega$, starting $T_J=25^\circ\text{C}$

3. $I_{\text{SD}} \leq 28\text{A}, dI/dt \leq 100\text{A}/\mu\text{s}, V_{\text{DD}} \leq \text{BV}_{\text{DSS}}$, starting $T_J=25^\circ\text{C}$

4. Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$

5. Essentially independent of operating temperature

■ ELECTRICAL CHARACTERISTICS DIAGRAMS

Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics

Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

Figure 4. Body Diode Forward Voltage
Variation with Source Current
and Temperature

Figure 5. Capacitance Characteristics

Figure 6. Gate Charge Characteristics

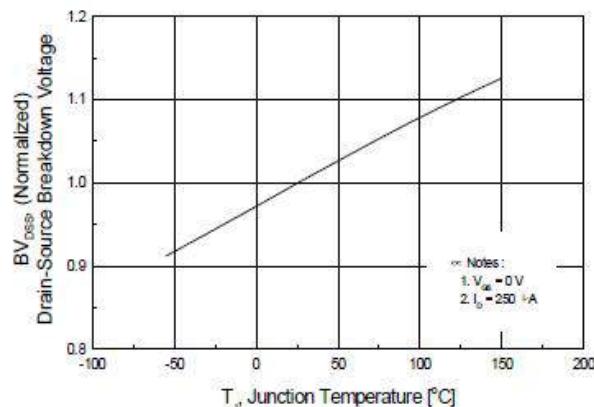
■ ELECTRICAL CHARACTERISTICS DIAGRAMS(Cont.)


Figure 7. Breakdown Voltage Variation
vs Temperature

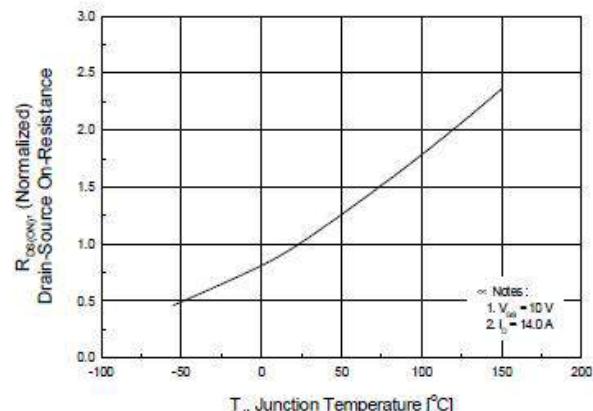


Figure 8. On-Resistance Variation
vs Temperature

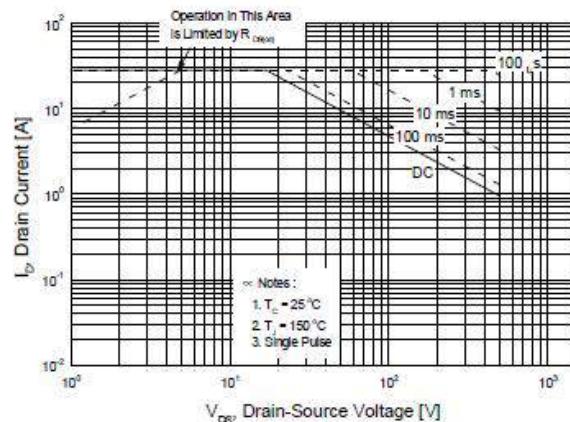


Figure 9. Maximum Safe Operating Area

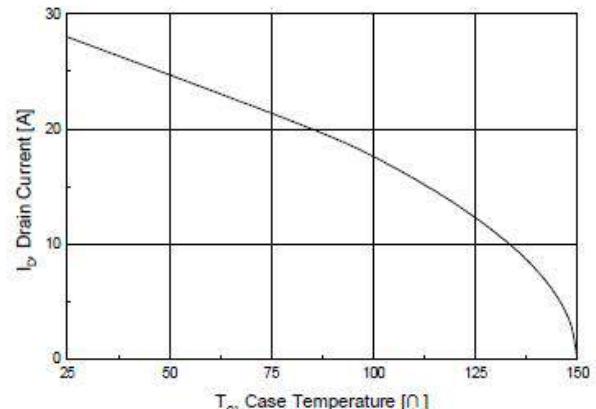


Figure 10. Maximum Drain Current
vs Case Temperature

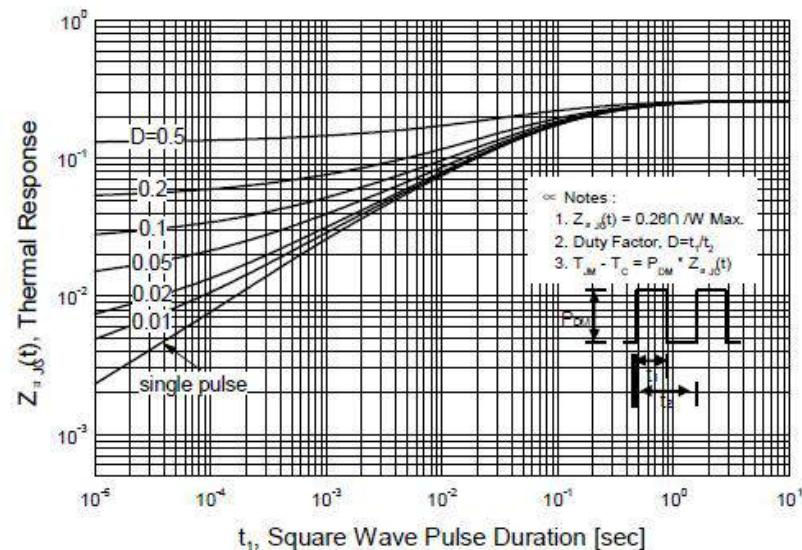


Figure 11. Transient Thermal Response Curve

■ TO-220F-3L PACKAGE OUTLINE DIMENSIONS

