

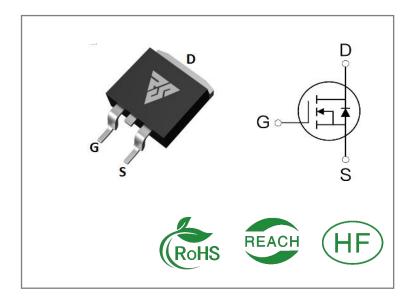
ID	R _{DS} (ON)(Typ)	VDSS
190A	3mΩ	100V

Applications:

- Load Switch
- PWM Applications
- Power Managment

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability



Ordering Information

Part Number	Package	Marking	Packing	Qty.
RS100N190S	T0-263	RS100N190S	Tape&reel	800 PCS

Absolute Maximun Ratings Tc= 25℃ unless otherwise specified

Symbol	Parameter	RS100N190S	Units
VDSS	Drain-to-Source Voltage	100	V
ID	Continuous Drain Current TC=25℃	190	
ID	Continuous Drain Current TC=100℃	130	Α
IDM	Pulsed Drain Current	680	
PD	Power Dissipation	310	W
VGS	Gate- to- Source Voltage	±20	V
EAS	Single Pulse Avalanche Engergy L = 0.3mH,IS =45A, RG = 25Ω , Tj = 25° C	725	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	${\mathbb C}$
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

^{*} Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.



Thermal Resistance

Symbol	Parameter	RS100N190S	Units	Test Conditions
RθJC	Junction-to-Case	0.40	°C/W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^{\circ}$ C
RθJA	Junction-to- Ambient	45		1 cubic foot chamber,free air.

OFF Characteristics TJ= 25 °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	100			V	VGS=0V,ID=250μ A
IDSS	Drain- to- Source Leakage Current			1	μΑ	VDS=80V,VGS=0 V
IGSS	Gate- to- Source Forward Leakage			100	nA	VGS=20V ,VDS=0 V
1033	Gate- to- Source Reverse Leakage	100		IIA	VGS=-20V ,VDS= 0V	

ON Characteristics TJ=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance		3.0	3.6	mΩ	VGS=10V,ID=20A
VGS(TH)	Gate Threshold Voltage	2.0	1	4.0	٧	VGS=VDS,ID=25 0μA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		21			\/DC
trise	Rise Time		35		C	VDS=50V RL=2.5Ω
td(OFF)	Turn- OFF Delay Time		50		nS	RG=3Ω VGS=10V
tfall	Fall Time		30			AG2-10A



Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		4790			VGS= 0V
Coss	Output Capacitance		900		рF	VDS=50V
Crss	Reverse Transfer Capacitance		18			f=1MHz
Qg	Total Gate Charge		83			VDS= 50V
Qgs	Gate- to- Source Charge		24		nC	ID=20A
Qgd	Gate-to-Drain(" Miller") Charge		26			VGS=10V

Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			190	Α	Integral pn- diode
ISM	Maximum Pulsed Current			680	Α	in MOSFET
VSD	Diode Forward Voltage			1.0	V	IS=1A,VGS=0V
trr	Reverse Recovery Time		70		nS	VGS=0V
Qrr	Reverse Recovery Charge		125		nC	IS=20A di/dt=100A/μs

Notes:

- * 1. Repetitive rating, pulse width limited by maximum junction temperature.
- * 2. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

Typical Feature Curve

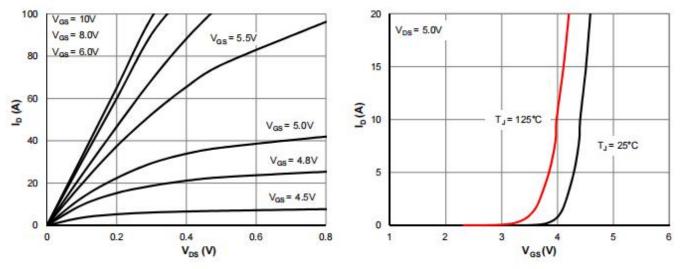


Figure 1: Saturation Characteristics

Figure 2: Transfer Characteristics



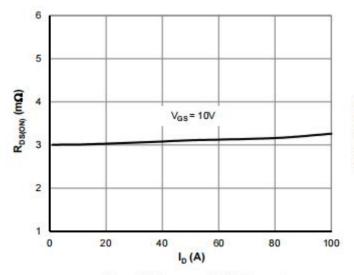


Figure 3: RDS(ON) vs. Drain Current

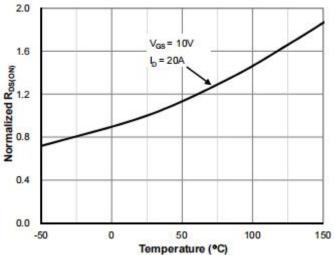


Figure 4: RDS(ON) vs. Junction Temperature

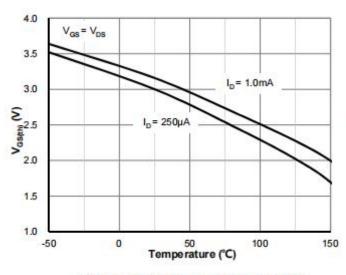


Figure 5: VGS(th) vs. Junction Temperature

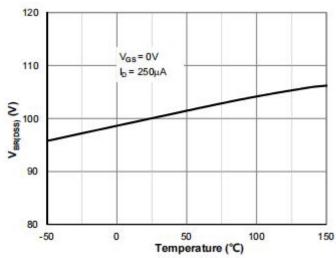


Figure 6: VBR(DSS) vs. Junction Temperature

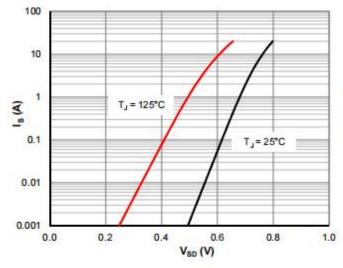


Figure 7: Body-Diode Characteristics

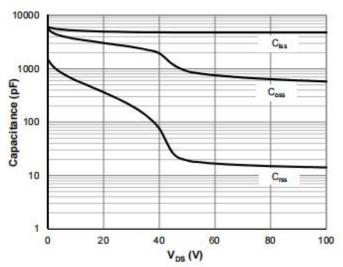


Figure 8: Capacitance Characteristics



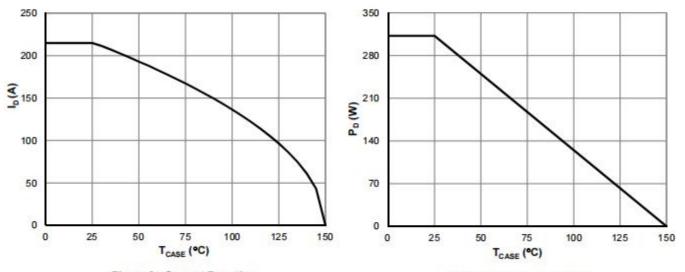


Figure 9: Current De-rating

Figure 10: Power De-rating

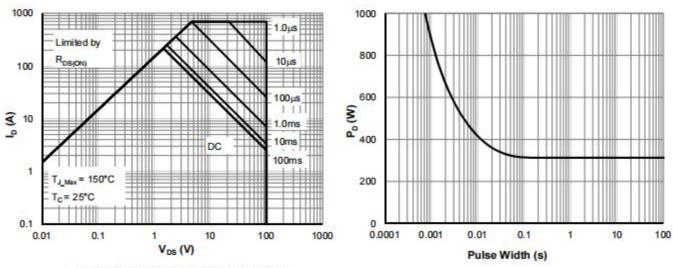


Figure 11: Maximum Safe Operating Area

Figure 12: Single Pulse Power Rating, Junction-to-Case

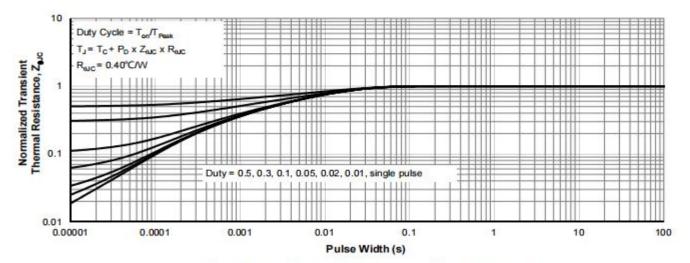


Figure 13: Normalized Maximum Transient Thermal Impedance



Test ircuits and Waveforms

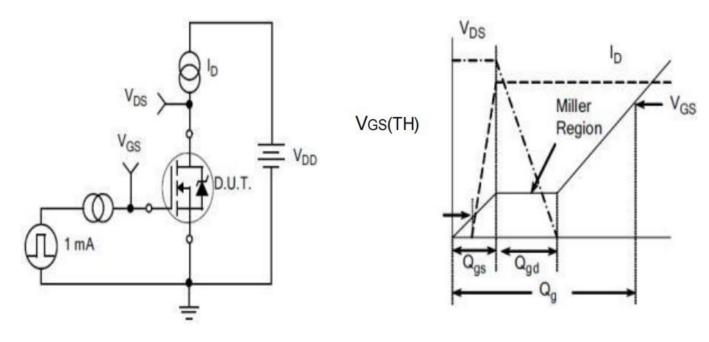


Figure A. Gate Charge Test Circuit

VDS - V_{GS} E VDD RG D.U.T. 10% VGS td(ON) trise

Figure C. Resistive Switching Test Circuit

Figure B. Gate Charge Waveform

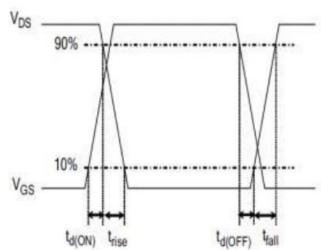
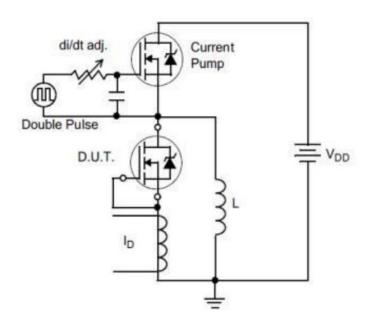


Figure D. Resistive Switching Waveforms



Test ircuits and Waveforms



 $di/dt = 100A/\mu A$ Q_{rr}

Figure E.Diode Reverse Recovery Test Circuit

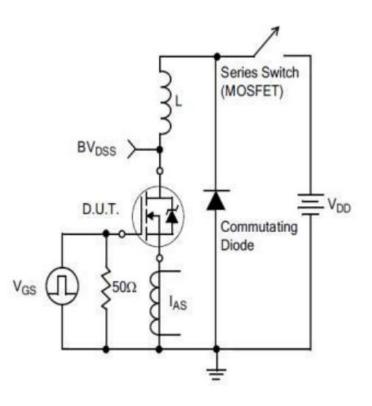


Figure F.Diode Reverse Recovery Waveform

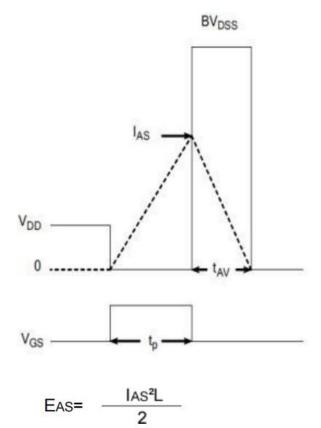


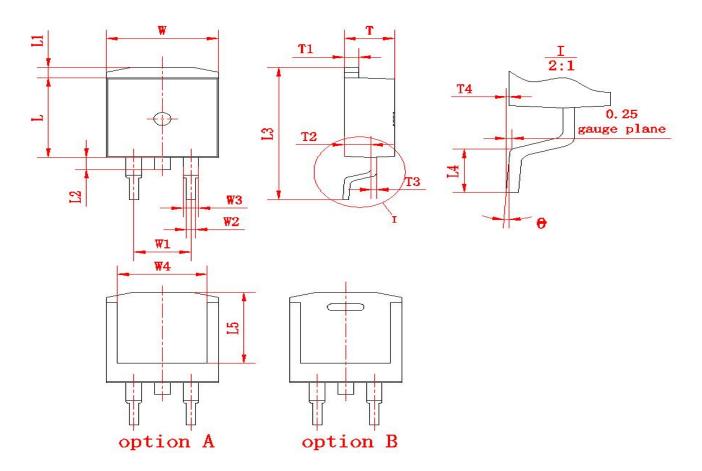
Figure G.Unclamped Inductive Switching Test Circuit

Figure H.Unclamped Inductive Switching Waveforms

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Package outline drawing(TO-263 Unit: mm)



(单位: mm)

符号	尺寸		かロ	尺	·寸	77 D	尺寸	
	Min	Max	符号	Min	Max	符号	Min	Max
W	9.80	10. 20	L1	1.00	1.40	T1	1. 20	1. 40
W1	(5.	08)	L2	1. 20	1.60	T2	2. 20	2. 60
W2	0. 70	0. 95	L3	15. 00	15. 60	Т3	0. 45	0. 65
W3	1. 17	1. 62	L4	2. 20	2. 80	T4	0	0. 25
W4	(8	. 0)	L5	(8. 2)		θ	0°	8°
L	9.00	9. 40	Т	4. 30	4. 70			



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