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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK1934

Silicon N Channel MOS FET

REJ03G0985-0200

(Previous: ADE-208-1333)

Rev.2.00 Sep 07, 2005

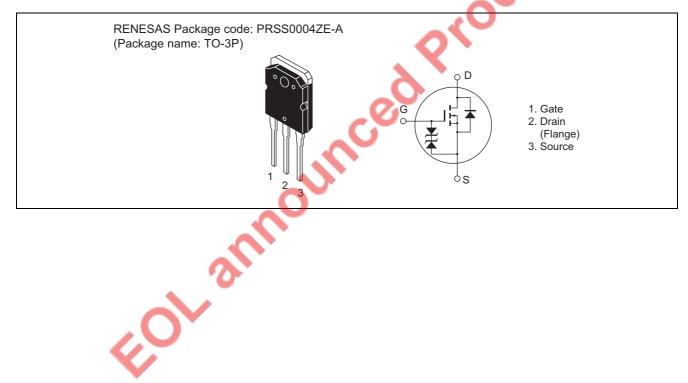
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- No secondary breakdown
- Suitable for switching regulator

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	1000	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	8	А
Drain peak current	I _{D(pulse)} *1	24	А
Body to drain diode reverse drain current	I _{DR}	8	А
Channel dissipation	Pch* ²	150	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1 %

2. Value at Tc = 25°C

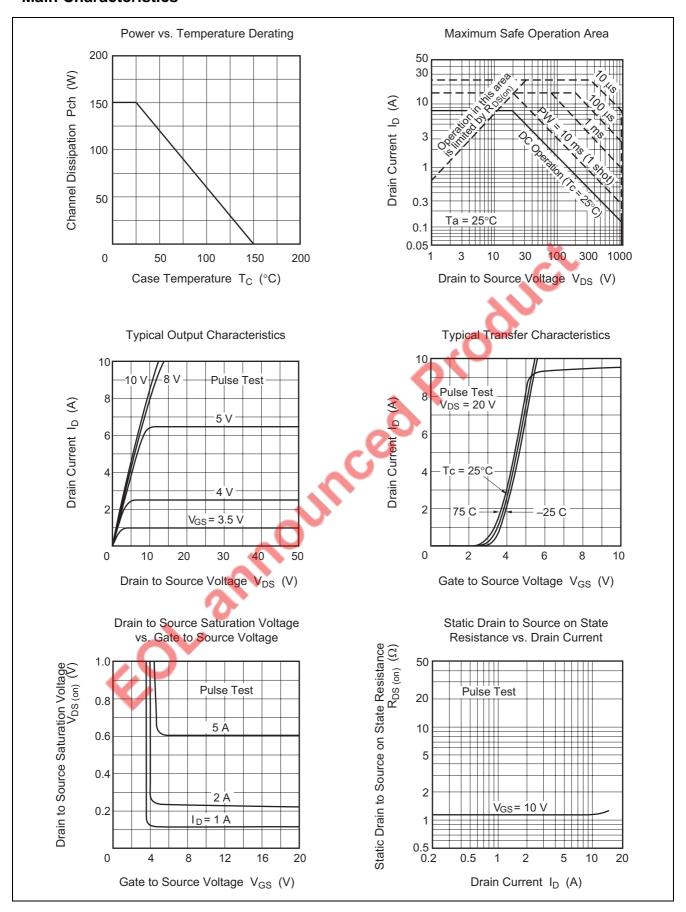
Electrical Characteristics

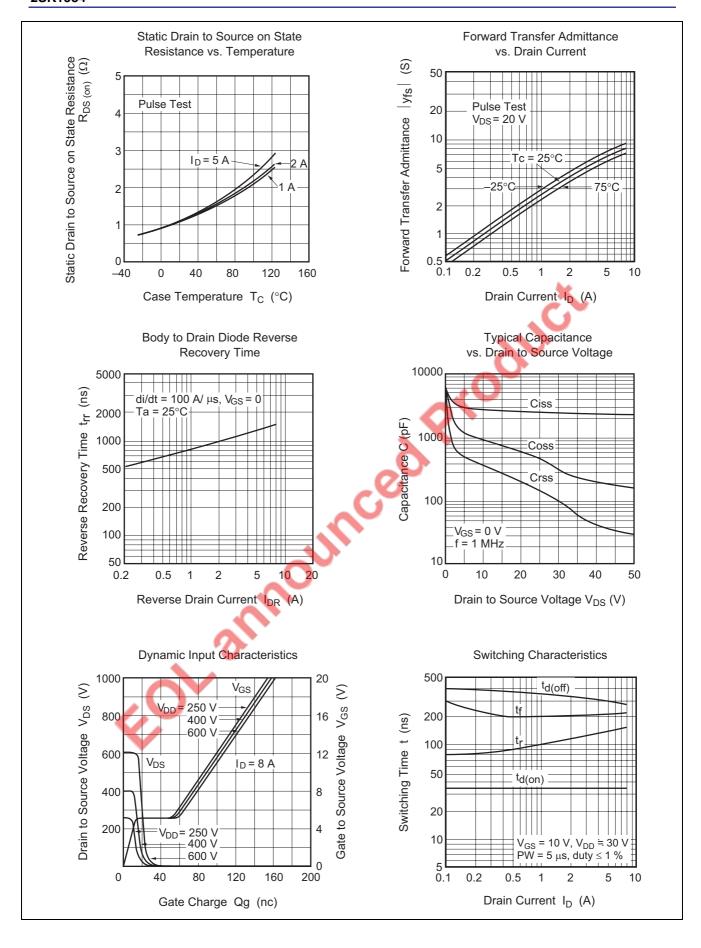
 $(Ta = 25^{\circ}C)$

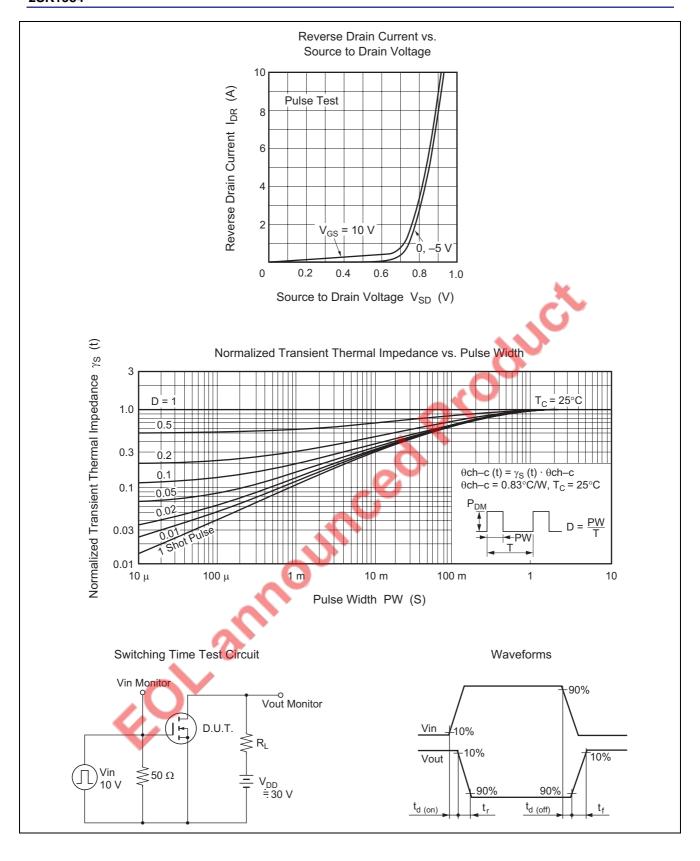
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	1000	_	_	V C	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	_	_	V	$I_G = \pm 100 \mu\text{A}, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	250	μΑ	V _{DS} = 800 V, V _{GS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	_	3.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R _{DS(on)}	_	1.2	1.6	Ω	$I_D = 4 \text{ A}, V_{GS} = 10 \text{ V}^{*3}$
Forward transfer admittance	y _{fs}	4	6) —	S	$I_D = 4 \text{ A}, V_{DS} = 20 \text{ V}^{*3}$
Input capacitance	Ciss	_	2690	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	- 4	920	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	-	375	_	pF	
Turn-on delay time	t _{d(on)}	7	35	_	ns	$I_D = 4 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time	tr	O-	135	_	ns	$R_L = 7.5 \Omega$
Turn-off delay time	t _{d(off)}	_	300	_	ns	
Fall time	ti	_	205	_	ns	
Body to drain diode forward voltage	V_{DF}	_	0.9	_	V	I _F = 8 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	_	1600	_	μs	$I_F = 8 \text{ A}, V_{GS} = 0,$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Note: 3. Pulse Test

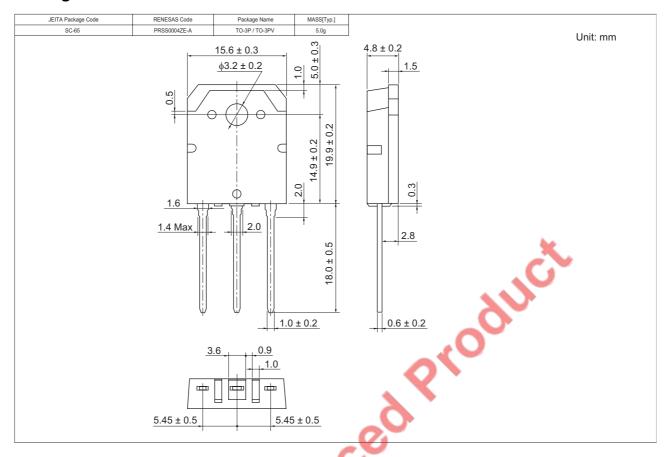
Main Characteristics







Package Dimensions



Ordering Information

Part Name	Quantity	3	Shipping Container
2SK1934-E	360 pcs		Box (Tube)

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Renesas Technology Europe Limited
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Renesas Technology Korea Co., Ltd.Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> 2-796-3115, Fax: <82> 2-796-2145

Renesas Technology Malaysia Sdn. Bhd. Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510