

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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EOL announced Product

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## 2SK1620(L), 2SK1620(S)

Silicon N Channel MOS FET

REJ03G0957-0200  
(Previous: ADE-208-1298)  
Rev.2.00  
Sep 07, 2005

### Application

High speed power switching

### Features

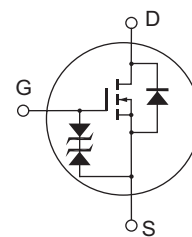
- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and motor driver

### Outline

RENESAS Package code: PRSS0004AE-A  
(Package name: LDKPAK(L))



RENESAS Package code: PRSS0004AE-B  
(Package name: LDKPAK(S)-(1))



1. Gate
2. Drain
3. Source
4. Drain

## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	150	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	10	A
Drain peak current	I <sub>D(pulse)</sub> <sup>*1</sup>	40	A
Body to drain diode reverse drain current	I <sub>DR</sub>	10	A
Channel dissipation	P <sub>ch</sub> <sup>*2</sup>	50	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%  
 2. Value at T<sub>C</sub> = 25°C

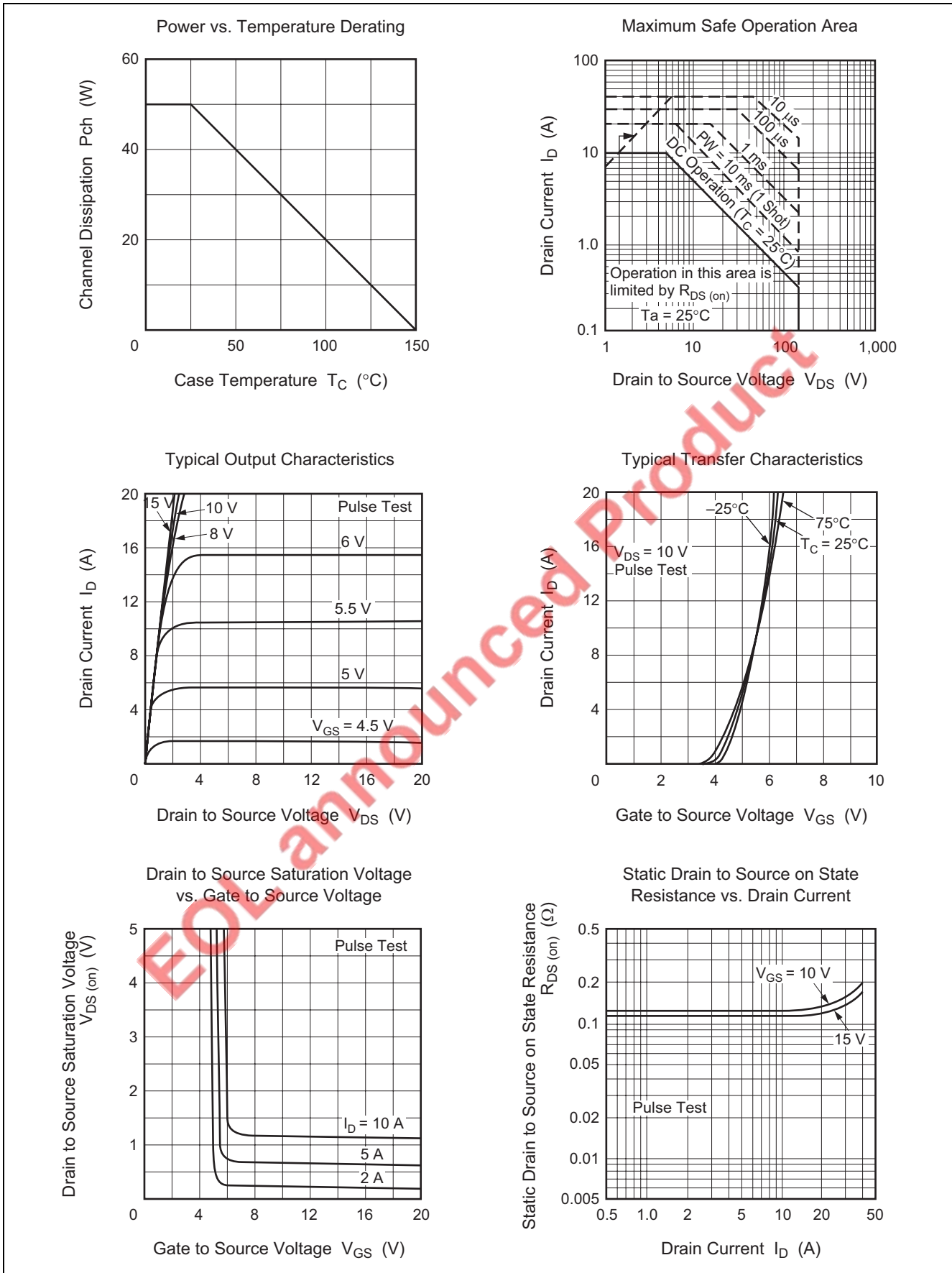
## Electrical Characteristics

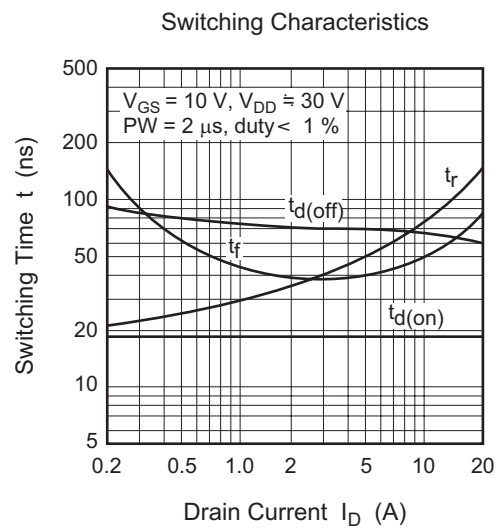
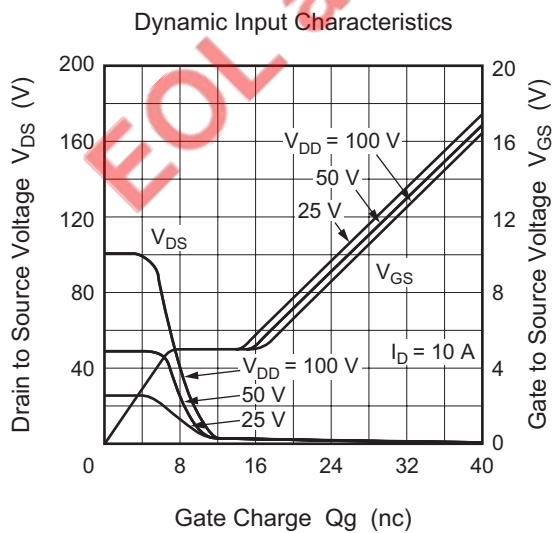
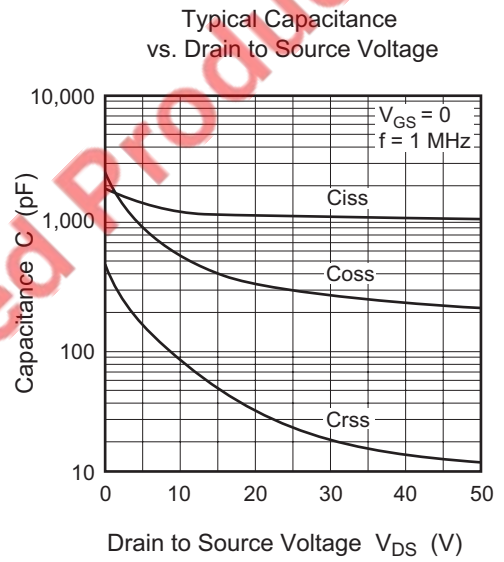
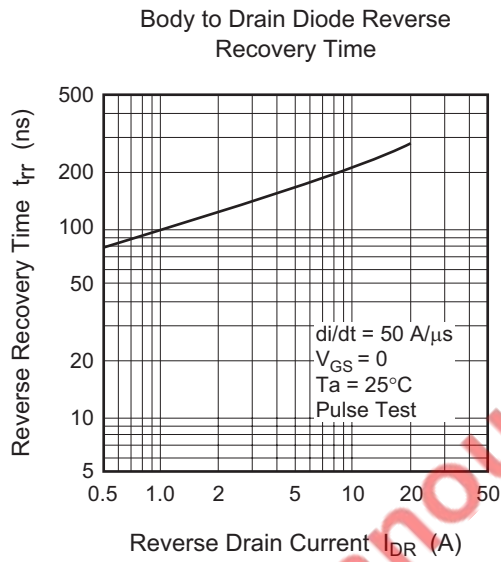
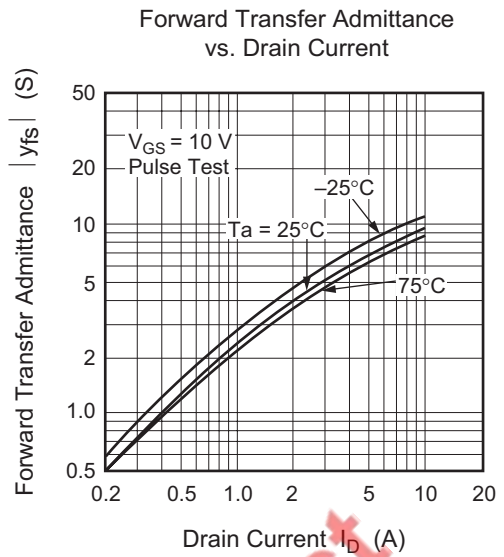
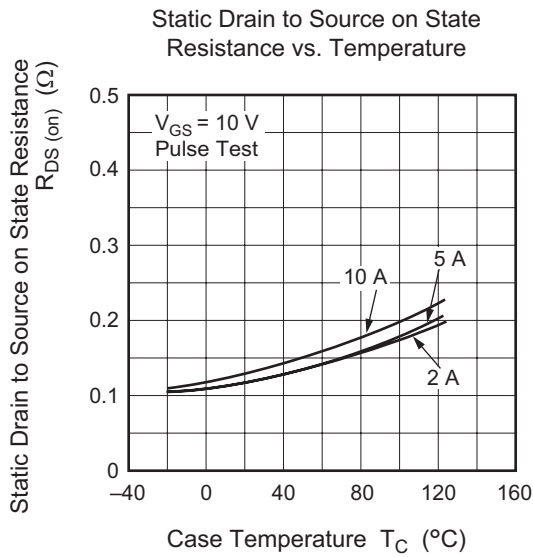
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	150	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±20	—	—	V	I <sub>G</sub> = ±100 μA, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	250	μA	V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	2.0	—	4.0	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V
Static drain to source on state resistance	R <sub>DS(on)</sub>	—	0.12	0.15	Ω	I <sub>D</sub> = 5 A, V <sub>GS</sub> = 10 V <sup>*3</sup>
Forward transfer admittance	y <sub>fs</sub>	4.0	7.0	—	S	I <sub>D</sub> = 5 A, V <sub>DS</sub> = 10 V <sup>*3</sup>
Input capacitance	C <sub>iss</sub>	—	1200	—	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0, f = 1 MHz
Output capacitance	C <sub>oss</sub>	—	550	—	pF	
Reverse transfer capacitance	C <sub>rss</sub>	—	85	—	pF	
Turn-on delay time	t <sub>d(on)</sub>	—	20	—	ns	I <sub>D</sub> = 5 A, V <sub>GS</sub> = 10 V, R <sub>L</sub> = 6 Ω
Rise time	t <sub>r</sub>	—	50	—	ns	
Turn-off delay time	t <sub>d(off)</sub>	—	70	—	ns	
Fall time	t <sub>f</sub>	—	40	—	ns	
Body to drain diode forward voltage	V <sub>DF</sub>	—	1.2	—	V	I <sub>F</sub> = 10 A, V <sub>GS</sub> = 0
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	220	—	ns	I <sub>F</sub> = 10 A, V <sub>GS</sub> = 0, di <sub>F</sub> /dt = 50 A/μs

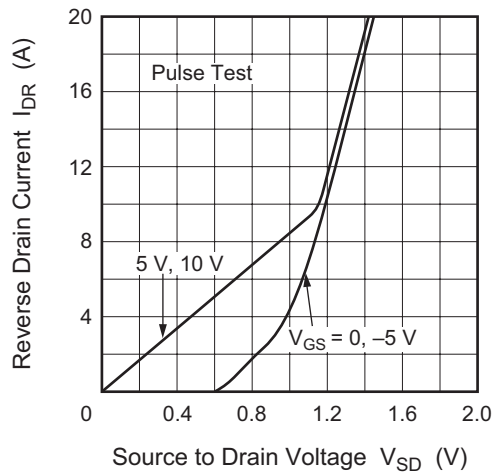
Note: 3. Pulse test

### Main Characteristics

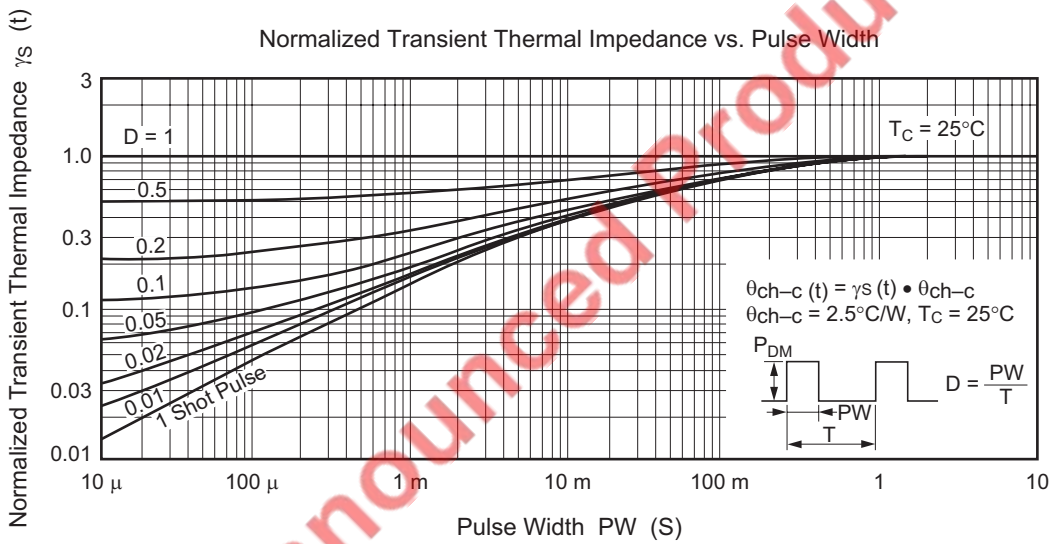




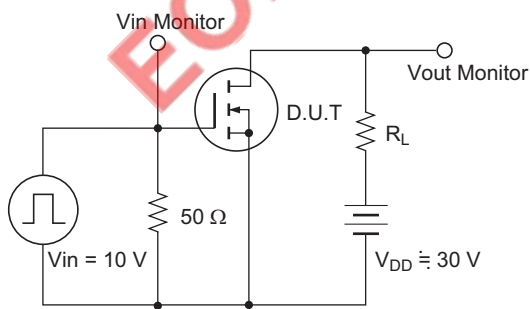
Reverse Drain Current vs. Source to Drain Voltage



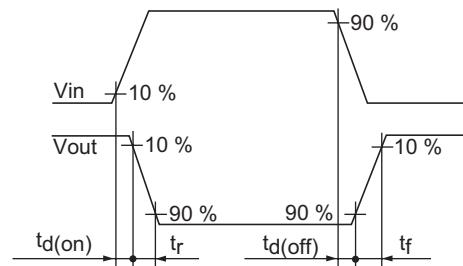
Normalized Transient Thermal Impedance vs. Pulse Width



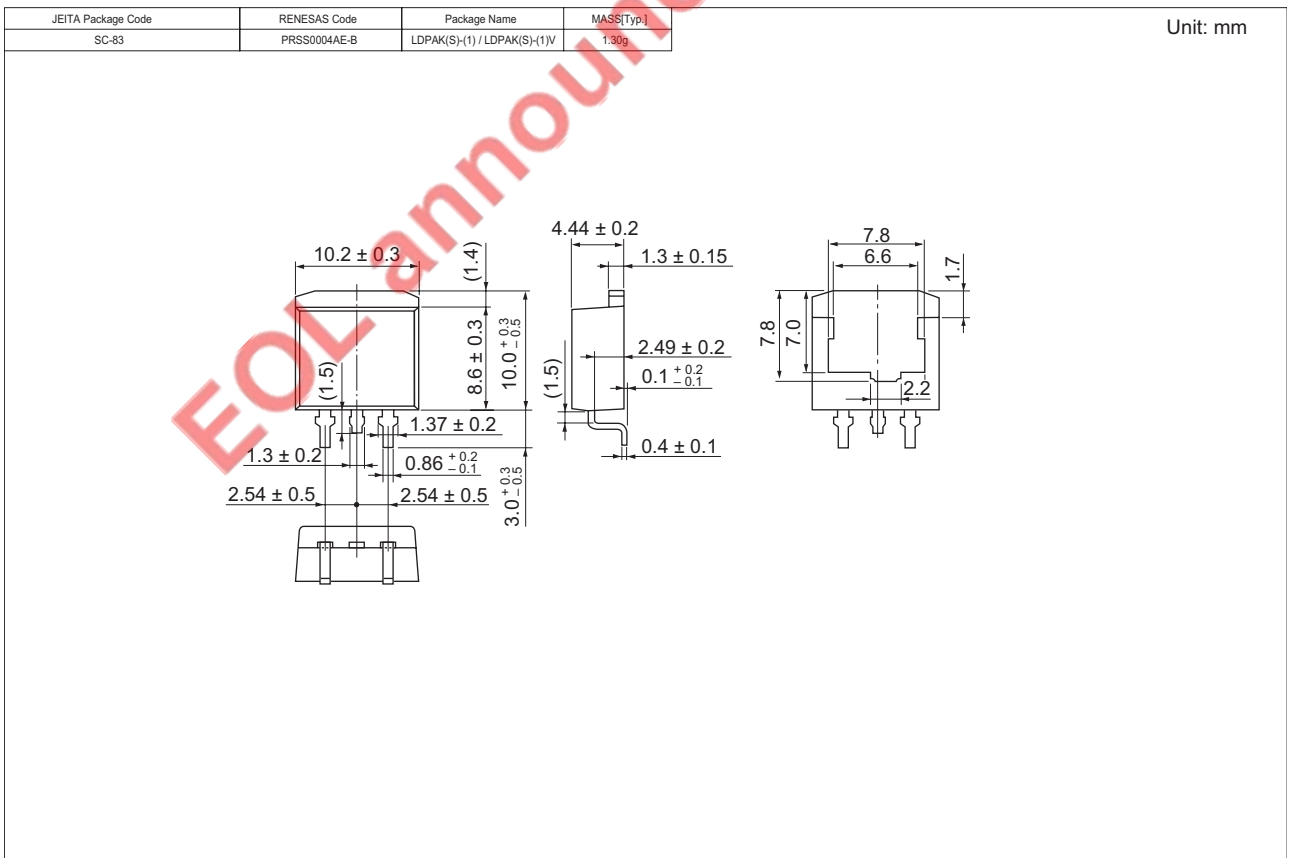
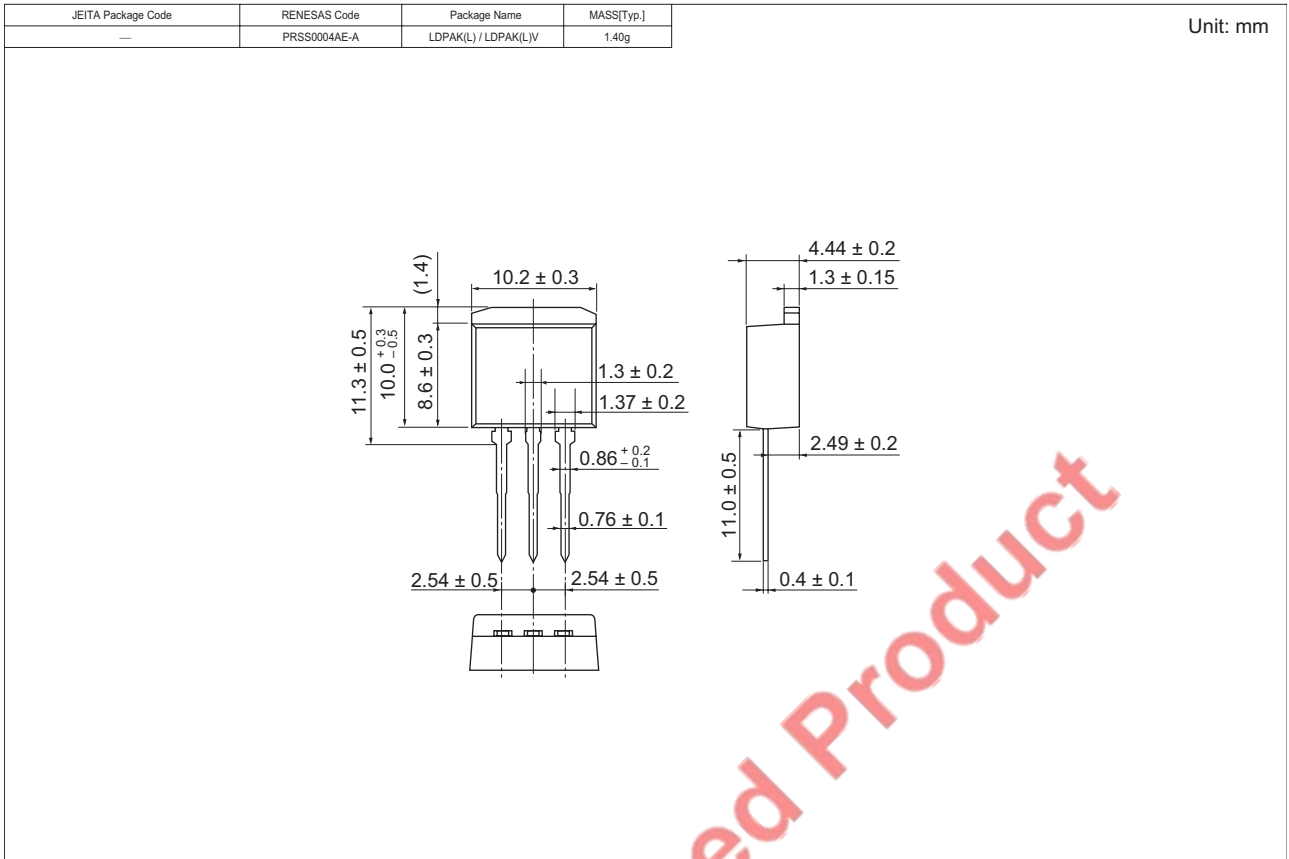
Switching Time Test Circuit



Waveforms



Package Dimensions





### Ordering Information

Part Name	Quantity	Shipping Container
2SK1620L-E	500 pcs	Box (Sack)
2SK1620STL-E	1000 pcs	Taping

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