

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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## 2SK2329(L), 2SK2329(S)

Silicon N Channel MOS FET

REJ03G1008-0200  
(Previous: ADE-208-1356)  
Rev.2.00  
Sep 07, 2005

### Application

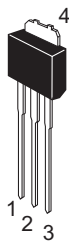
High speed power switching

### Features

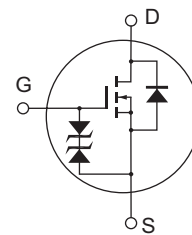
- Low on-resistance
- High speed switching
- Low drive current
- 2.5 V gate drive device can be driven from 3 V source
- Suitable for Switching regulator, DC-DC converter

### Outline

RENESAS Package code: PRSS0004ZD-B  
(Package name: DPAK(L)-(2))



RENESAS Package code: PRSS0004ZD-C  
(Package name: DPAK(S))



1. Gate
2. Drain
3. Source
4. Drain

## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	30	V
Gate to source voltage	$V_{GSS}$	±10	V
Drain current	$I_D$	10	A
Drain peak current	$I_{D(pulse)}^{*1}$	40	A
Body to drain diode reverse drain current	$I_{DR}$	10	A
Channel dissipation	$P_{ch}^{*2}$	20	W
Channel temperature	$T_{ch}$	150	°C
Storage temperature	$T_{stg}$	-55 to +150	°C

Notes: 1.  $PW \leq 10 \mu s$ , duty cycle  $\leq 1 \%$   
 2. Value at  $T_c = 25^\circ C$

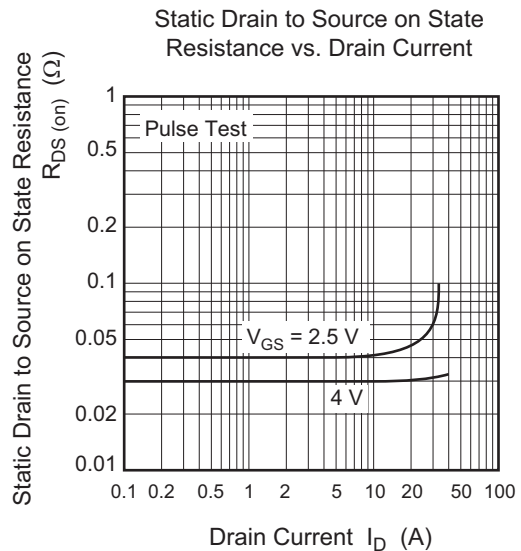
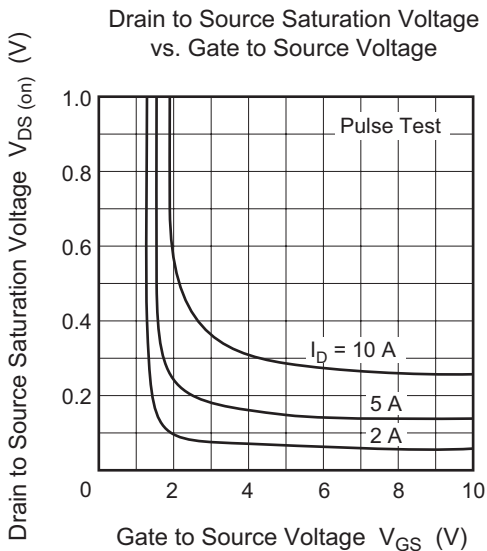
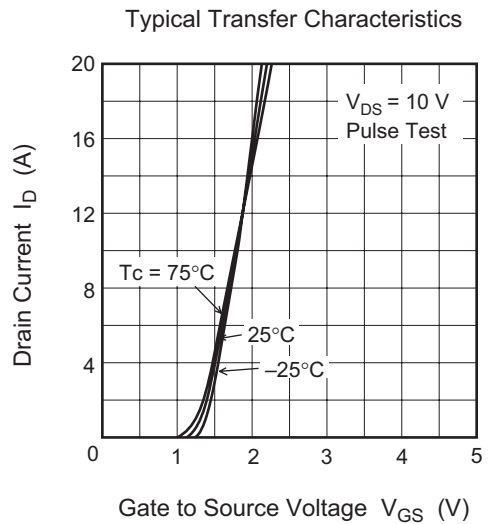
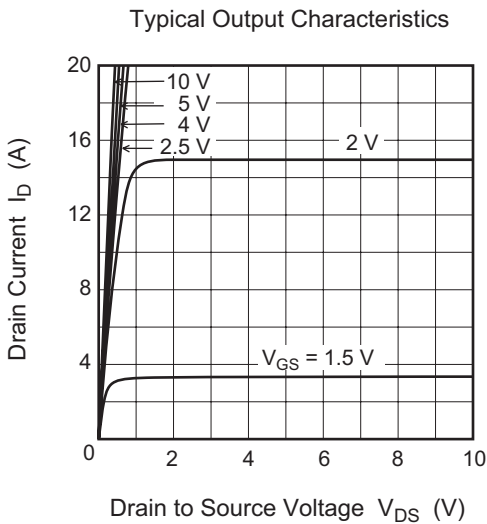
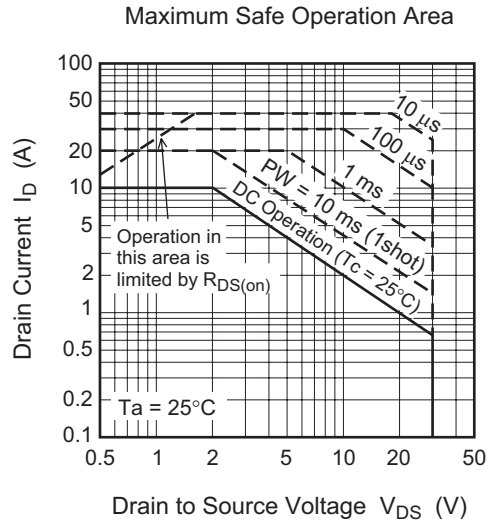
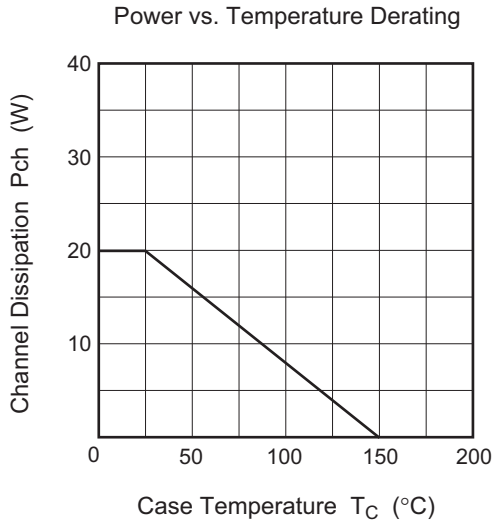
## Electrical Characteristics

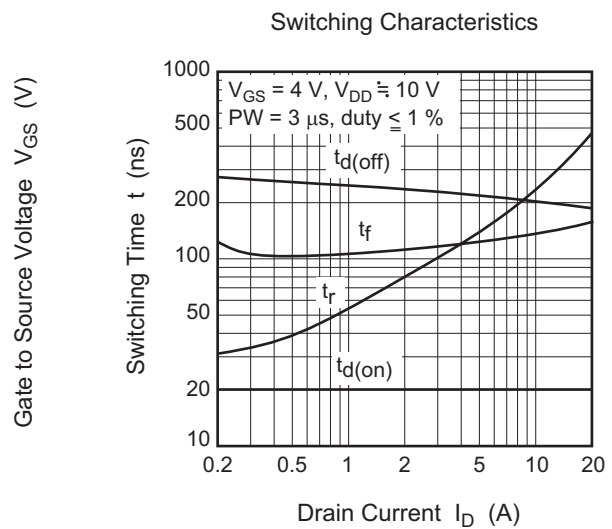
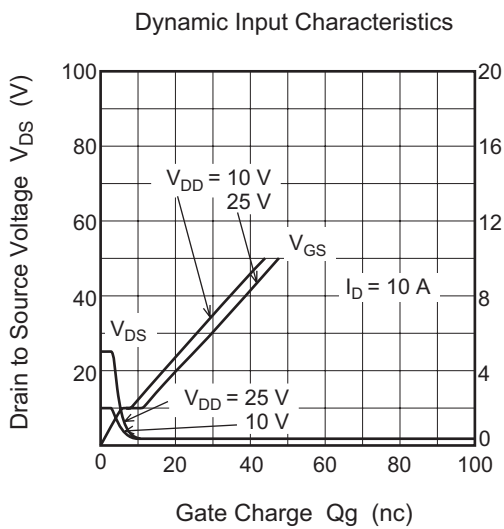
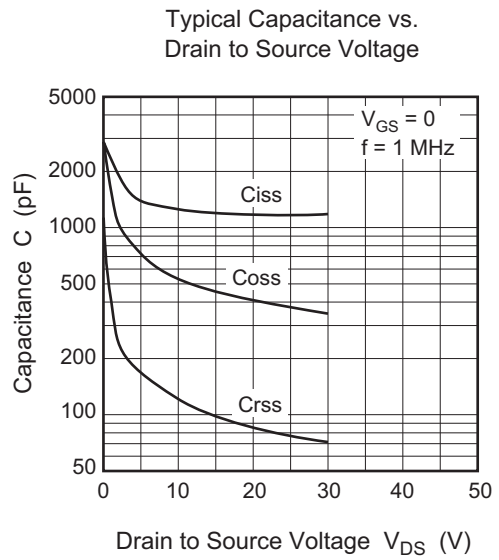
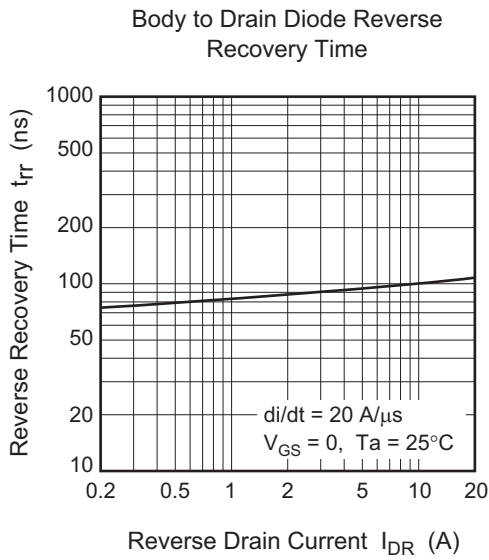
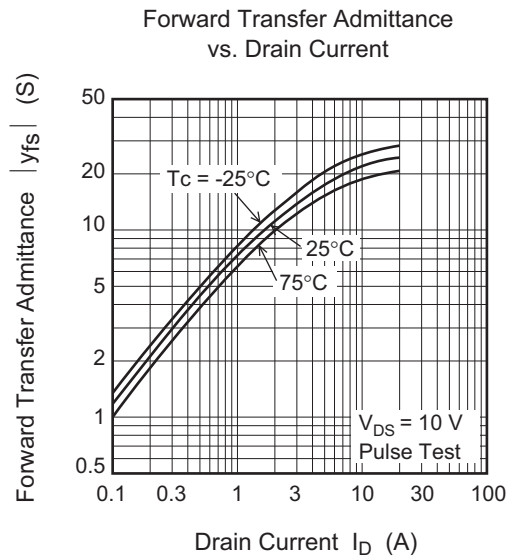
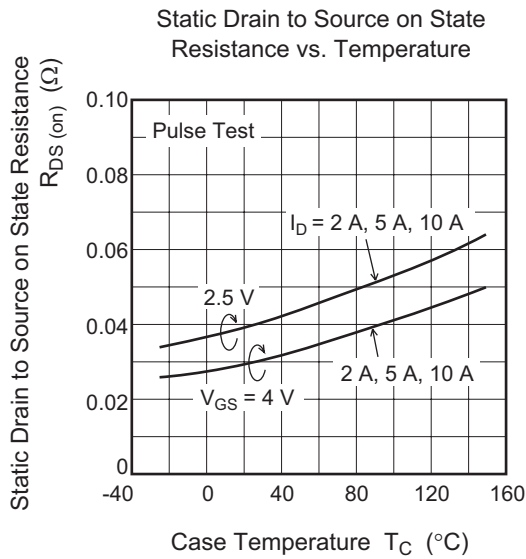
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±10	—	—	V	$I_G = \pm 200 \mu A$ , $V_{DS} = 0$
Gate to source leak current	$I_{GSS}$	—	—	±10	μA	$V_{GS} = \pm 6.5 \text{ V}$ , $V_{DS} = 0$
Zero gate voltage drain current	$I_{DSS}$	—	—	100	μA	$V_{DS} = 25 \text{ V}$ , $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.4	—	1.4	V	$I_D = 1 \text{ mA}$ , $V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.03	0.04	Ω	$I_D = 5 \text{ A}$ , $V_{GS} = 4 \text{ V}^{*3}$
		—	0.04	0.06	Ω	$I_D = 5 \text{ A}$ , $V_{GS} = 2.5 \text{ V}^{*3}$
Forward transfer admittance	$ y_{fs} $	10	18	—	S	$I_D = 5 \text{ A}$ , $V_{DS} = 10 \text{ V}^{*3}$
Input capacitance	$C_{iss}$	—	1250	—	pF	$V_{DS} = 10 \text{ V}$ , $V_{GS} = 0$ , $f = 1 \text{ MHz}$
Output capacitance	$C_{oss}$	—	540	—	pF	
Reverse transfer capacitance	$C_{rss}$	—	120	—	pF	
Turn-on delay time	$t_{d(on)}$	—	20	—	ns	$I_D = 5 \text{ A}$ , $V_{GS} = 4 \text{ V}$ , $R_L = 2 \Omega$
Rise time	$t_r$	—	145	—	ns	
Turn-off delay time	$t_{d(off)}$	—	225	—	ns	
Fall time	$t_f$	—	125	—	ns	
Body to drain diode forward voltage	$V_{DF}$	—	0.9	—	V	$I_F = 10 \text{ A}$ , $V_{GS} = 0$
Body to drain diode reverse recovery time	$t_{rr}$	—	100	—	ns	$I_F = 10 \text{ A}$ , $V_{GS} = 0$ , $di_F / dt = 20 \text{ A} / \mu s$

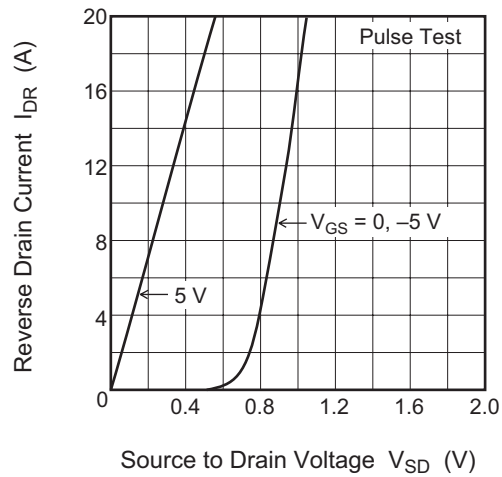
Note: 3. Pulse Test

### Main Characteristics

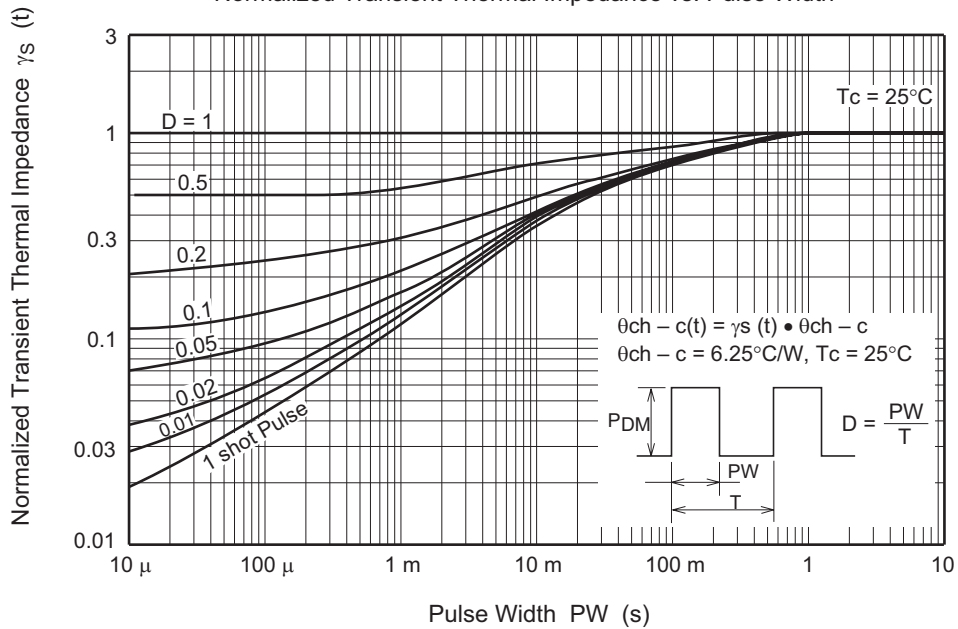




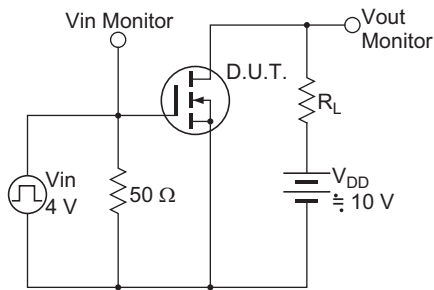
Reverse Drain Current vs. Source to Drain Voltage



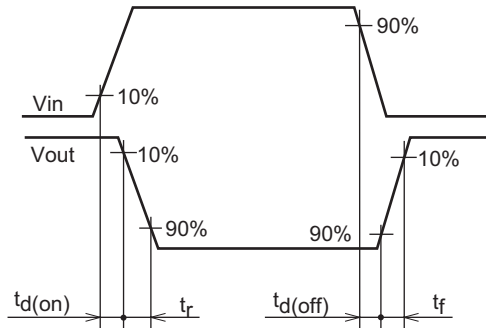
Normalized Transient Thermal Impedance vs. Pulse Width



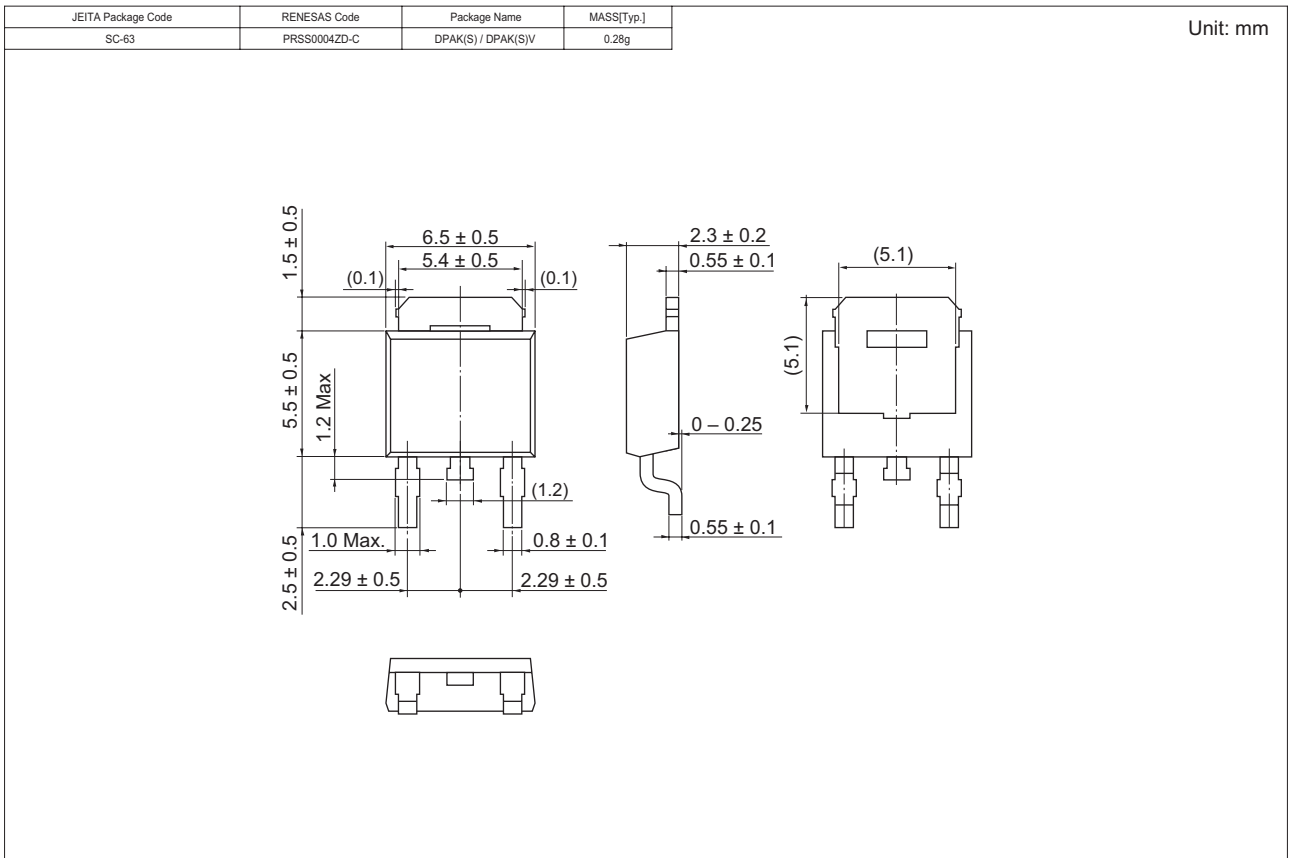
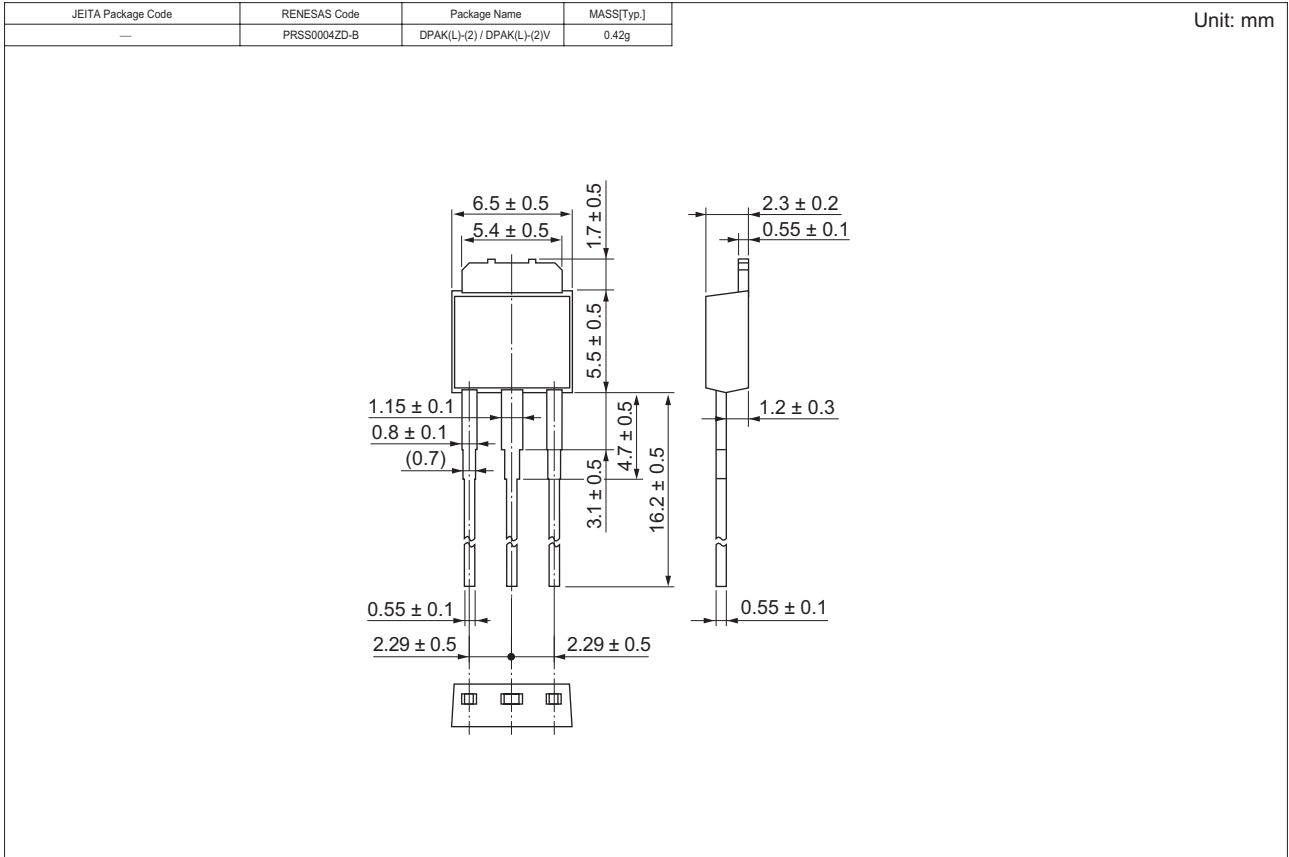
Switching Time Test Circuit



Waveforms



Package Dimensions





### Ordering Information

Part Name	Quantity	Shipping Container
2SK3239L-E	3000 pcs	Box (Sack)
2SK3239STL-E	3000 pcs	Taping

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Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China  
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#### **Renesas Technology Malaysia Sdn. Bhd.**

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