

To our customers,

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## Old Company Name in Catalogs and Other Documents

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April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

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EOL announced Product

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## 2SK1838(L), 2SK1838(S)

Silicon N Channel MOS FET

REJ03G0980-0300

Rev.3.00

Nov 21, 2005

### Application

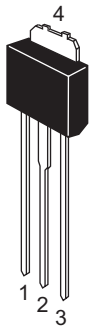
High speed power switching

### Features

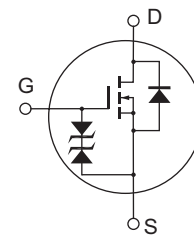
- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter

### Outline

RENESAS Package code: PRSS0004ZD-A  
(Package name: DPAK(L)-(1))



RENESAS Package code: PRSS0004ZD-C  
(Package name: DPAK(S))



1. Gate
2. Drain
3. Source
4. Drain

## Absolute Maximum Ratings

(Ta = 25°C)

| Item                                      | Symbol                              | Ratings     | Unit |
|---|-------------------------------------|-------------|------|
| Drain to source voltage                   | V <sub>DSS</sub>                    | 250         | V    |
| Gate to source voltage                    | V <sub>GSS</sub>                    | ±30         | V    |
| Drain current                             | I <sub>D</sub>                      | 1           | A    |
| Drain peak current                        | I <sub>D(pulse)</sub> <sup>*1</sup> | 2           | A    |
| Body to drain diode reverse drain current | I <sub>DR</sub>                     | 1           | A    |
| Channel dissipation                       | P <sub>ch</sub> <sup>*2</sup>       | 10          | W    |
| Channel temperature                       | T <sub>ch</sub>                     | 150         | °C   |
| Storage temperature                       | T <sub>stg</sub>                    | -55 to +150 | °C   |

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1 %  
 2. Value at T<sub>c</sub> = 25°C

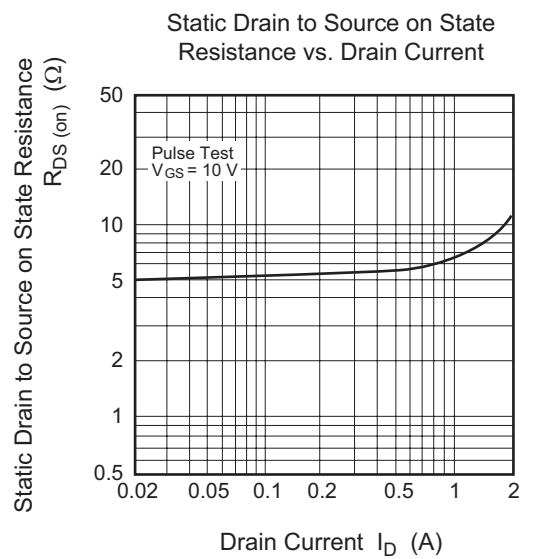
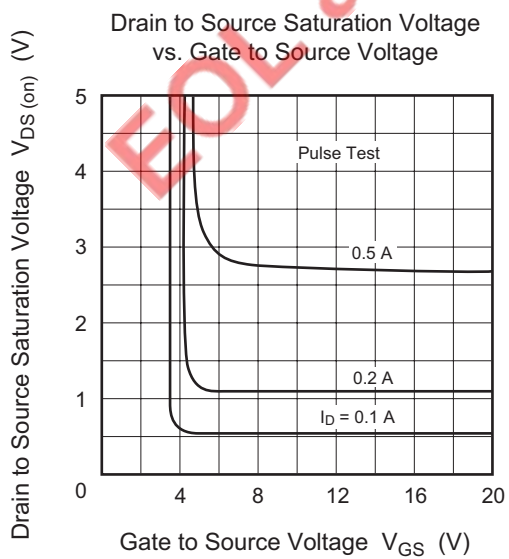
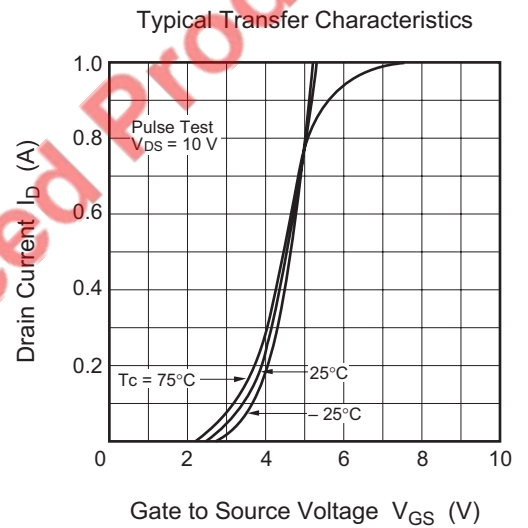
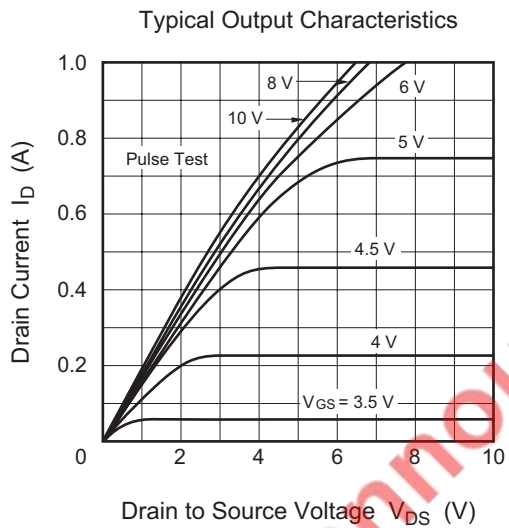
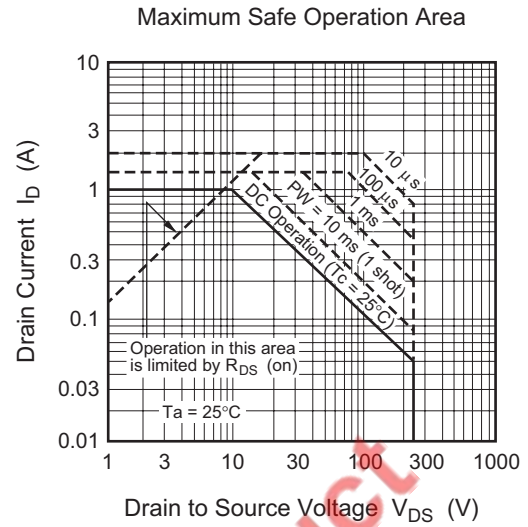
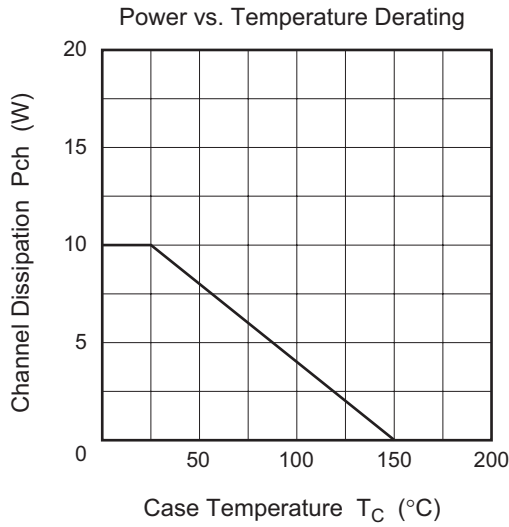
## Electrical Characteristics

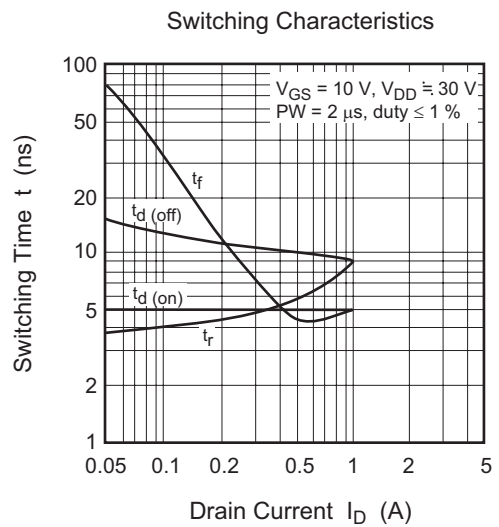
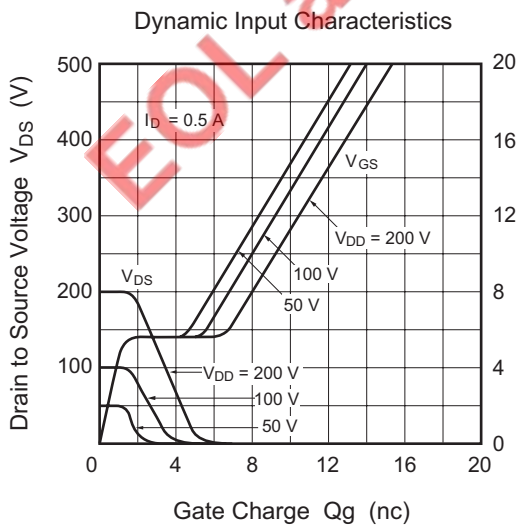
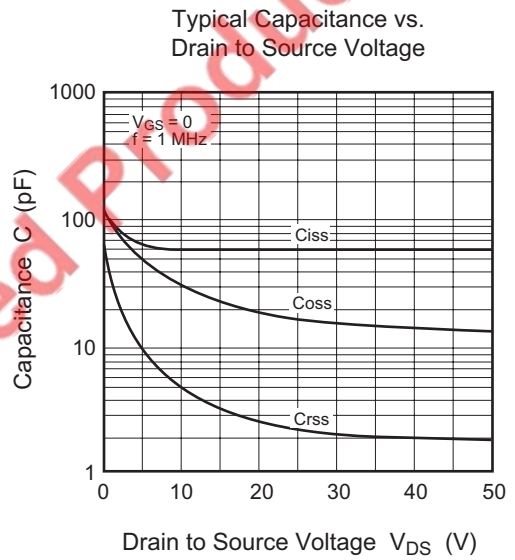
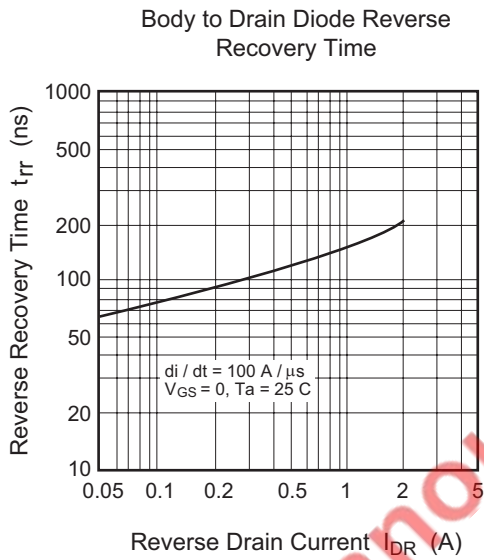
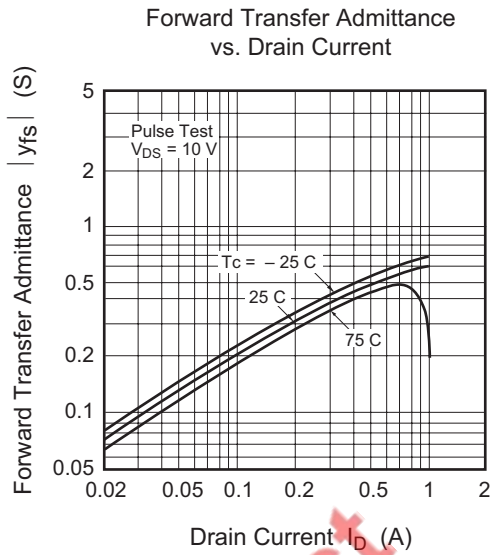
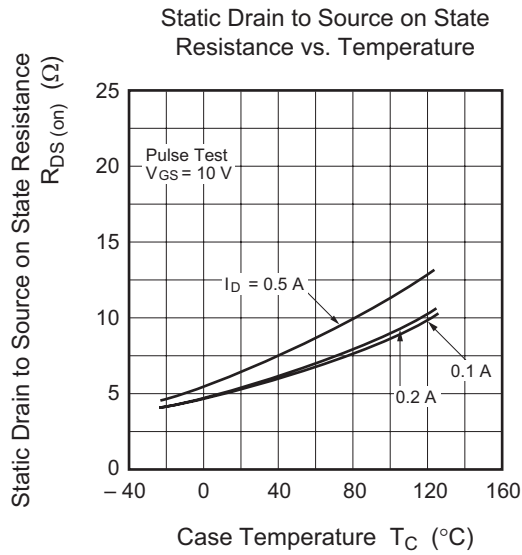
(Ta = 25°C)

| Item                                       | Symbol               | Min | Typ  | Max | Unit | Test conditions  |
|--|----------------------|-----|------|-----|------|--|
| Drain to source breakdown voltage          | V <sub>(BR)DSS</sub> | 250 | —    | —   | V    | I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0                                  |
| Gate to source breakdown voltage           | V <sub>(BR)GSS</sub> | ±30 | —    | —   | V    | I <sub>G</sub> = ±100 μA, V <sub>DS</sub> = 0                                |
| Gate to source leak current                | I <sub>GSS</sub>     | —   | —    | ±10 | μA   | V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0                                 |
| Zero gate voltage drain current            | I <sub>DSS</sub>     | —   | —    | 50  | μA   | V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0                                 |
| Gate to source cutoff voltage              | V <sub>GS(off)</sub> | 2.0 | —    | 3.0 | V    | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA                                |
| Forward transfer admittance                | y <sub>fs</sub>      | 0.3 | 0.5  | —   | S    | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.5 A <sup>*3</sup>                 |
| Static drain to source on state resistance | R <sub>DS(on)</sub>  | —   | 5.5  | 8.0 | Ω    | I <sub>D</sub> = 0.5 A, V <sub>GS</sub> = 10 V <sup>*3</sup>                 |
| Input capacitance                          | C <sub>iss</sub>     | —   | 60   | —   | pF   | V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0,<br>f = 1 MHz                    |
| Output capacitance                         | C <sub>oss</sub>     | —   | 30   | —   | pF   |  |
| Reverse transfer capacitance               | C <sub>rss</sub>     | —   | 5    | —   | pF   |  |
| Turn-on delay time                         | t <sub>d(on)</sub>   | —   | 5    | —   | ns   | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 A,<br>R <sub>L</sub> = 60 Ω     |
| Rise time                                  | t <sub>r</sub>       | —   | 6    | —   | ns   |  |
| Turn-off delay time                        | t <sub>d(off)</sub>  | —   | 10   | —   | ns   |  |
| Fall time                                  | t <sub>f</sub>       | —   | 4.5  | —   | ns   |  |
| Body to drain diode forward voltage        | V <sub>DF</sub>      | —   | 0.96 | —   | V    | I <sub>F</sub> = 1 A, V <sub>GS</sub> = 0                                    |
| Body to drain diode reverse recovery time  | t <sub>rr</sub>      | —   | 160  | —   | ns   | I <sub>F</sub> = 1 A, V <sub>GS</sub> = 0,<br>di <sub>F</sub> /dt = 100 A/μs |

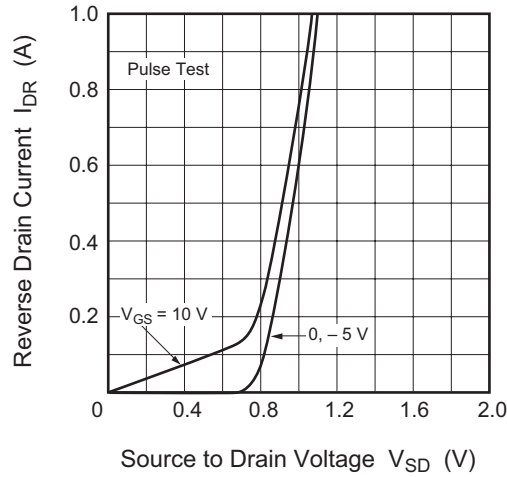
Note: 3. Pulse test

Main Characteristics

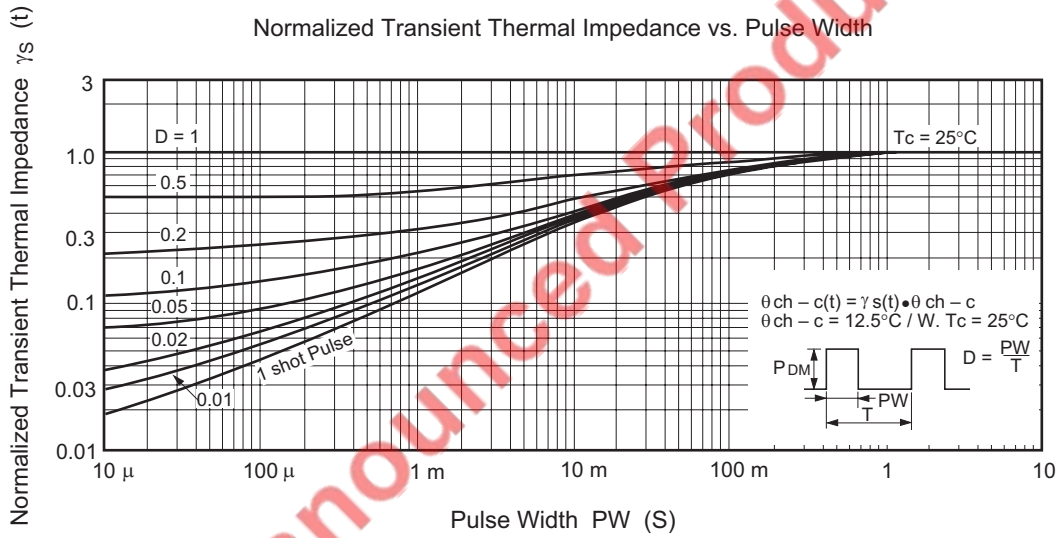




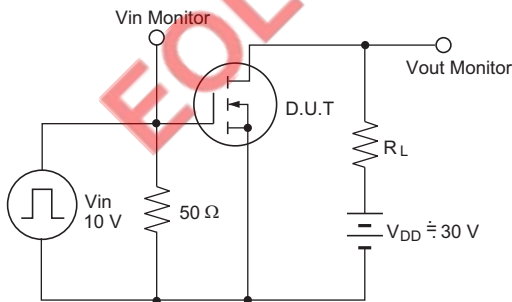
Reverse Drain Current vs. Source to Drain Voltage



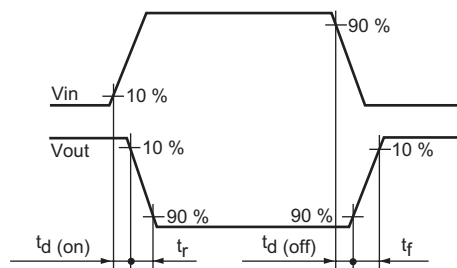
Normalized Transient Thermal Impedance vs. Pulse Width



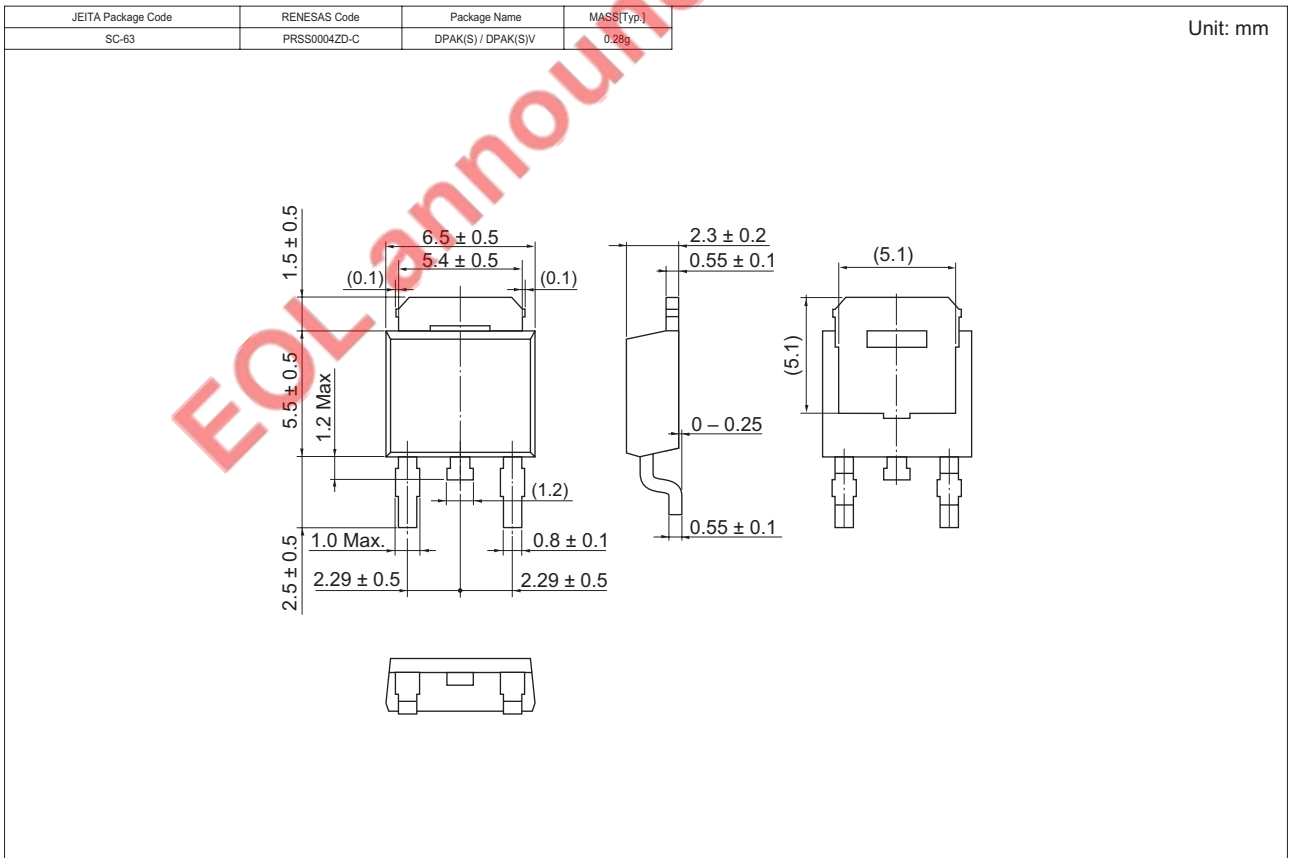
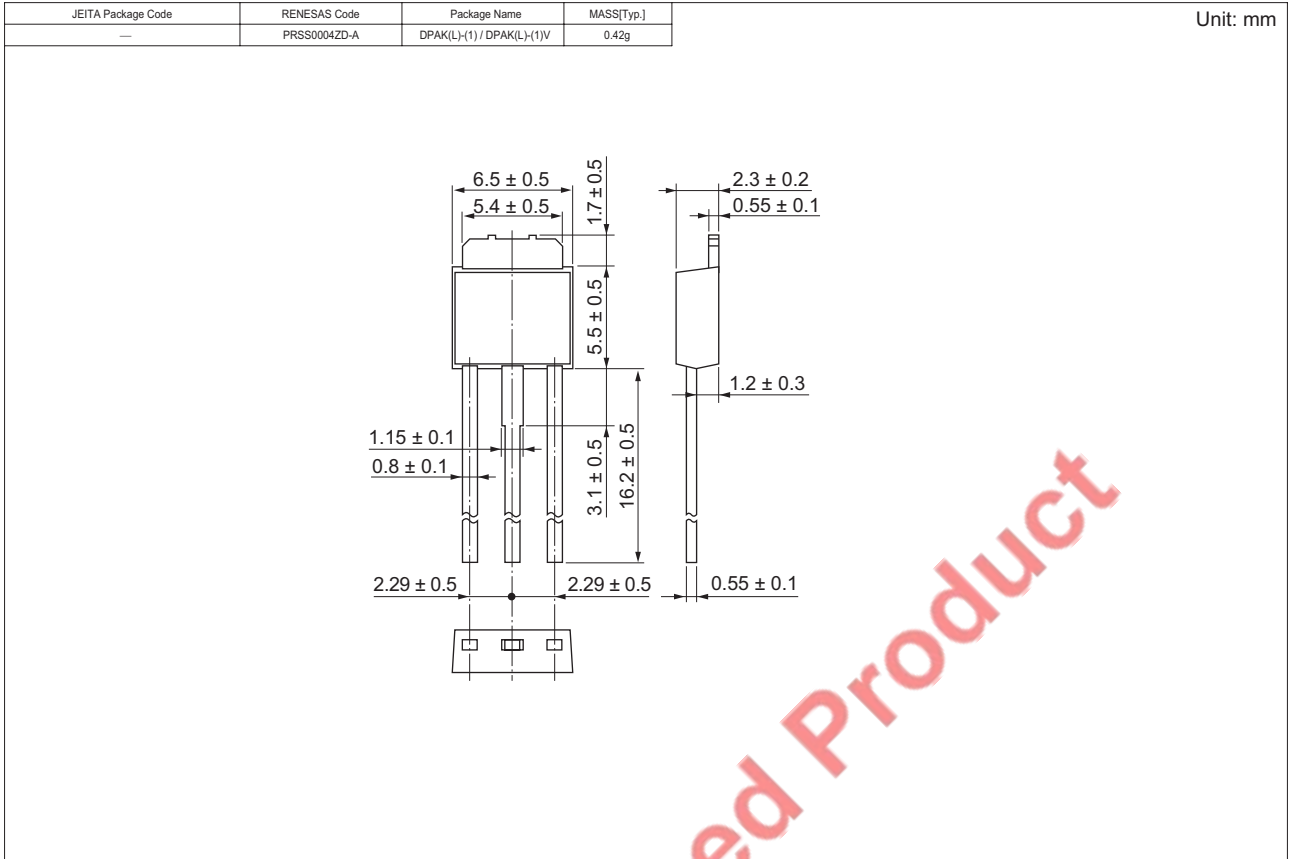
Switching Time Test Circuit



Waveforms



Package Dimensions





### Ordering Information

| Part Name    | Quantity | Shipping Container |
|--------------|----------|--------------------|
| 2SK1838L-E   | 3200 pcs | Box (Sack)         |
| 2SK1838STL-E | 3000 pcs | Taping             |

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