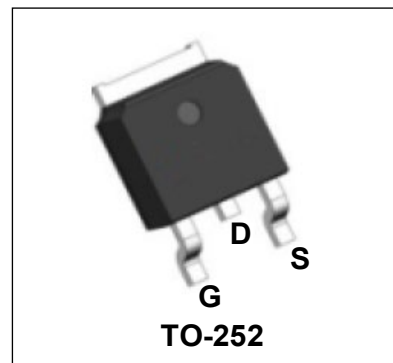


100V N-Channel Enhancement Mode Power MOSFET

Description

WMO25N10T1 uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

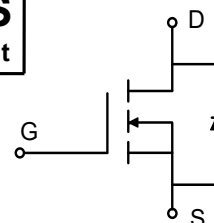


Features

- $V_{DS} = 100V$, $I_D = 25A$
- $R_{DS(on)} < 45m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(on)} < 55m\Omega$ @ $V_{GS} = 4.5V$
- Green Device Available
- 100% EAS Guaranteed
- Optimized for High Speed Smooth Switching



RoHS
compliant



Applications

- Power Management Switches
- DC/DC Converter

Absolute Maximum Ratings (T_c = 25°C, unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T _c =25°C	I_D	25	A
	T _c =100°C		15.8	
Pulsed Drain Current ⁴		I_{DM}	100	A
Single Pulse Avalanche Energy ³		EAS	8.45	mJ
Total Power Dissipation	T _c =25°C	P_D	53.2	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ¹	$R_{\theta JA}$	62	°C/W
Thermal Resistance from Junction-to-Case	$R_{\theta Jc}$	2.35	°C/W

Electrical Characteristics (T_c = 25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V_{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	100	-	-	V
Gate-Body Leakage Current	I_{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T _J =25°C	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μA
	T _J =100°C		-	-	100	
Gate-Threshold Voltage	V_{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.2	1.9	2.5	V
Drain-Source on-Resistance ²	R_{DS(on)}	V _{GS} = 10V, I _D = 10A	-	35	45	mΩ
		V _{GS} = 4.5V, I _D = 5A	-	42	55	
Forward Transconductance ²	g_{fs}	V _{DS} = 10V, I _D = 10A	-	22	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	V _{DS} = 50V, V _{GS} = 0V, f = 1MHz	-	2332	-	pF
Output Capacitance	C_{oss}		-	61.5	-	
Reverse Transfer Capacitance	C_{rss}		-	50	-	
Switching Characteristics						
Gate Resistance	R_G	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz	-	0.5	-	Ω
Total Gate Charge	Q_g	V _{GS} = 10V, V _{DD} = 50V, I _D = 10A	-	37.9	-	nC
Gate-Source Charge	Q_{gs}		-	8.3	-	
Gate-Drain Charge	Q_{gd}		-	7.2	-	
Turn-on Delay Time	t_{d(on)}		-	7.5	-	
Rise Time	t_r	V _{GS} = 10V, V _{DD} = 50V, R _G = 3Ω, I _D = 10A	-	35.8	-	ns
Turn-off Delay Time	t_{d(off)}		-	36.2	-	
Fall Time	t_f		-	22	-	
Drain-Source Body Diode Characteristics						
Diode Forward Voltage ²	V_{SD}	I _S = 10A, V _{GS} = 0V	-	-	1.2	V
Continuous Source Current ^{1,5}	I_S	V _G =V _D =0V, Force Current	-	-	25	A
Reverse Recovery Time	t_{rr}	V _R = 50V, I _F = 10A, dI _F /dt = 100A/μs	-	42	-	ns
Reverse Recovery Charge	Q_{rr}		-	45	-	nC

Notes:

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=13A
- Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C.
- The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Characteristics

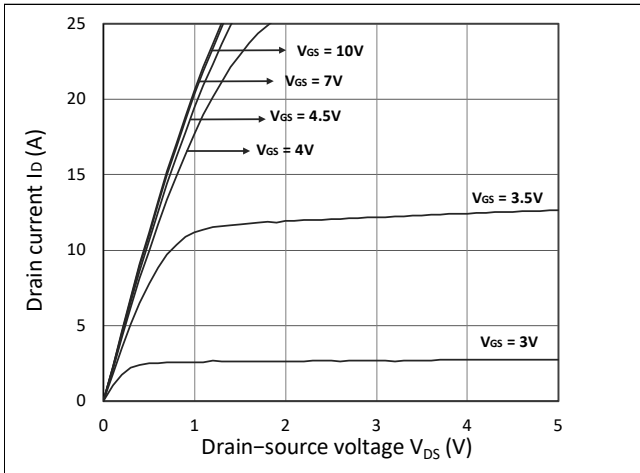


Figure 1. Output Characteristics

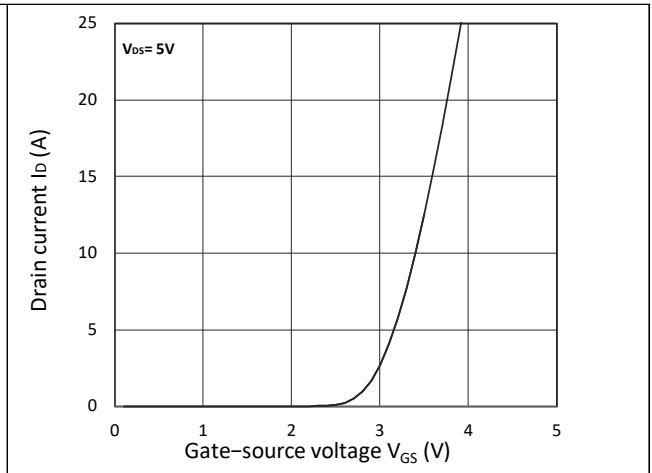


Figure 2. Transfer Characteristics

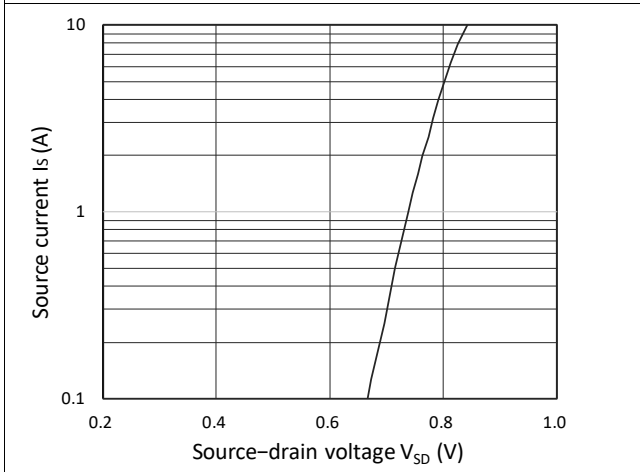


Figure 3. Forward Characteristics of Reverse

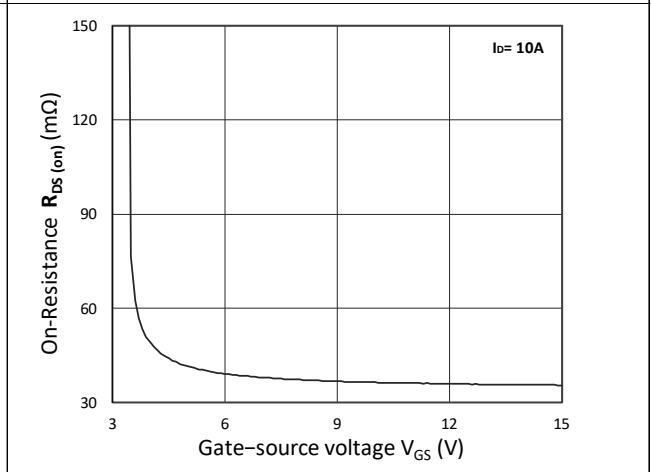


Figure 4. $R_{DS(on)}$ vs. V_{GS}

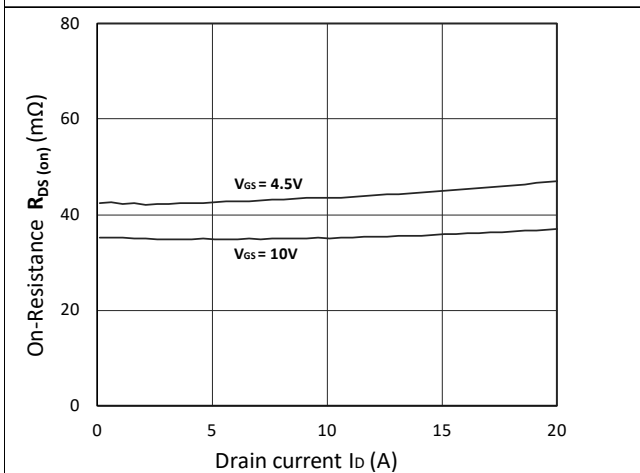


Figure 5. $R_{DS(on)}$ vs. I_D

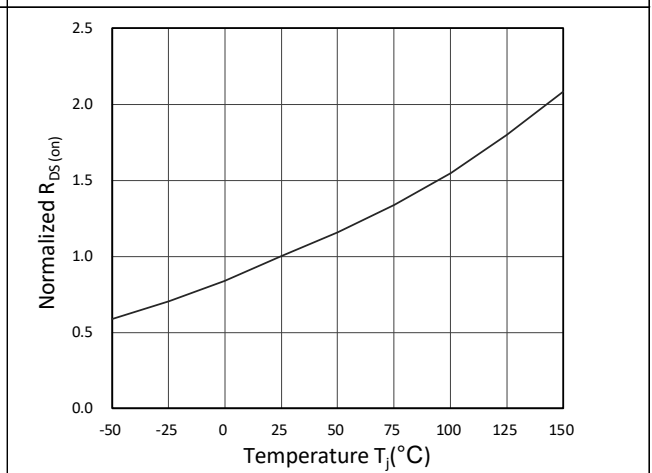


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

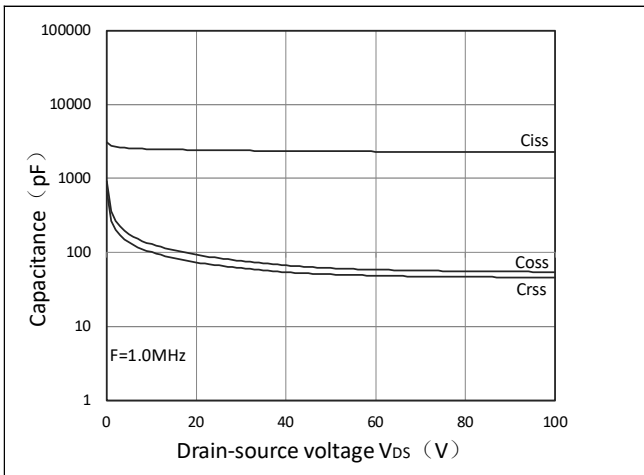


Figure 7. Capacitance Characteristics

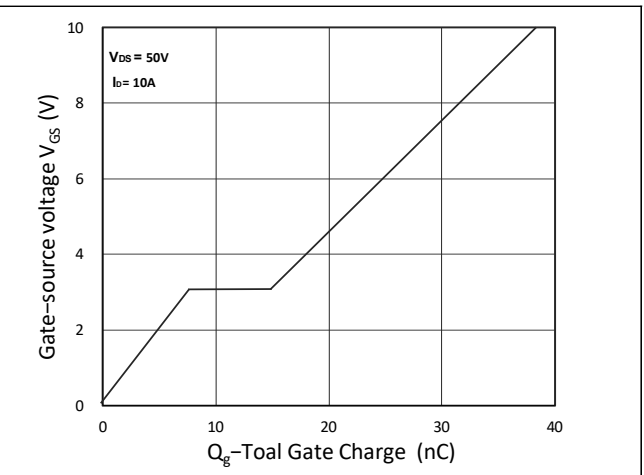


Figure 8. Gate Charge Characteristics

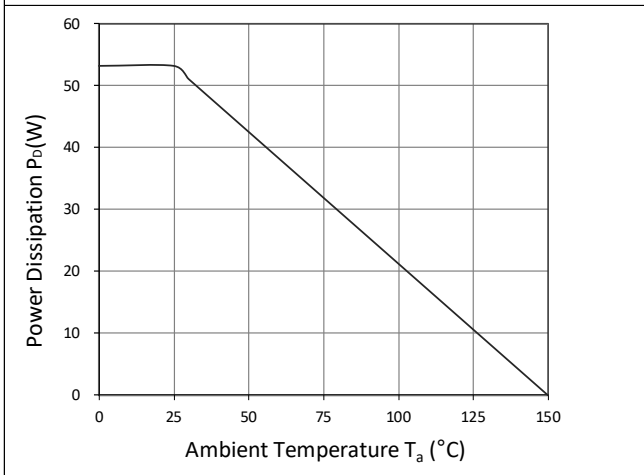


Figure 9. Power Dissipation

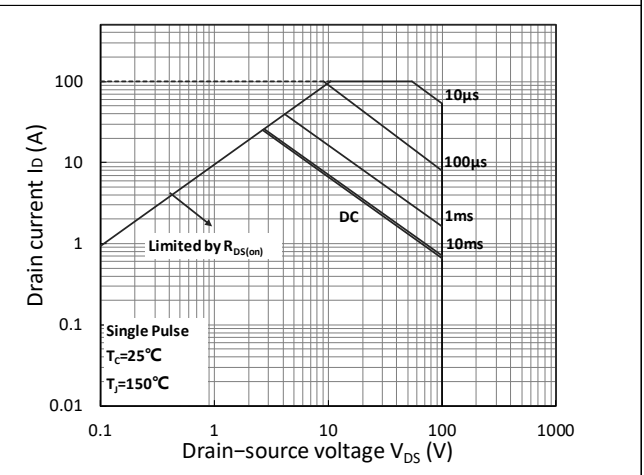


Figure 10. Safe Operating Area

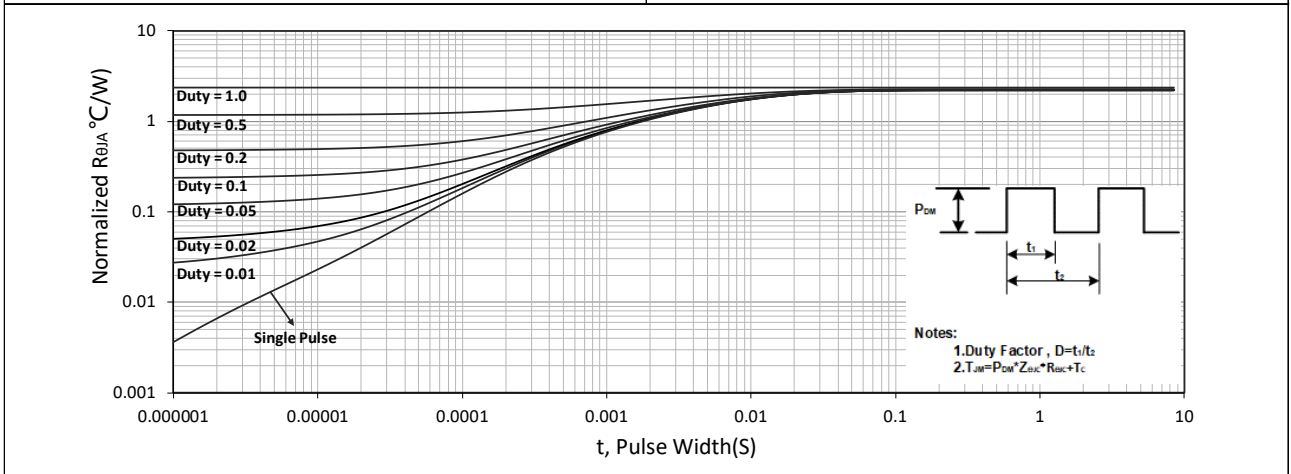


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

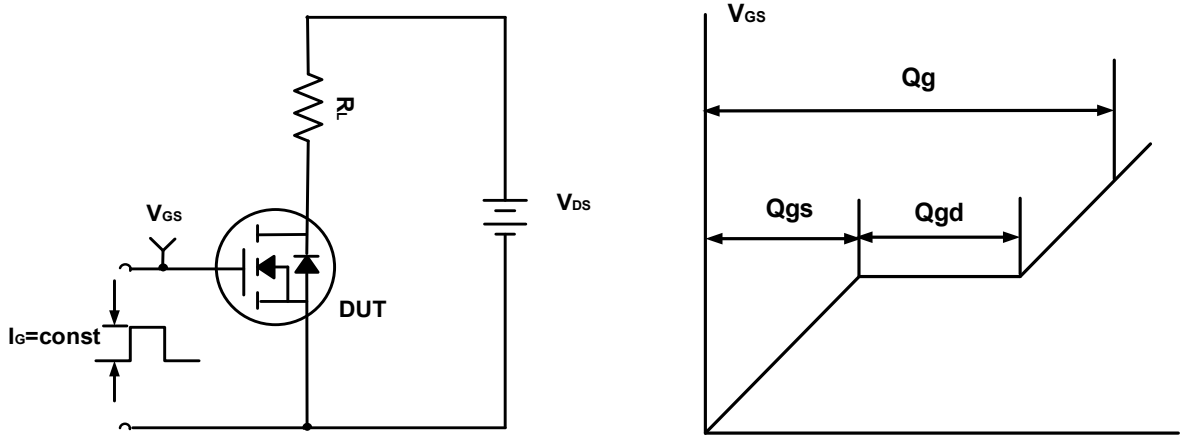


Figure A. Gate Charge Test Circuit & Waveforms

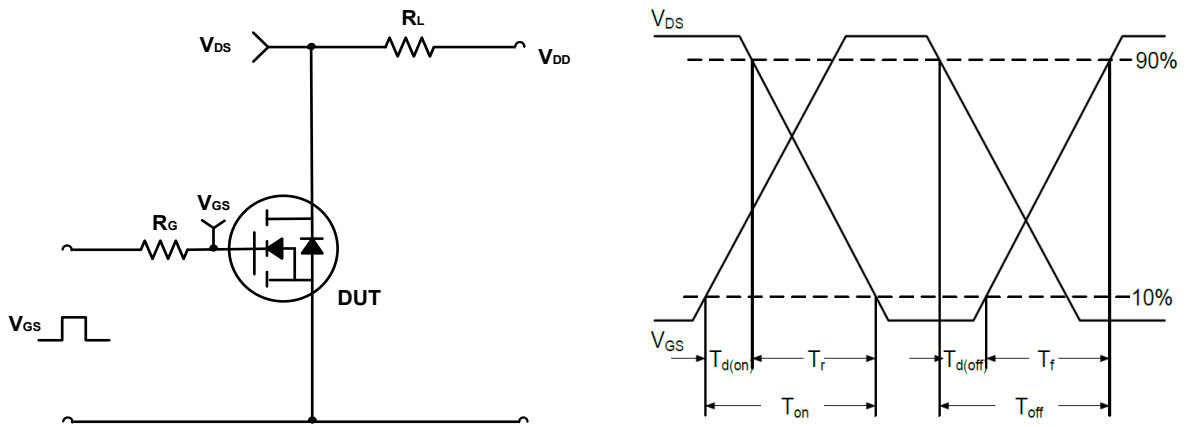


Figure B. Switching Test Circuit & Waveforms

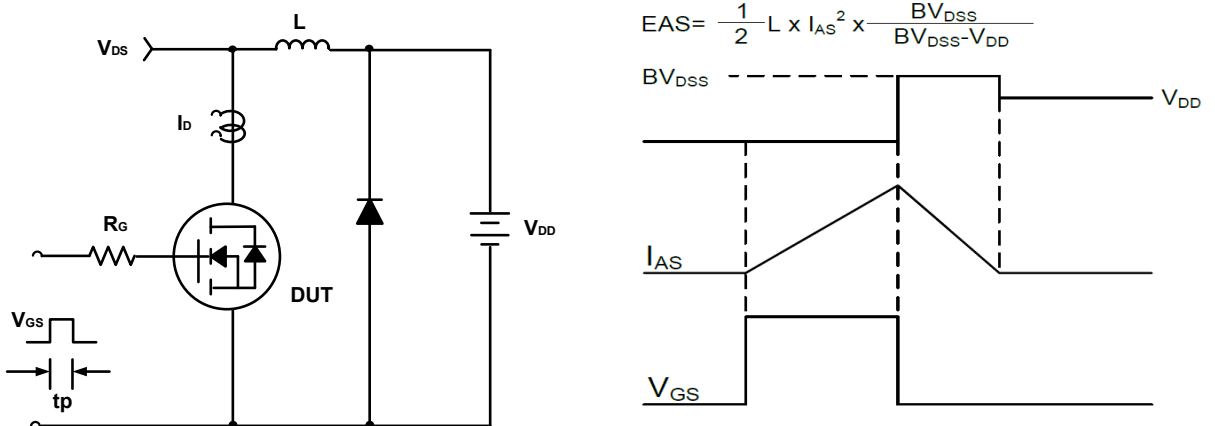
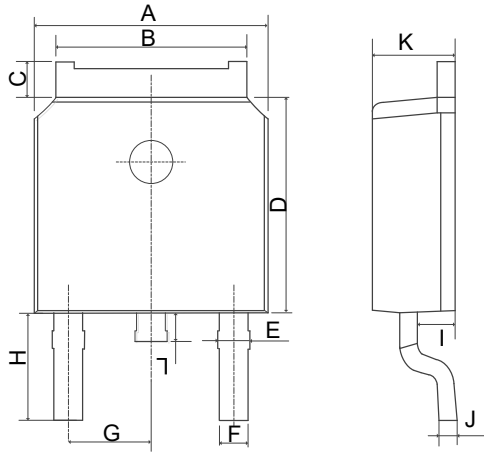


Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for TO-252

COMMON DIMENSIONS

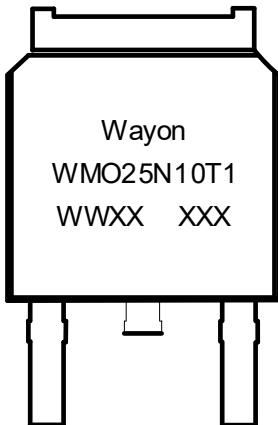


SYMBOL	MM	
	MIN	MAX
A	6.40	6.80
B	5.13	5.50
C	0.88	1.28
D	5.90	6.22
E	0.68	1.10
F	0.68	0.91
G	2.29REF	
H	2.90REF	
I	0.85	1.17
J	0.51REF	
K	2.10	2.50
L	0.40	1.00

Ordering Information

Part	Package	Marking	Packing method
WMO25N10T1	TO-252	WMO25N10T1	Tape and Reel

Marking Information



WMO25N10T1 = Device code

WWXX XXX= Date code


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