

## P-Channel Enhancement Mode Power MOSFET

### Description

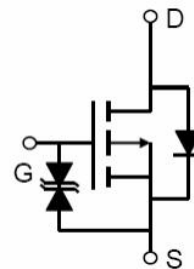
The G 3415LL uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge. It can be used in a wide variety of applications.

### General Features

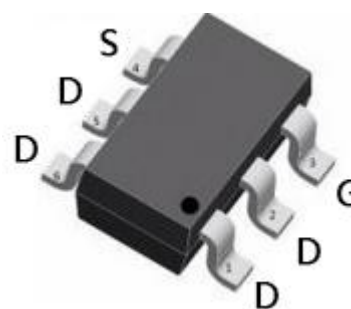
- $V_{DS}$  -20V
- $I_D$  (at  $V_{GS} = -10V$ ) -4A
- $R_{DS(ON)}$  (at  $V_{GS} = -4.5V$ ) < 35m $\Omega$
- $R_{DS(ON)}$  (at  $V_{GS} = -2.5V$ ) < 45m $\Omega$
- 100% Avalanche Tested
- RoHS Compliant
- ESD (HBM)>4KV

### Application

- Power switch
- DC/DC converters



Schematic diagram



SOT-23-6

### Ordering Information

Device	Package	Marking	Packaging
G3415LL	SOT-23-6	R15	3000pcs/Reel

### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	-20	V
Continuous Drain Current	$I_D$	-4	A
Pulsed Drain Current (note1)	$I_{DM}$	-16	A
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Power Dissipation	$P_D$	1.4	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	$^\circ\text{C}$

### Thermal Resistance

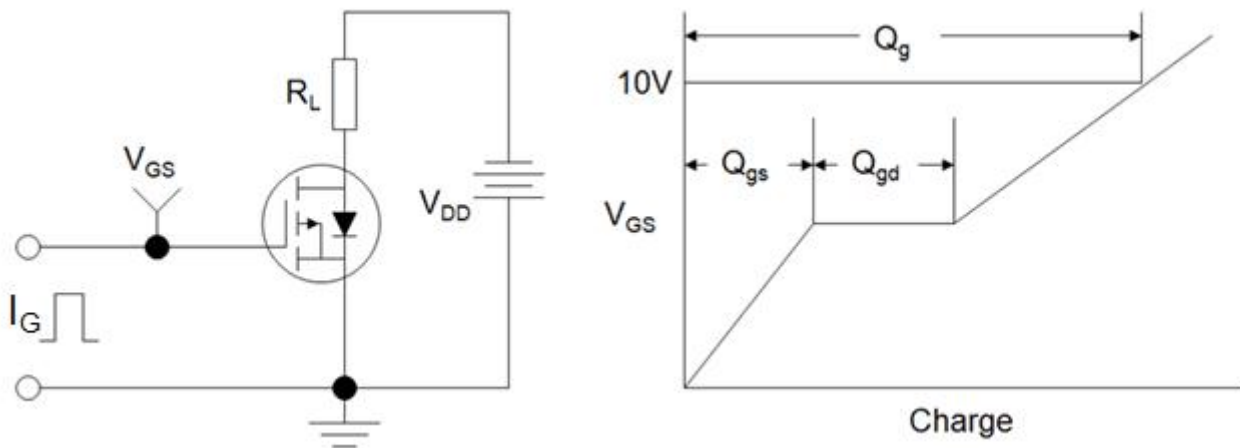
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	90	$^\circ\text{C/W}$

Specifications $T_J = 25^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-20	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -20V, V_{GS} = 0V$	--	--	-1	$\mu A$
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 10V$	--	--	$\pm 20$	$\mu A$
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-0.45	-0.7	-1.1	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = -4.5V, I_D = -2A$	--	28	35	m $\Omega$
		$V_{GS} = -2.5V, I_D = -2A$	--	36	45	
Forward Transconductance	$g_{FS}$	$V_{DS} = -5V, I_D = -2A$	--	9	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V,$ $V_{DS} = -10V,$ $f = 1.0MHz$	--	1106	--	pF
Output Capacitance	$C_{oss}$		--	133	--	
Reverse Transfer Capacitance	$C_{rss}$		--	122	--	
Total Gate Charge	$Q_g$	$V_{DD} = -10V,$ $I_D = -2A,$ $V_{GS} = -10V$	--	10	--	nC
Gate-Source Charge	$Q_{gs}$		--	1.3	--	
Gate-Drain Charge	$Q_{gd}$		--	2.4	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -10V,$ $I_D = -2A,$ $R_G = 3\Omega$	--	12	--	ns
Turn-on Rise Time	$t_r$		--	10	--	
Turn-off Delay Time	$t_{d(off)}$		--	25	--	
Turn-off Fall Time	$t_f$		--	19	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	-4	A
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = -2A, V_{GS} = 0V$	--	--	-1.2	V
Reverse Recovery Charge	$Q_{rr}$	$I_F = -2A, V_{GS} = 0V$ $di/dt = -100A/\mu s$	--	7	--	nC
Reverse Recovery Time	$T_{rr}$		--	18	--	ns

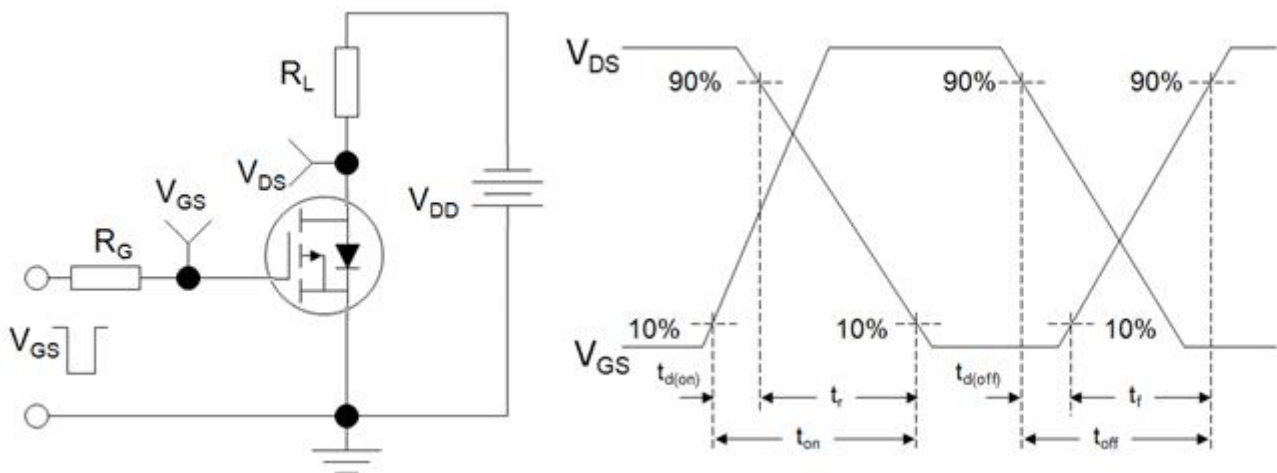
### Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Identical low side and high side switch with identical  $R_G$

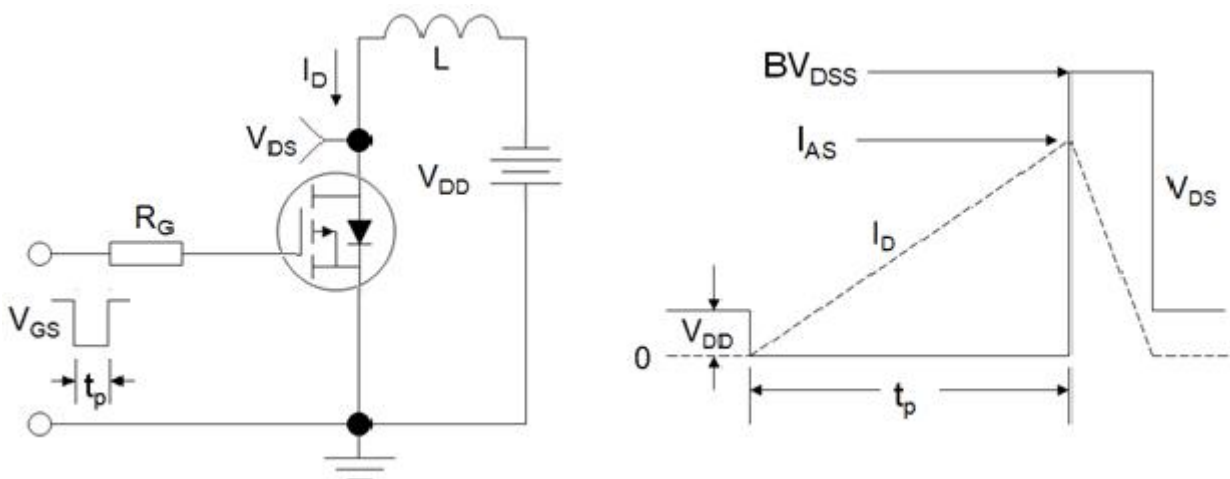
### Gate Charge Test Circuit



### Switch Time Test Circuit



### EAS Test Circuit



Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Figure 1. Output Characteristics

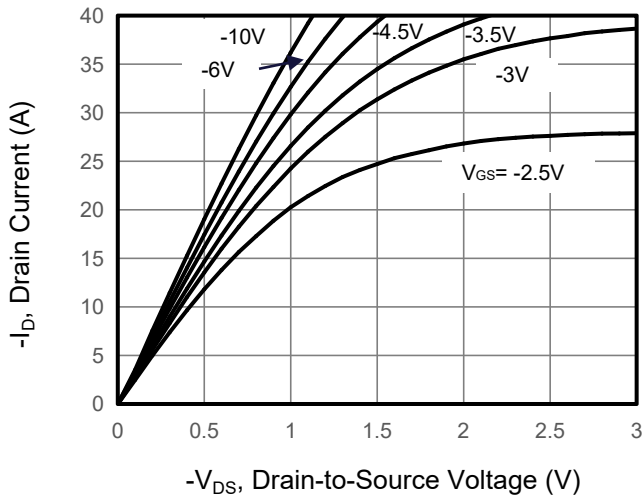


Figure 2. Transfer Characteristics

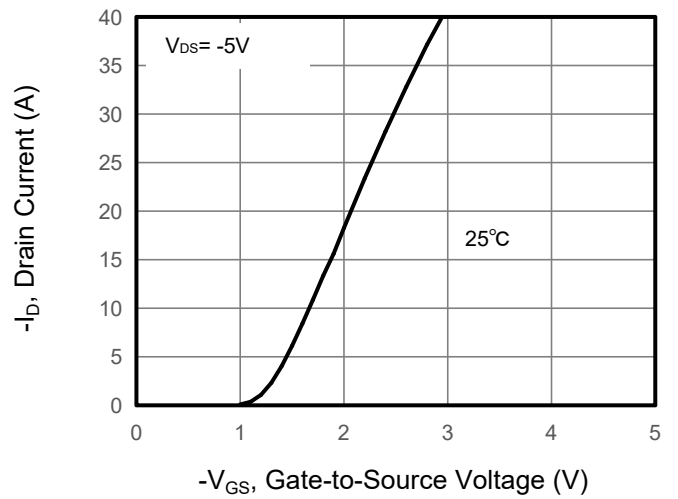


Figure 3. Drain Source On Resistance

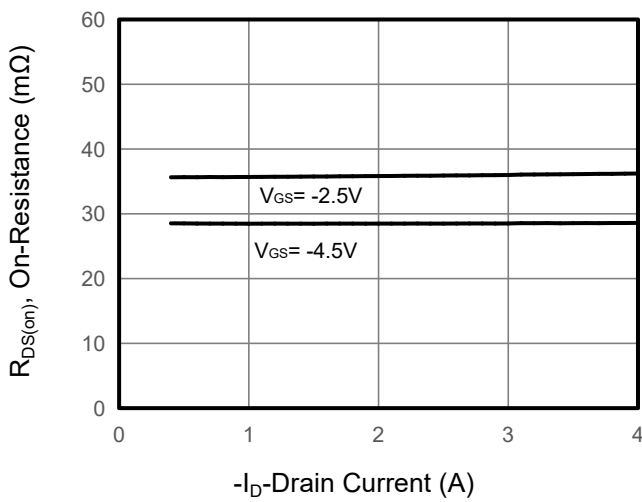


Figure 4. Gate Charge

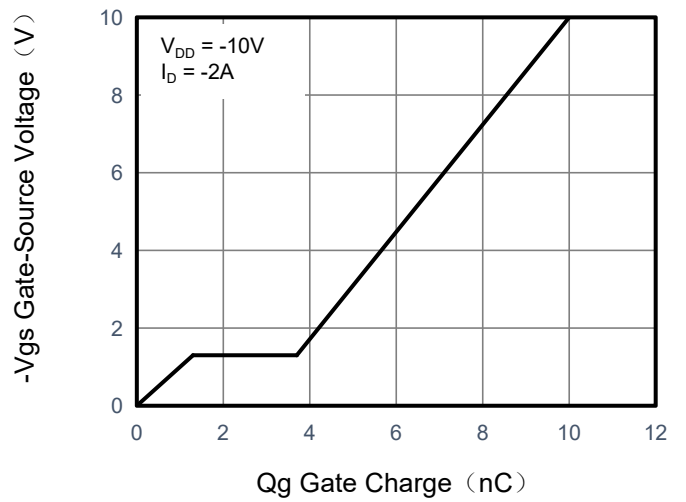


Figure 5. Capacitance

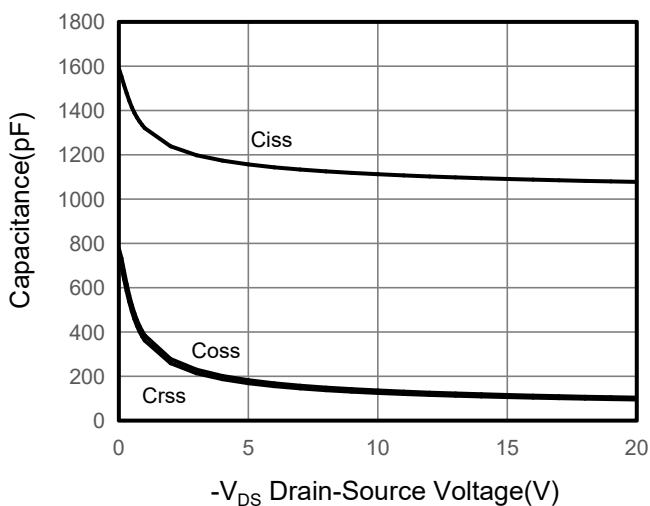
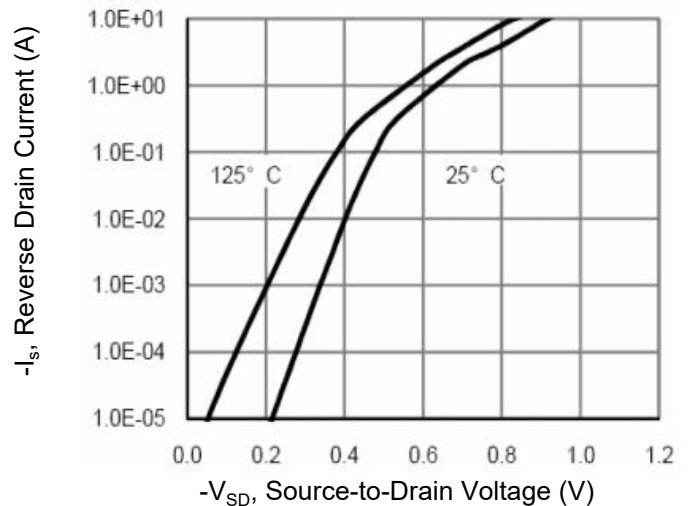
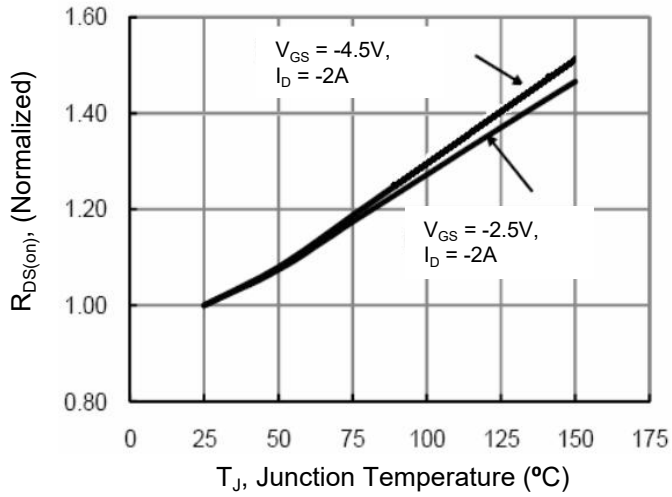


Figure 6. Source-Drain Diode Forward

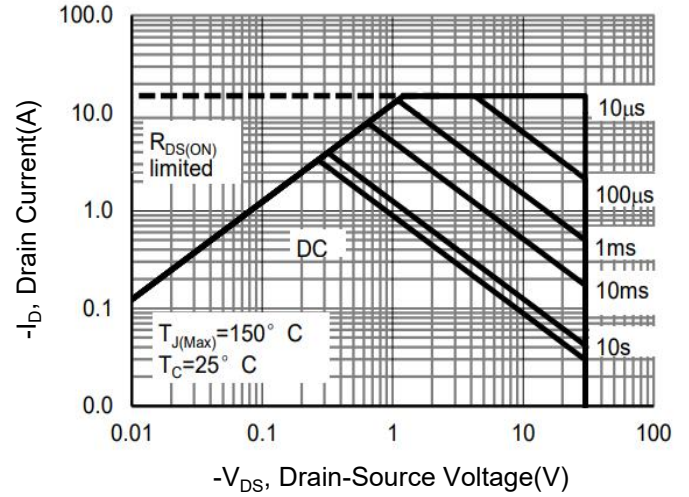


## Typical Characteristics $T_J = 25^\circ\text{C}$ , unless otherwise noted

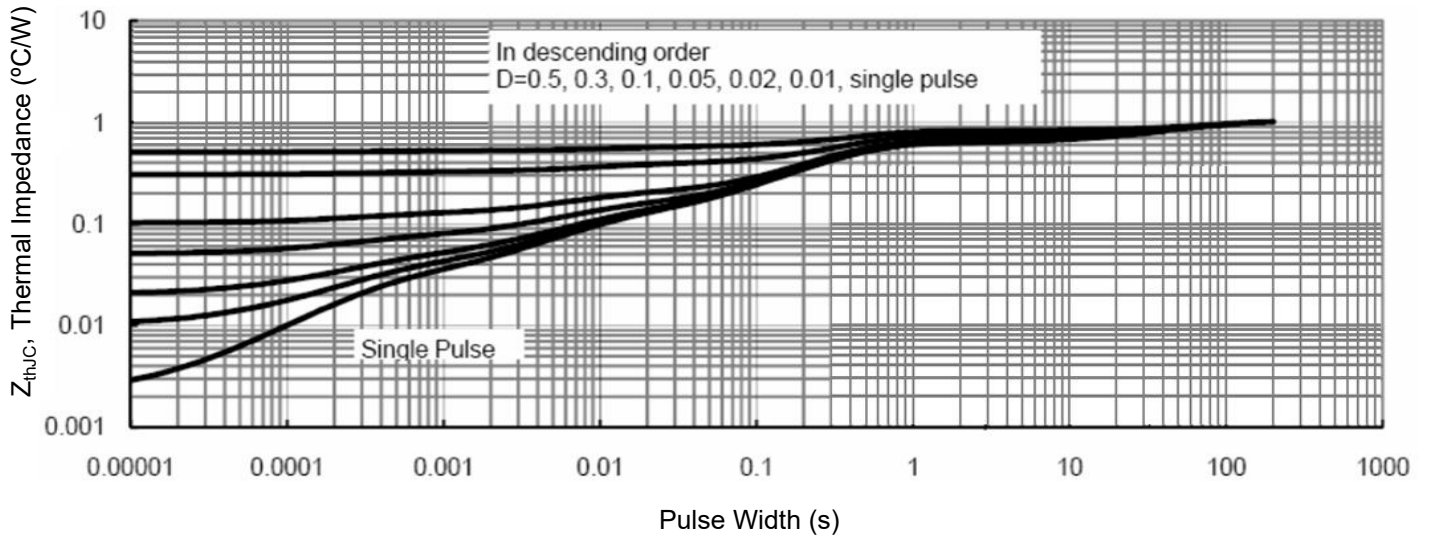
**Figure 7. Drain-Source On-Resistance**



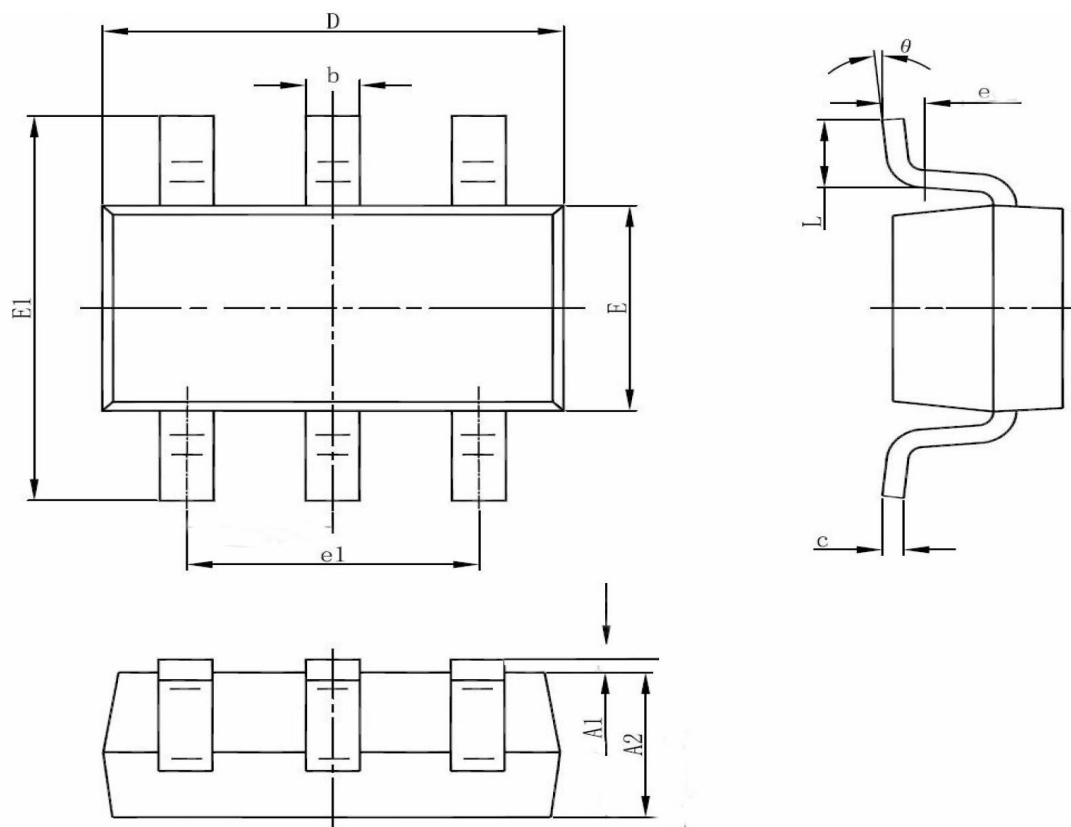
**Figure 10. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



## SOT-23-6 Package Information



DIM	MIN	NOM	MAX
A1	0.00	-	0.10
A2	1.00	1.10	1.20
b	0.30	0.40	0.50
c	0.10	0.15	0.20
D	2.80	2.90	3.00
E	1.50	1.60	1.70
E1	2.60	2.80	3.00
e	0.2GAUGE PLANE		
e1	-	1.90	-
L	0.30	0.45	0.60
θ	0°	-	8°
All Dimensions in mm			