

**Description**

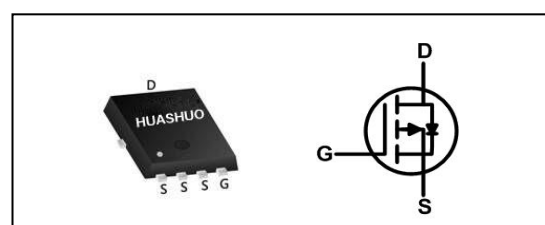
The HSBA90P02 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The HSBA90P02 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

**Product Summary**

$V_{DS}$	-20	V
$R_{DS(ON),typ}$	1.9	m $\Omega$
$I_D$	-90	A

**PRPAK5X6 Pin Configuration**

**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ -10V^{1,6}$	-90	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ -10V^{1,6}$	-78	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-530	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	370	mJ
$I_{AS}$	Avalanche Current	-40	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation <sup>4</sup>	150	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup> ( $t \leq 10S$ )	---	18	$^\circ\text{C/W}$
	Thermal Resistance Junction-ambient <sup>1</sup> (Steady State)	---	60	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-case <sup>1</sup>	---	0.9	$^\circ\text{C/W}$

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-20	---	---	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-20A	---	1.9	2.5	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-20A	---	2.7	3.5	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-0.5	---	-1.0	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	-1	uA
		V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	-5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ± 12V, V <sub>DS</sub> =0V	---	---	± 100	nA
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-20A	---	170	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	2.2	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	3.3	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-20V, V <sub>GS</sub> =-10V, R <sub>G</sub> =3Ω, I <sub>D</sub> =-20A	---	17	---	ns
T <sub>r</sub>	Rise Time		---	4.1	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	25	---	
T <sub>f</sub>	Fall Time		---	33	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V, f=1MHz	---	10211	---	pF
C <sub>oss</sub>	Output Capacitance		---	1280	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	1300	---	

**Diode Characteristics**

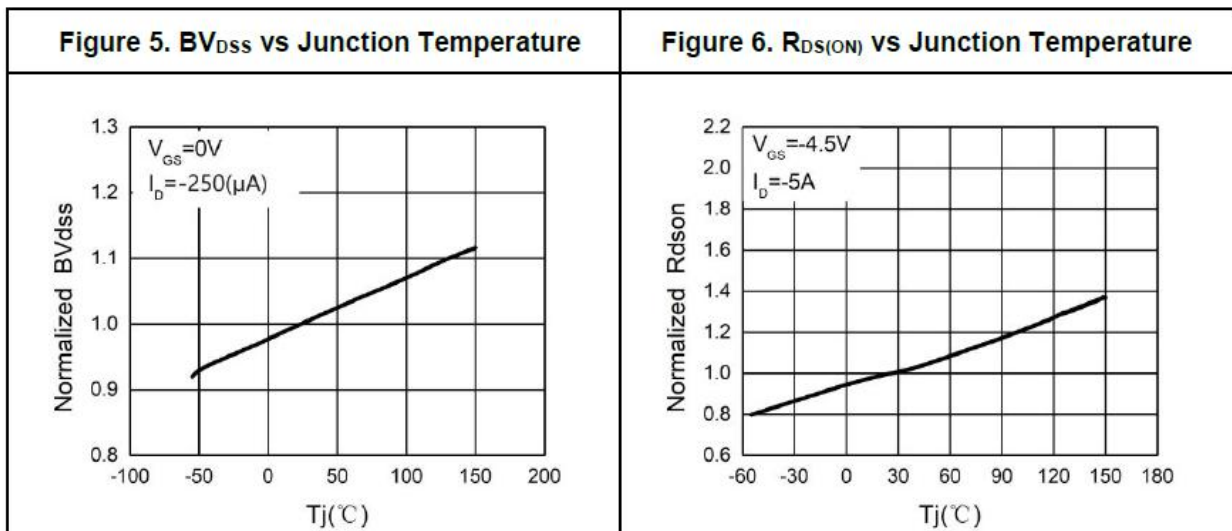
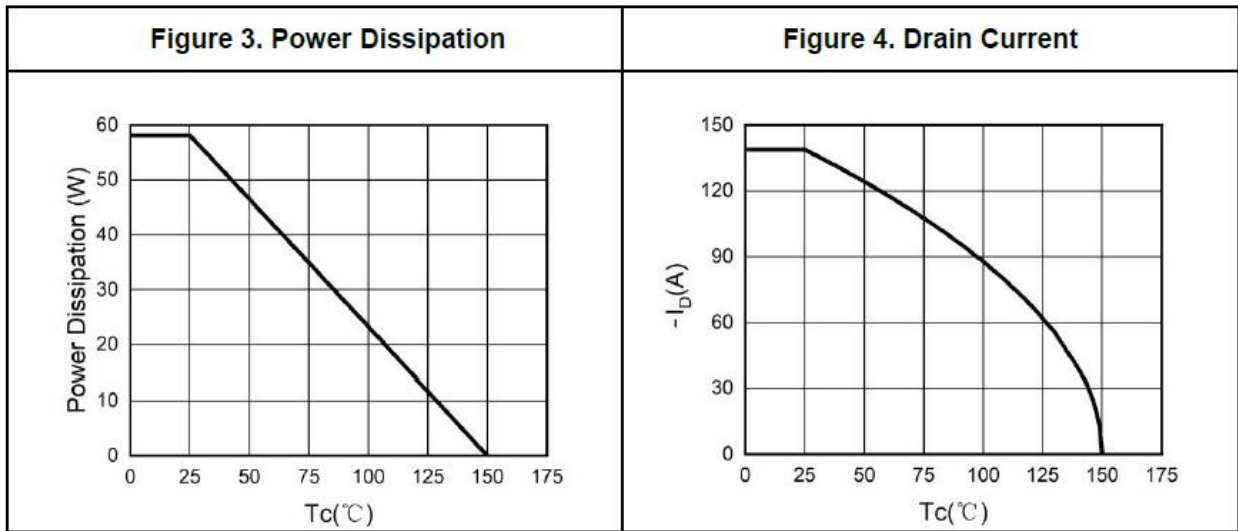
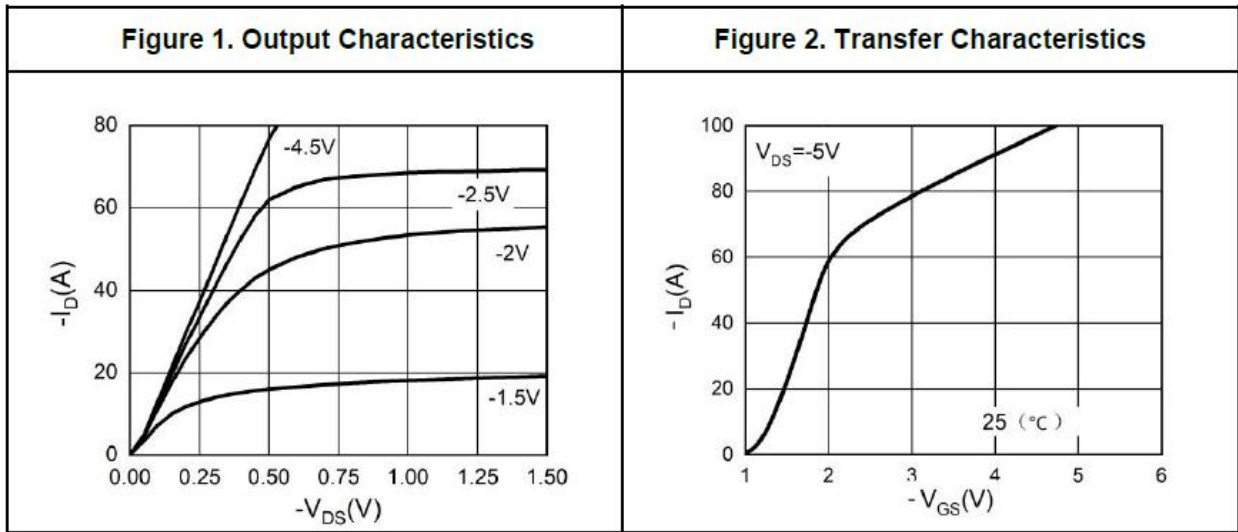
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-90	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =-20A, T <sub>J</sub> =25°C	---	---	-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =-20A, di/dt=100A/μs, T <sub>J</sub> =25°C	---	68	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge		---	49	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V<sub>DD</sub>=-15V, V<sub>GS</sub>=-4.5V, L=0.5mH, I<sub>AS</sub>=-40A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation
- 6.The maximum current rating is package limited.

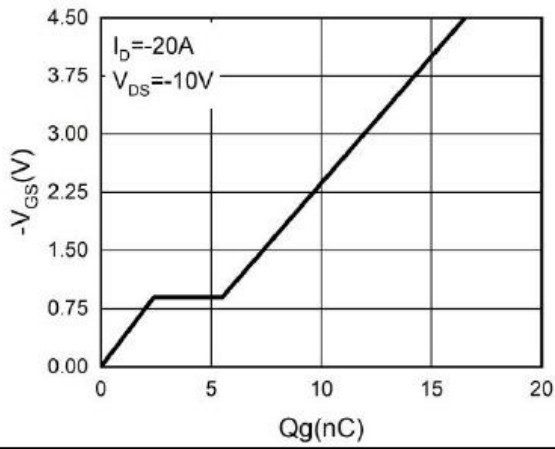


**Typical Characteristics**

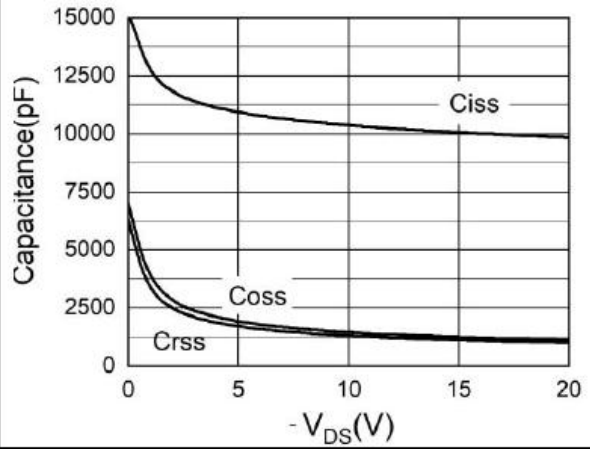




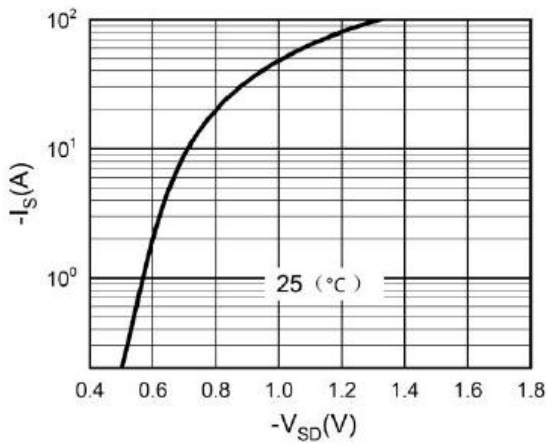
**Figure 7. Gate Charge Waveforms**



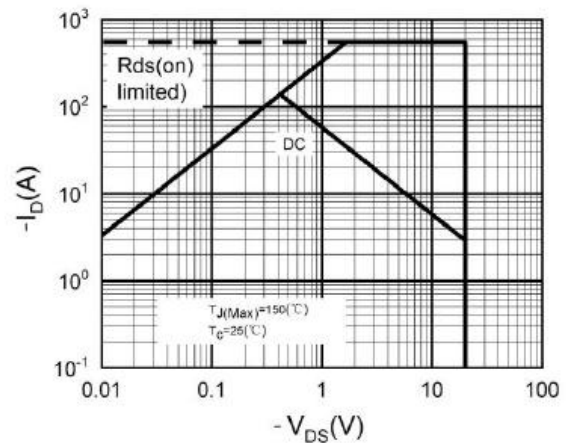
**Figure 8. Capacitance**



**Figure 9. Body-Diode Characteristics**

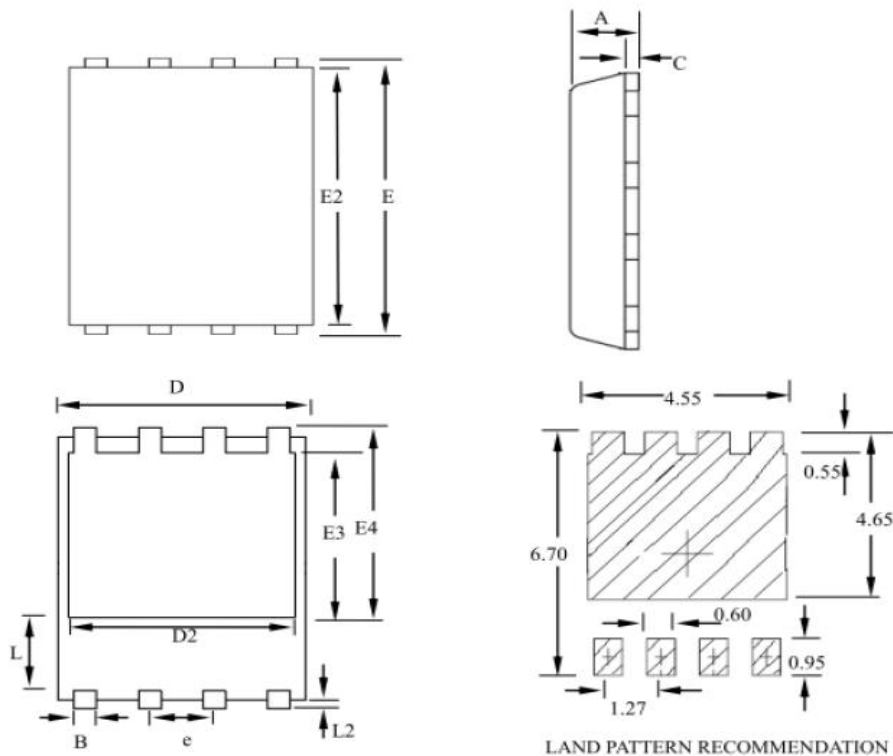


**Figure 10. Maximum Safe Operating Area**



## Ordering Information

Part Number	Package code	Packaging
HSBA90P02	PRPAK5*6	3000/Tape&Reel



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	--	1.20	0.031	--	0.047
B	0.30	--	0.51	0.012	--	0.020
C	0.15	--	0.35	0.006	--	0.014
D	4.80	--	5.30	0.189	--	0.209
D2	3.61	--	4.35	0.142	--	0.171
E	5.90	--	6.35	0.232	--	0.250
E2	5.42	--	5.90	0.213	--	0.232
E3	3.23	--	3.90	0.127	--	0.154
E4	3.69	--	4.55	0.145	--	0.179
L	0.61	--	1.80	0.024	--	0.071
L2	0.05	--	0.36	0.002	--	0.014
e	--	1.27	--	--	0.050	--