

# N-Channel 700V (D-S) Super Junction Power MOSFET

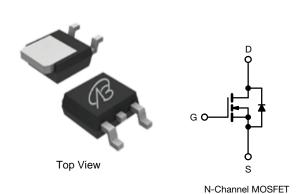
PRODUCT SUMMARY					
V <sub>DS</sub> (V) at T <sub>J</sub> max.	700				
R <sub>DS(on)</sub> typ. (Ω) at 25 °C	V <sub>GS</sub> = 10 V	0.260			

#### **FEATURES**

- Low figure-of-merit (FOM) Ron x Qa
- Low input capacitance (Ciss)
- Reduced switching and conduction losses
- Ultra low gate charge (Qg)
- Avalanche energy rated (UIS)



#### TO-252



#### **APPLICATIONS**

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- Lighting
  - High-intensity discharge (HID)
  - Fluorescent ballast lighting
- Industrial
  - Welding
  - Induction heating
  - Motor drives
  - Battery chargers
- Renewable energy
- Solar (PV inverters)

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_C = 25$ °C, unless otherwise noted)					
PARAMETER			SYMBOL	LIMIT	UNIT
Drain-source voltage	Drain-source voltage			700	V
Gate-source voltage			$V_{GS}$	± 30	V
Continuous drain current (T. – 150 °C)	V at 10 V	$T_{\rm C} = 25  ^{\circ}{\rm C}$ $T_{\rm C} = 100  ^{\circ}{\rm C}$	- I <sub>D</sub>	15	
Continuous drain current (T <sub>J</sub> = 150 °C)	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 100 °C		9	Α
Pulsed drain current <sup>a</sup>			I <sub>DM</sub>	45	
Linear derating factor			1.7	W/°C	
Single pulse avalanche energy b		E <sub>AS</sub>	340	mJ	
Maximum power dissipation		$P_{D}$	180	W	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	
Drain-source voltage slope	T <sub>J</sub> = 125 °C		dV/dt	50	V/ns
Reverse diode dV/dt <sup>d</sup>		uv/ut	5.1	V/IIS	
Soldering recommendations (peak temperature) c	For	10 s		260	°C

#### Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature
- b.  $V_{DD}$  = 100 V, starting  $T_J$  = 25 °C, L = 30 mH,  $R_g$  = 25  $\Omega$ ,  $I_{AS}$  = 7 A
- c. 1.6 mm from case
- d.  $I_{SD} \le I_D$ ,  $dI/dt = 100 \text{ A/}\mu\text{s}$ , starting  $T_J = 25 \,^{\circ}\text{C}$

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THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYP.	MAX.	UNIT		
Maximum junction-to-ambient	R <sub>thJA</sub>	=	62	°C/W		
Maximum junction-to-case (drain)	R <sub>thJC</sub>	-	0.85	G/ <b>VV</b>		

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static		-					
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		700	-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	Referenc	e to 25 °C, I <sub>D</sub> = 1 mA	-	1.08	-	V/°C
Gate-source threshold Voltage (N)	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		-	4.0	V
Oalana and Indiana	_	$V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA
Gate-source leakage	I <sub>GSS</sub>		V <sub>GS</sub> = ± 30 V	-	-	± 1	μΑ
		V <sub>DS</sub> =	= 700 V, V <sub>GS</sub> = 0 V	-	-	1	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 560 \	/, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	-	-	10	μA
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> =5A	-	0.260	-	Ω
Forward transconductance	9 <sub>fs</sub>	$V_{DS}$	= 30 V, I <sub>D</sub> = 5 A	-	8.7	-	S
Dynamic		•					<u> </u>
Input capacitance	C <sub>iss</sub>		$V_{GS} = 0 V$ ,	_	1900	-	
Output capacitance	C <sub>oss</sub>	1	$V_{DS} = 0 V,$ $V_{DS} = 100 V,$	-	51	-	-
Reverse transfer capacitance	C <sub>rss</sub>	1	f = 1 MHz		12	-	pF
Effective output capacitance, energy related <sup>a</sup>	C <sub>o(er)</sub>			-	46	-	
Effective output capacitance, time related <sup>b</sup>	$C_{o(tr)}$	V <sub>DS</sub> = 0 \	$V_{DS} = 0 \text{ V to } 480 \text{ V}, V_{GS} = 0 \text{ V}$		205	-	
Total gate charge	Qg			-	25	-	
Gate-source charge	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$V_{GS} = 10 \text{ V}$ $I_D = 5 \text{ A}, V_{DS} = 480 \text{ V}$		8	-	nC
Gate-drain charge	Q <sub>gd</sub>			-	10	-	1
Turn-on delay time	t <sub>d(on)</sub>			-	12	24	
Rise time	t <sub>r</sub>	V	$V_{DD} = 480 \text{ V}, I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}, R_q = 9.1 \Omega$		14	23	ns
Turn-off delay time	t <sub>d(off)</sub>	V <sub>GS</sub>			61	110	
Fall time	t <sub>f</sub>		J	-	16	1	1
Gate input resistance	$R_g$	f = 1	f = 1 MHz, open drain		0.7	1.4	Ω
Drain-Source Body Diode Characteristic	s	-					
Continuous source-drain diode current	I <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		-	-	15	
Pulsed diode forward current	I <sub>SM</sub>			-	-	45	A
Diode forward voltage	V <sub>SD</sub>	T <sub>J</sub> = 25 °C, I <sub>S</sub> = 5 A, V <sub>GS</sub> = 0 V		-	-	1.2	V
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C, I <sub>F</sub> = I <sub>S</sub> = 5 A, dl/dt = 100 A/μs, V <sub>R</sub> = 25 V		-	416	832	ns
Reverse recovery charge	Q <sub>rr</sub>			_	6.4	12.8	μC
Reverse recovery current	I <sub>RRM</sub>			_	27	-	A

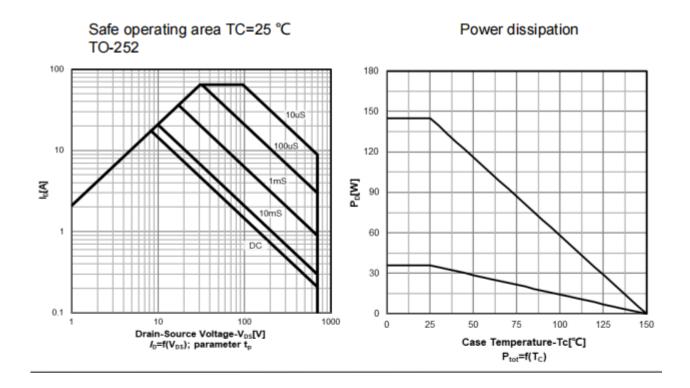
#### Notes

- a.  $C_{oss(er)}$  is a fixed capacitance that gives the same energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 80 %  $V_{DSS}$  b.  $C_{oss(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 % to 80 %  $V_{DSS}$

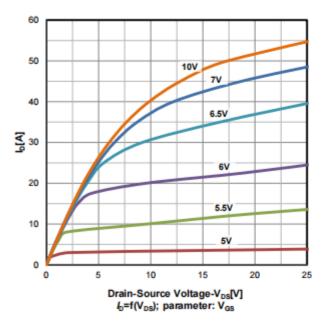
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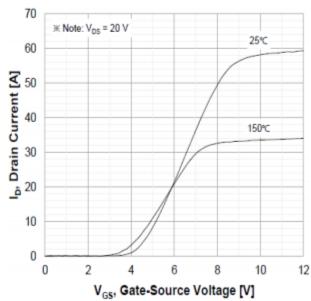
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Typ. output characteristics  $T_i$ =25  $^{\circ}C$ 



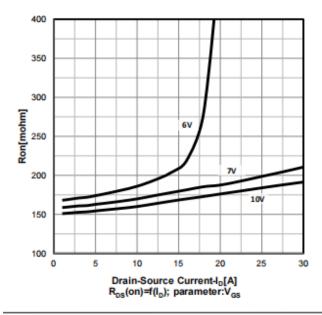
## Transfer characteristics



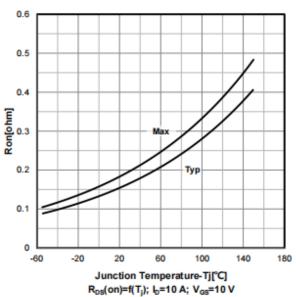
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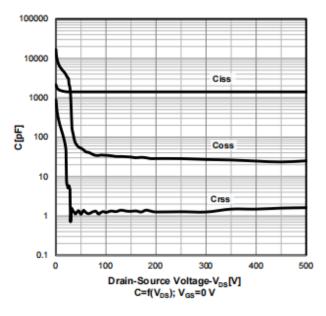
Typ. drain-source on-state resistance



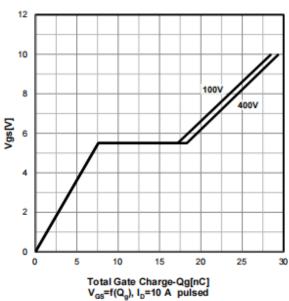
On-resistance vs temperature



Typ. capacitances



Typ. gate charge characteristics

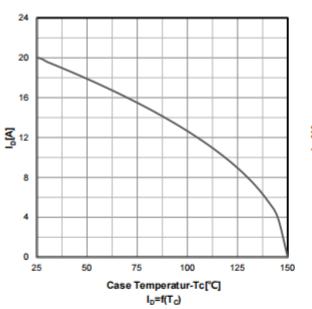


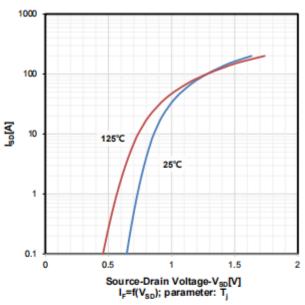
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## Drain current vs temperature

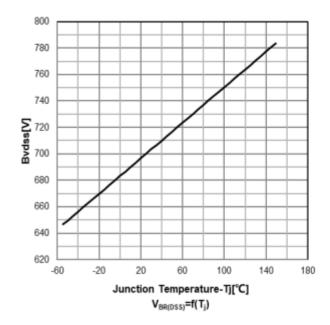
## Forward characteristics of reverse diode

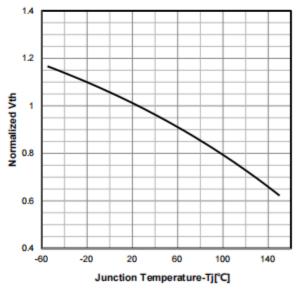




## Drain-source breakdown voltage

## Normalized V<sub>GS(th)</sub> characteristics

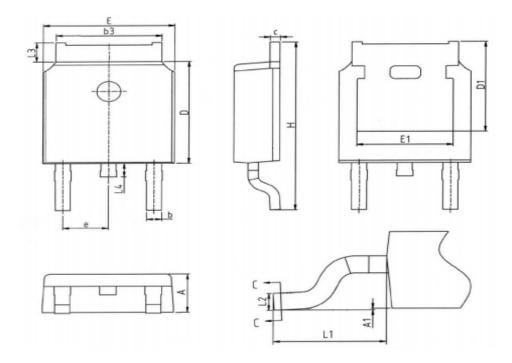




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Package Outline: TO 252



#### COMMON DIMENSIONS

SYMBOL	UNIT(mm)				
SIMBOL	MIN	NOM	MAX		
A	2.20	2.30	2.40		
A1	0.00	-	0.127		
b	0.66	0.78	0.90		
b3	5.16	5.31	5.46		
c	0.43	0.53	0.63		
D	5.98	6.10	6.22		
D1	5.30REF				
E	6.40	6.60	6.75		
E1	4.63	-	-		
e	2.286BSC				
H	9.40	10.10	10.50		
L1	2.90REF				
L2	0.51BSC				
L3	0.88	1.08	1.28		
L4	0.50	0.80	1.00		

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