

Complete Data Sheet available via web, Harris' home page: <http://www.semi.harris.com> or via Harris AnswerFAX, see Section 17

August 1997

Features

- Low Power Consumption
- Break-Before-Make Switching (Typ)
 - t_{OFF} 130ns
 - t_{ON} 150ns
- TTL, CMOS Compatible
- Low $r_{DS(ON)}$ $\leq 50\Omega$
- Single Supply Operation
- True Second Source

Description

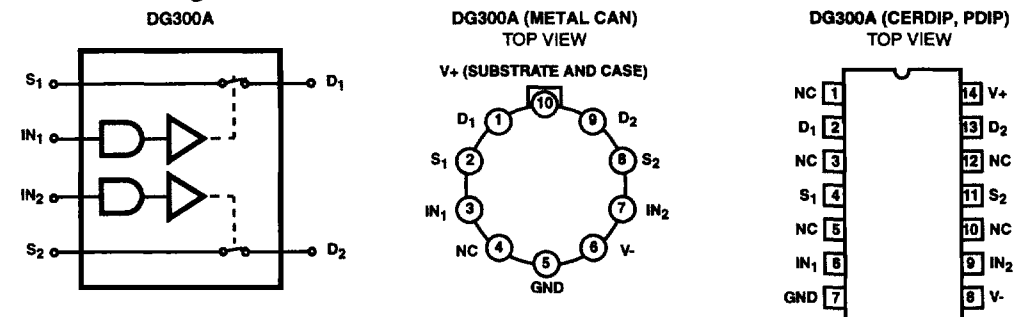
The DG300A through DG303A family of monolithic CMOS switches are truly compatible second source of the original manufacturer. The switches are latch-proof and are designed to block signals up to $30V_{P-P}$ when OFF. Featuring low leakage and low power consumption, these switches are ideally suited for precision application in instrumentation, communication, data acquisition and battery powered applications. Other key features include Break-Before-Make switching, TTL and CMOS compatibility, and low ON resistance. Single supply operation (for positive switch voltages) is possible by connecting V- to 0V.

Ordering Information

PART NUMBER	TEMP. RANGE (°C)	PACKAGE	PKG. NO.
DG300AAK	-55 to 125	14 Ld CERDIP	F14.3
DG301AAK	-55 to 125	14 Ld CERDIP	F14.3
DG302AAK	-55 to 125	14 Ld CERDIP	F14.3
DG303AAK	-55 to 125	14 Ld CERDIP	F14.3
DG300ABK	-25 to 85	14 Ld CERDIP	F14.3
DG301ABK	-25 to 85	14 Ld CERDIP	F14.3
DG302ABK	-25 to 85	14 Ld CERDIP	F14.3
DG303ABK	-25 to 85	14 Ld CERDIP	F14.3
DG300ACK	0 to 70	14 Ld CERDIP	F14.3
DG301ACK	0 to 70	14 Ld CERDIP	F14.3
DG303ACK	0 to 70	14 Ld CERDIP	F14.3
DG300ACJ	0 to 70	14 Ld PDIP	E14.3

PART NUMBER	TEMP. RANGE (°C)	PACKAGE	PKG. NO.
DG301ACJ	0 to 70	14 Ld PDIP	E14.3
DG302ACJ	0 to 70	14 Ld PDIP	E14.3
DG303ACJ	0 to 70	14 Ld PDIP	E14.3
DG300AAA	-55 to 125	10 Pin Metal Can	T10.B
DG301AAA	-55 to 125	10 Pin Metal Can	T10.B
DG303ACY	0 to 70	16 Ld SOIC	M16.3
DG300AAA/883B	-55 to 125	10 Pin Metal Can	T10.B
DG300AAK/883B	-55 to 125	14 Ld CERDIP	F14.3
DG301AAA/883B	-55 to 125	10 Pin Metal Can	T10.B
DG301AAK/883B	-55 to 125	14 Ld CERDIP	F14.3
DG302AAK/883B	-55 to 125	14 Ld CERDIP	F14.3
DG303AAK/883B	-55 to 125	14 Ld CERDIP	F14.3

Functional Diagrams and Pinouts



TRUTH TABLE

LOGIC	SWITCH
0	OFF
1	ON

Logic "0" $\leq 0.8V$, Logic "1" $\geq 4.0V$; Two SPST switches per package (switches shown for Logic "1" input)