# **BLP7G10S-160P**

# Power LDMOS transistor

**AMMPLEON** 

Rev. 1 — 30 August 2018

Product data sheet

#### **Product profile** 1.

### 1.1 General description

160 W LDMOS power transistor for base station applications at frequencies from 600 MHz to 960 MHz.

#### Table 1. Typical performance

RF performance at  $T_h$  = 25 °C in a common source test circuit.

Test signal	f	V <sub>DS</sub>	I <sub>Dq</sub>	P <sub>L(AV)</sub>	P <sub>L(1dB)</sub>	Gp	η <sub>D</sub>
	(MHz)	(V)	(mA)	(W)	(W)	(dB)	(%)
pulsed, class-AB	860	28	100	-	160	20	62

#### 1.2 Features and benefits

- Integrated ESD protection
- Excellent ruggedness
- High power gain
- High efficiency
- Excellent reliability
- Easy power control
- For RoHS compliance see the product details on the Ampleon website

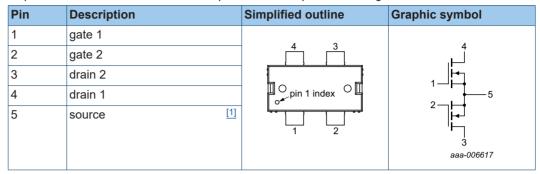
### 1.3 Applications

■ RF power amplifiers for base stations and multi carrier applications in the 600 MHz to 960 MHz frequency range

## 2. Pinning information

Table 2. Pinning

All pins must be connected for correct operation and to prevent damage to the device.



[1] Connected to flange.

# 3. Ordering information

Table 3. Ordering information

Type number	Package	ackage			
	Name	Name Description			
BLP7G10S-160P	HSOP4F	plastic, heatsink small outline package; 4 leads (flat)	SOT1223-2		

# 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage		-	65	V
$V_{GS}$	gate-source voltage		-6	+11	V
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature	[1]	-	225	°C

<sup>[1]</sup> Continuous use at maximum temperature will affect the reliability, for details refer to the online MTF calculator

### 5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$R_{\text{th(j-case)}}$	thermal resistance from junction to case	$T_{case} = 80  ^{\circ}C; P_{L} = 160  W  \frac{[1]}{}$	0.5	K/W

[1]  $R_{th(j-case)}$  is measured under RF conditions.

#### **Characteristics** 6.

#### **DC** characteristics Table 6.

 $T_i$  = 25 °C per section; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 0.9 \text{ mA}$	65	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 90 mA	1.5	1.86	2.3	V
I <sub>DSS</sub>	drain leakage current	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 28 V	-1.4	-	+1.4	μΑ
I <sub>DSX</sub>	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 V;$ $V_{DS} = 10 V$	15	16	-	А
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 11 V; V <sub>DS</sub> = 0 V	-	-	140	nA
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 3.15 A	-	6	-	S
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ $I_D = 3.15 A$	-	0.2	-	Ω

#### **AC** characteristics Table 7.

 $T_i$  = 25 °C per section; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
C <sub>iss</sub>	input capacitance	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 28 V; f = 1 MHz	-	79	-	pF
Coss	output capacitance	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 28 V; f = 1 MHz	-	32	-	pF
C <sub>rs</sub>	feedback capacitance	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 28 V; f = 1 MHz	-	1.5	-	pF

#### **RF** characteristics

Test signal: pulsed CW; f = 860 MHz; RF performance measured at  $V_{DS} = 28$  V;  $I_{Dq} = 100$  mA;  $T_{case}$  = 25 °C; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Gp	power gain	P <sub>L(1dB)</sub> = 160 W	16.5	19.4	-	dB
$\eta_{D}$	drain efficiency	P <sub>L(1dB)</sub> = 160 W	57.5	59.7	-	%

#### **Test information 7**.

### 7.1 Ruggedness in class-AB operation

The BLP7G10S-160P is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS}$  = 28 V; f = 860 MHz at rated load power.

### 7.2 Demo circuit information

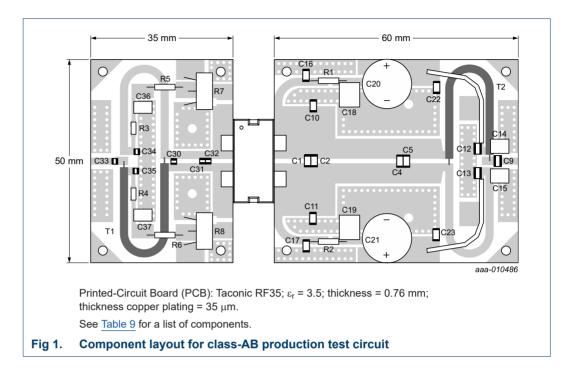


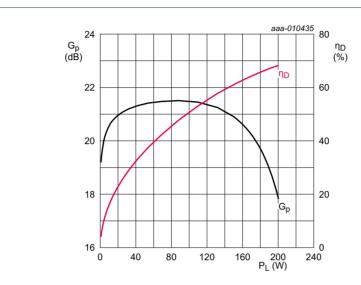
Table 9. List of components

For test circuit see Figure 1.

Component	Description	Value	Remarks
C1, C2	multilayer ceramic chip capacitor	5.6 pF	ATC800B
C4, C5	C5 multilayer ceramic chip capacitor		ATC800B
C9	multilayer ceramic chip capacitor	100 pF	ATC180R
C10, C11	multilayer ceramic chip capacitor	10 pF	ATC800B
C12, C13	multilayer ceramic chip capacitor	100 pF	ATC180R
C14, C15	multilayer ceramic chip capacitor	4.7 μF, 50 V	TDK
C16, C17	multilayer ceramic chip capacitor	100 pF	ATC800B
C18, C19	multilayer ceramic chip capacitor	10 μF	TDK
C20, C21	electrolytic capacitor	470 μF, 63 V	
C22, C23	multilayer ceramic chip capacitor	1 nF	ATC800B
C30	multilayer ceramic chip capacitor	33 pF	ATC800A
C31	multilayer ceramic chip capacitor	10 pF	ATC800A
C32	multilayer ceramic chip capacitor	11 pF	ATC800A
C33, C34, C35	multilayer ceramic chip capacitor	91 pF	ATC800A
C36, C37	electrolytic capacitor	4.7 μF, 50 V	
T1	semi rigid coax	25 Ω	Micro-Coax: UT-090C-25
T2	semi rigid coax	25 Ω	Micro-Coax: UT-090C-25
R1, R2	resistor	10 Ω	Vishay: MRS25
R3, R4	resistor	5.6 Ω	SMD 1206
R5, R6	resistor	100 Ω	Vishay: MRS25
R7, R8	potentiometer	10 kΩ	Bourns

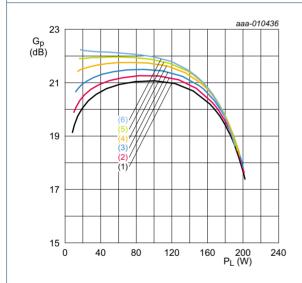
### 7.3 Graphical data

#### 7.3.1 1-Tone pulsed



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 200 mA; f = 860 MHz.

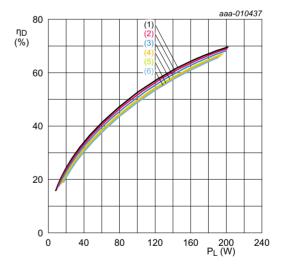
#### Fig 2. Power gain and drain efficiency as function of output power; typical values



 $V_{DS}$  = 28 V; f = 860 MHz.

- (1)  $I_{Dq} = 50 \text{ mA}$
- (2)  $I_{Dq} = 100 \text{ mA}$
- (3)  $I_{Dq} = 200 \text{ mA}$
- (4)  $I_{Dq} = 400 \text{ mA}$
- (5)  $I_{Dq} = 600 \text{ mA}$
- (6)  $I_{Dq} = 800 \text{ mA}$

Fig 3. Power gain as a function of output power; typical values

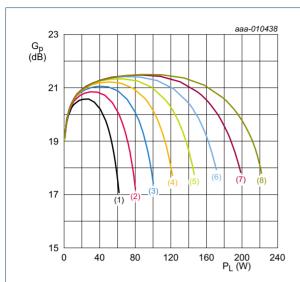


V<sub>DS</sub> = 28 V; f = 860 MHz.

- (1)  $I_{Dq} = 50 \text{ mA}$
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(6)  $I_{Dq} = 800 \text{ mA}$ 

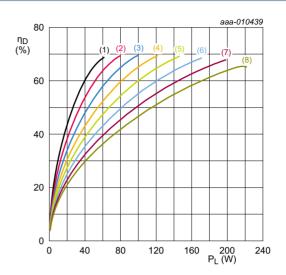
Fig 4. Drain efficiency as a function of output power; typical values



 $I_{Dq} = 200 \text{ mA}$ ; f = 860 MHz.

- (1)  $V_{DS} = 16 V$
- (2)  $V_{DS} = 18 V$
- (3)  $V_{DS} = 20 \text{ V}$
- (4)  $V_{DS} = 22 V$
- (5)  $V_{DS} = 24 \text{ V}$
- (6)  $V_{DS} = 26 \text{ V}$
- (7)  $V_{DS} = 28 \text{ V}$
- (8)  $V_{DS} = 30 \text{ V}$

Fig 5. Power gain as a function of output power; typical values



 $I_{Dq} = 200 \text{ mA}$ ; f = 860 MHz.

- (1)  $V_{DS} = 16 V$
- (2)  $V_{DS} = 18 \text{ V}$
- (3)  $V_{DS} = 20 \text{ V}$
- (4)  $V_{DS} = 22 V$
- (5)  $V_{DS} = 24 V$
- (6)  $V_{DS} = 26 \text{ V}$
- (7)  $V_{DS} = 28 V$
- (8)  $V_{DS} = 30 \text{ V}$

Fig 6. Drain efficiency as a function of output power; typical values

# 8. Package outline

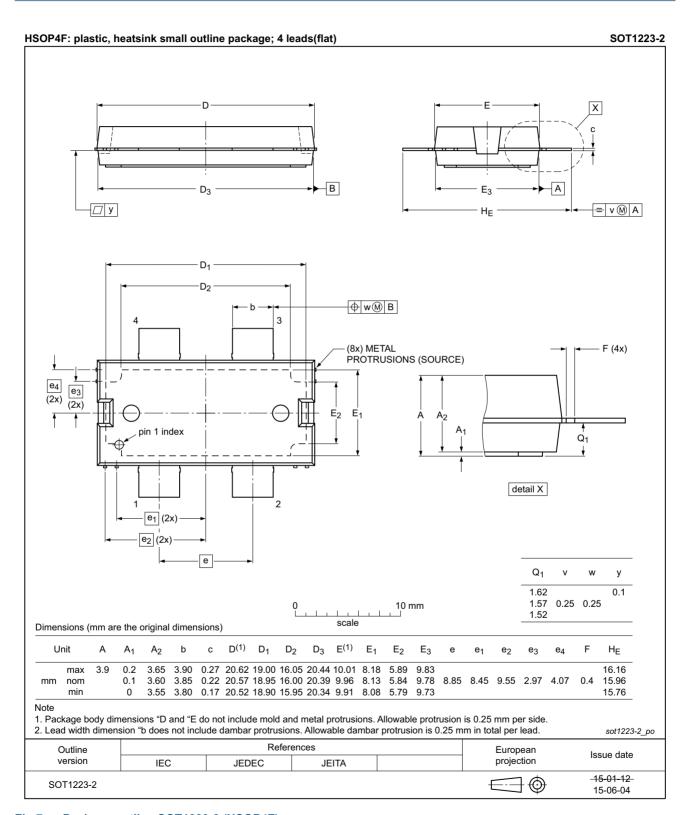


Fig 7. Package outline SOT1223-2 (HSOP4F)

# 9. Handling information

#### **CAUTION**



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

Table 10. ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C2A [1]
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	2 [2]

- [1] CDM classification C2A is granted to any part that passes after exposure to an ESD pulse of 500 V.
- [2] HBM classification 2 is granted to any part that passes after exposure to an ESD pulse of 2000 V.

### 10. Abbreviations

Table 11. Abbreviations

Acronym	Description		
CW	Continuous Wave		
ESD	ElectroStatic Discharge		
HF	High Frequency		
LDMOS	Laterally Diffused Metal-Oxide Semiconductor		
MTF	Median Time to Failure		
RoHS	Restriction of Hazardous Substances		
SMD	Surface Mounted Device		
VSWR	Voltage Standing Wave Ratio		

# 11. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLP7G10S-160P v.1	20180830	Product data sheet	-	-

## 12. Legal information

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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# **AMPLEON**

# **BLP7G10S-160P**

#### **Power LDMOS transistor**

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