

## **Rochester Electronics Manufactured Components**

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceed the OCM data sheet.

## **Quality Overview**

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-35835
  - Class Q Military
  - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
  - Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

# TYPES SN54H11, SN54LS11, SN54S11, SN74H11, SN74LS11, SN74S11 TRIPLE 3-INPUT POSITIVE-AND GATES

REVISED APRIL 1985

- Package Options Include Both Plastic and Ceramic Chip Carriers in Addition to Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

## description

These devices contain three independent 3-input AND gates.

The SN54H11, SN54LS11, SN54S11 are characterized for operation over the full military temperature range of  $-55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . The SN74H11, SN74LS11, and SN74S11, are characterized for operation from  $0^{\circ}\text{C}$  to  $70^{\circ}\text{C}$ .

FUNCTION TABLE (each gate)

INPUTS			OUTPUT
A	B	C	Y
H	H	H	H
L	X	X	L
X	L	X	L
X	X	L	L

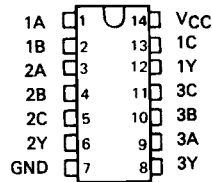
## logic diagram (each gate)



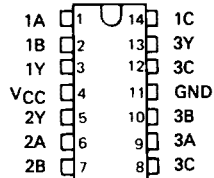
## positive logic

$$Y = A \cdot B \cdot C \text{ or } Y = \overline{\overline{A} + \overline{B} + \overline{C}}$$

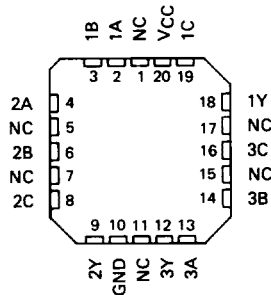
SN54H11 ... J PACKAGE  
SN54LS11, SN54S11 ... J OR W PACKAGE  
SN74H11 ... J OR N PACKAGE  
SN74LS11, SN74S11 ... D, J OR N PACKAGE  
(TOP VIEW)



SN54H11 ... W PACKAGE  
(TOP VIEW)



SN54LS11, SN54S11 ... FK PACKAGE  
SN74LS11, SN74S11 ... FN PACKAGE  
(TOP VIEW)



NC - No internal connection

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## PRODUCTION DATA

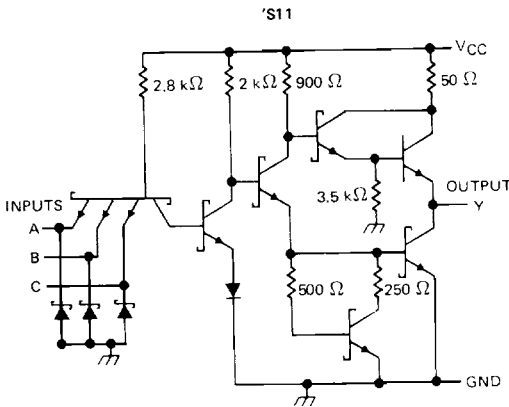
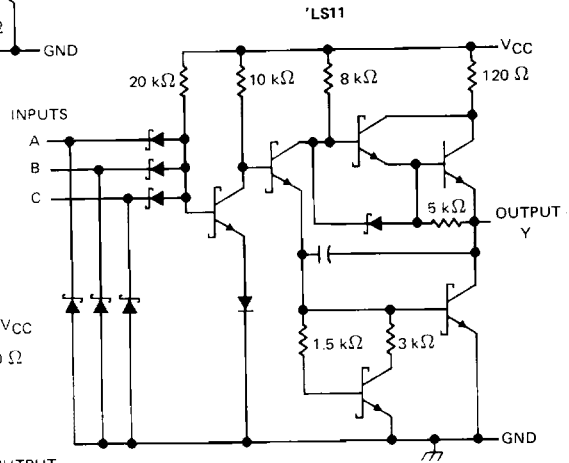
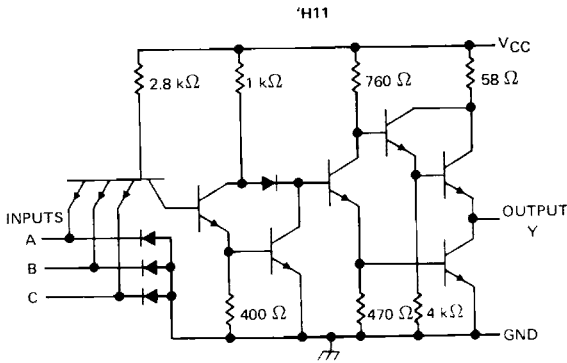
This document contains information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

TEXAS  
INSTRUMENTS

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**TYPES SN54H11, SN54LS11, SN54S11  
SN74H11, SN74LS11, SN74S11  
TRIPLE 3-INPUT POSITIVE-AND GATES**

schematics (each gate)



Resistor values shown are nominal

**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)**

Supply voltage, $V_{CC}$ (see Note 1)	7 V
Input voltage: 'H11, 'S11	5.5 V
'LS11	7 V
Operating free-air temperature: SN54'	-55°C to 125°C
SN74'	0°C to 70°C
Storage temperature range	-65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.

# TYPES SN54H11, SN74H11 TRIPLE 3-INPUT POSITIVE-AND GATES

## recommended operating conditions

	SN54H11			SN74H11			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
$V_{CC}$ Supply voltage	4.5	5	5.5	4.75	5	5.25	V
$V_{IH}$ High-level input voltage	2			2			V
$V_{IL}$ Low-level input voltage			0.8			0.8	V
$I_{OH}$ High-level output current			-0.5			-0.5	mA
$I_{OL}$ Low-level output current			20			20	mA
$T_A$ Operating free-air temperature	-55		125	0		70	°C

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS †	SN54H11		SN74H11		UNIT		
		MIN	TYP ‡	MAX	MIN		TYP ‡	MAX
$V_{IK}$	$V_{CC} = \text{MIN}, I_I = -8 \text{ mA}$			-1.5		-1.5	V	
$V_{OH}$	$V_{CC} = \text{MIN}, V_{IH} = 2 \text{ V}, I_{OH} = -0.5 \text{ mA}$	2.4	3.4	2.4	3.4		V	
$V_{OL}$	$V_{CC} = \text{MIN}, V_{IL} = 0.8 \text{ V}, I_{OL} = 20 \text{ mA}$	0.2	0.4	0.2	0.4		V	
$I_I$	$V_{CC} = \text{MAX}, V_I = 5.5 \text{ V}$			1		1	mA	
$I_{IH}$	$V_{CC} = \text{MAX}, V_I = 2.4 \text{ V}$			50		50	µA	
$I_{IL}$	$V_{CC} = \text{MAX}, V_I = 0.4 \text{ V}$			-2		-2	mA	
$I_{OS} §$	$V_{CC} = \text{MAX}$	-40		-100	-40	-100	mA	
$I_{CCH}$	$V_{CC} = \text{MAX}, V_I = 4.5 \text{ V}$		18	30		18	30	mA
$I_{CCL}$	$V_{CC} = \text{MAX}, V_I = 0 \text{ V}$		30	48		30	48	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at  $V_{CC} = 5 \text{ V}, T_A = 25^\circ\text{C}$ .

§ Not more than one output should be shorted at a time, and the duration of the output short circuit should not exceed one second.

## switching characteristics, $V_{CC} = 5 \text{ V}, T_A = 25^\circ\text{C}$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$t_{PLH}$	A, B or C	Y	$R_L = 280 \Omega, C_L = 25 \text{ pF}$		7.6	12	ns
$t_{PHL}$					8.8	12	ns

NOTE 2: See General Information Section for load circuits and voltage waveforms.

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# TYPES SN54LS11, SN74LS11

## TRIPLE 3-INPUT POSITIVE-AND GATES

### recommended operating conditions

	SN54LS11			SN74LS11			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V <sub>CC</sub> Supply voltage	4.5	5	5.5	4.75	5	5.25	V
V <sub>IH</sub> High-level input voltage	2			2			V
V <sub>IL</sub> Low-level input voltage			0.7			0.8	V
I <sub>OH</sub> High-level output current			-0.4			-0.4	mA
I <sub>OL</sub> Low-level output current			4			8	mA
T <sub>A</sub> Operating free-air temperature	-55		125	0		70	°C

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS †	SN54LS11		SN74LS11		UNIT		
		MIN	TYP ‡	MAX	MIN		TYP ‡	MAX
V <sub>IK</sub>	V <sub>CC</sub> = MIN, I <sub>I</sub> = -18 mA			-1.5		-1.5	V	
V <sub>OH</sub>	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2 V, I <sub>OH</sub> = -0.4 mA	2.5	3.4	2.7	3.4		V	
V <sub>OL</sub>	V <sub>CC</sub> = MIN, V <sub>IL</sub> = MAX, I <sub>OL</sub> = 4 mA	0.25	0.4	0.25	0.4		V	
	V <sub>CC</sub> = MIN, V <sub>IL</sub> = MAX, I <sub>OL</sub> = 8 mA			0.35	0.5			
I <sub>I</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 7 V			0.1		0.1	mA	
I <sub>IH</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.7 V			20		20	μA	
I <sub>IL</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.4 V			-0.4		-0.4	mA	
I <sub>OS</sub> §	V <sub>CC</sub> = MAX	-20		-100		-100	mA	
I <sub>CCH</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 4.5 V		1.8	3.6		1.8	3.6	mA
I <sub>CCL</sub>	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0 V		3.3	6.6		3.3	6.6	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C.

§ Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

### switching characteristics, V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS		MIN	TYP	MAX	UNIT
t <sub>PLH</sub>	A, B or C	Y	R <sub>L</sub> = 2 kΩ,	C <sub>L</sub> = 15 pF		8	15	ns
t <sub>PHL</sub>						10	20	ns

NOTE 2: See General Information Section for load circuits and voltage waveforms.

# TYPES SN54S11, SN74S11 TRIPLE 3-INPUT POSITIVE-AND GATES

## recommended operating conditions

	SN54S11			SN74S11			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
$V_{CC}$ Supply voltage	4.5	5	5.5	4.75	5	5.25	V
$V_{IH}$ High-level input voltage	2			2			V
$V_{IL}$ Low-level input voltage	0.8			0.8			V
$I_{OH}$ High-level output current	-1			-1			mA
$I_{OL}$ Low-level output current	20			20			mA
$T_A$ Operating free-air temperature	-55		125	0		70	$^{\circ}$ C

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS †	SN54S11		SN74S11		UNIT
		MIN	TYP ‡	MAX	MIN	
$V_{IK}$	$V_{CC} = \text{MIN}$ , $I_I = -18 \text{ mA}$	-1.2		-1.2		V
$V_{OH}$	$V_{CC} = \text{MIN}$ , $V_{IH} = 2 \text{ V}$ , $I_{OH} = -1 \text{ mA}$	2.5	3.4	2.7	3.4	V
$V_{OL}$	$V_{CC} = \text{MIN}$ , $V_{IL} = 0.8 \text{ V}$ , $I_{OL} = 20 \text{ mA}$	0.5		0.5		V
$I_I$	$V_{CC} = \text{MAX}$ , $V_I = 5.5 \text{ V}$	1		1		mA
$I_{IH}$	$V_{CC} = \text{MAX}$ , $V_I = 2.7 \text{ V}$	50		50		$\mu$ A
$I_{IL}$	$V_{CC} = \text{MAX}$ , $V_I = 0.5 \text{ V}$	-2		-2		mA
$I_{OS} §$	$V_{CC} = \text{MAX}$	-40	-100	-40	-100	mA
$I_{CCH}$	$V_{CC} = \text{MAX}$ , $V_I = 4.5 \text{ V}$	13.5	24	13.5	24	mA
$I_{CCL}$	$V_{CC} = \text{MAX}$ , $V_I = 0 \text{ V}$	24	42	24	42	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ .

§ Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

## switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
$t_{PLH}$	A, B or C	Y	$R_L = 280 \Omega$ , $C_L = 15 \text{ pF}$	4.5	7	ns	
$t_{PHL}$				5	7.5	ns	
$t_{PLH}$			$R_L = 280 \Omega$ , $C_L = 50 \text{ pF}$	6	ns		
$t_{PHL}$				7.5	ns		

NOTE 2: See General Information Section for load circuits and voltage waveforms.

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