

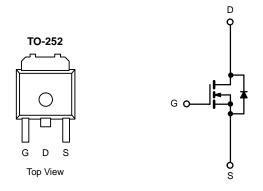
N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{(BR)DSS} (V)	$r_{DS(on)}(\Omega)$	I _D (A)		
100	$0.030 \text{ at V}_{GS} = 10 \text{ V}$	40		
	0.035 at V _{GS} = 4.5 V	37		

FEATURES

- TrenchFET® Power MOSFETS
- 175 °C Junction Temperature
- Low Thermal Resistance Package





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _C = 25 °C, unless oth	erwise noted			
Parameter	-	Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	100	V	
Gate-Source Voltage		V _{GS}	± 20	7 V	
Continuous Drain Current (T _{.1} = 175 °C)	T _C = 25 °C	I-	40		
Continuous Diam Current (1) = 173 C)	T _C = 125 °C	- ID	23		
Pulsed Drain Current	I _{DM}	120	Α		
Avalanche Current		I _{AR}	35		
Repetitive Avalanche Energy ^a	L = 0.1 mH	E _{AR}	61	mJ	
	T _C = 25 °C	D	107 ^b	W	
Maximum Power Dissipation ^a	T _A = 25 °C°	$ P_{D}$	3.75		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Limit	Unit		
Junction-to-Ambient	(PCB Mount) ^c	R _{thJA}	40	°C/W		
Junction-to-Case (Drain)		R _{thJC}	1.4	C/ V V		

Notes:

- a. Duty cycle ≤ 1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{SS} = 0 \text{ V, } I_D = 250 \mu\text{A}$				V
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		3	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
		V _{DS} = 100 ,W _{GS} = 0 V			1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 125 °C			50	μA
		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 175 °C			250	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	75			Α
		V _{GS} = 10 V, I _D = 5 A		0.030		Ω
		$V_{GS} = 4.5 \text{ V}, I_D = 3 \text{ A}$		0.035		
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 5 A, T _J = 125 °C		0.053		
		V _{GS} = 10 V, I _D = 3 A, T _J = 175 °C		0.067		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A	10			S
Dynamic ^b	.					
Input Capacitance	C _{iss}			2600		pF
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		290		
Reverse Transfer Capacitance	C _{rss}			120		
Total Gate Charge ^c	Qg			35	60	nC
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 40 \text{ A}$		11		
Gate-Drain Charge ^c	Q _{gd}			9		
Gate Resistance	R _G			1.7		Ω
Turn-On Delay Time ^c	t _{d(on)}			11	20	
Rise Time ^c	t _r	$V_{DD} = 50 \text{ V}, R_{L} = 1.25 \Omega$		12	20	ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 40 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 2.5 \Omega$		30	45	
Fall Time ^c	t _f			12	20	
Source-Drain Diode Ratings and Cha	racteristics	_C = 25 °C ^b				
Continuous Current	Is				40	
Pulsed Current	I _{SM}				120	Α
Forward Voltage ^a	V _{SD}	I _F = 30 A, V _{GS} = 0 V		1.0	1.5	V
Reverse Recovery Time	t _{rr}			60	100	ns
Peak Reverse Recovery Current	I _{RM(REC)}	$I_F = 30 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$		5	8	Α
Reverse Recovery Charge	Q _{rr}	·		0.15	0.4	μC

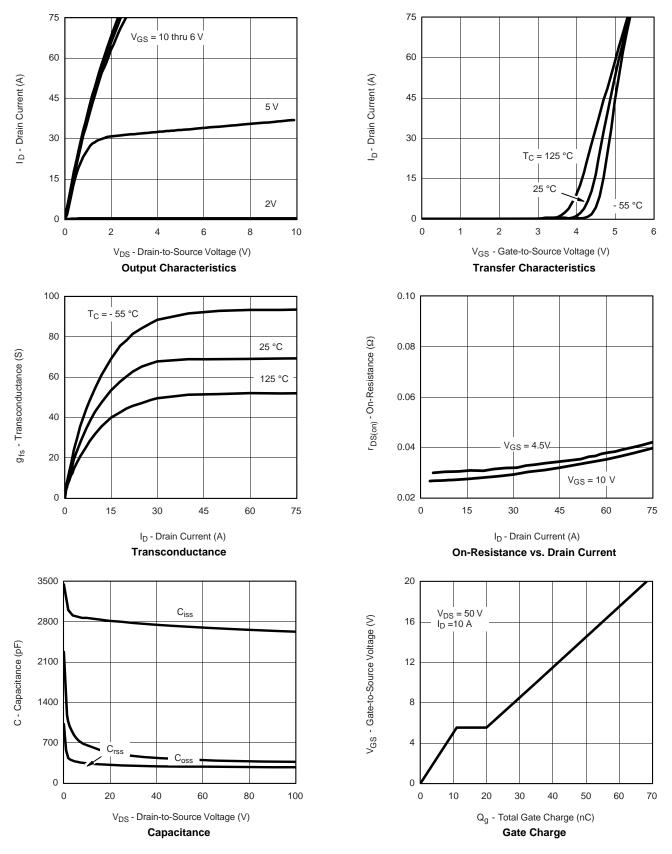
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

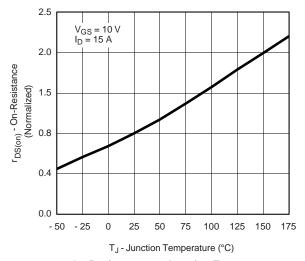


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

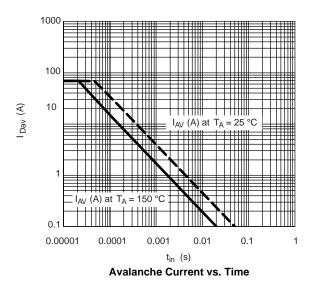




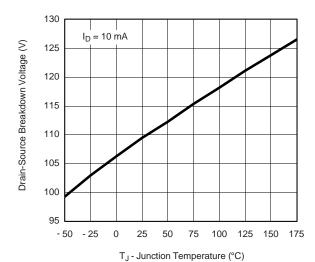
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On-Resistance vs. Junction Temperature



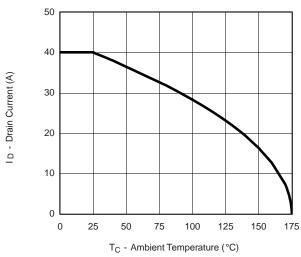
Source-Drain Diode Forward Voltage



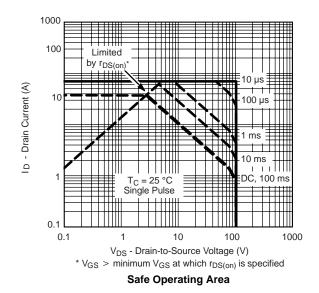
Drain-Source Breakdown Voltage vs. Junction Temperature

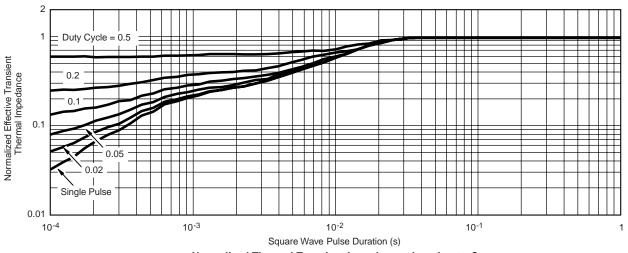


THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature





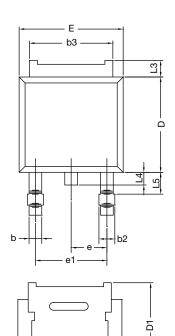
Normalized Thermal Transient Impedance, Junction-to-Case

服务热线:400-655-8788

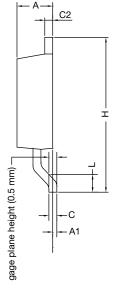
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TO-252AA CASE OUTLINE



E1



	MILLIN	METERS	S INCHES			
DIM.	MIN.	MAX.	MIN.	MAX.		
А	2.18	2.38	0.086	0.094		
A1	-	0.127	-	0.005		
b	0.64	0.88	0.025	0.035		
b2	0.76	1.14	0.030	0.045		
b3	4.95	5.46	0.195	0.215		
С	0.46	0.61	0.018	0.024		
C2	0.46	0.89	0.018	0.035		
D	5.97	6.22	0.235	0.245		
D1	5.21	-	0.205	-		
Е	6.35	6.73	0.250	0.265		
E1	4.32	-	0.170	-		
Н	9.40	10.41	0.370	0.410		
е	2.28	BSC 0.090 BSC		2.28 BSC		BSC
e1	4.56	BSC	0.180	BSC		
L	1.40	1.78	0.055	0.070		
L3	0.89	1.27	0.035	0.050		
L4	-	1.02	-	0.040		
L5	1.14	1.52	0.045	0.060		
FCN: X12-0247-Rev. M. 24-Dec-12						

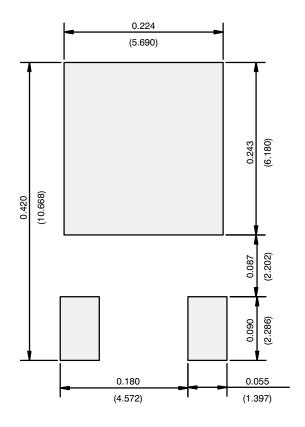
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347

Note

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)



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