

## P-Channel 30-V (D-S) MOSFET

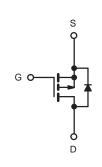
PRODUCT SUMMARY		
V <sub>DS</sub>	-30	V
$R_{DS(on)}$ $V_{GS} = 10$ V	8	mΩ
$R_{DS(on)}$ $V_{GS} = 4.5$ V	11	mΩ
I <sub>D</sub>	-70	А
Configuration	Sin	gle

#### **FEATURES**

- Halogen-free
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested

#### **APPLICATIONS**

- Load Switch
- Notebook Adaptor Switch



P-Channel MOSFET

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 30	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
	T <sub>C</sub> = 25 °C		- 70	
Continuous Drain Current ( $T_1 = 150 \ ^{\circ}C$ )	T <sub>C</sub> = 70 °C		- 55	
Continuous Drain Current (1j = 150°C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 55	
	T <sub>A</sub> = 70 °C		-45	A
Pulsed Drain Current		I <sub>DM</sub> - 200	- 200	A
Orational Design Design Director Orange	T <sub>C</sub> = 25 °C	L.	- 50	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	- 50	
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	- 20	
Single-Pulse Avalanche Energy	L = 0.1 mm	E <sub>AS</sub>	20	mJ
	T <sub>C</sub> = 25 °C		50	
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P	32	w
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	7 <sup>a, b</sup>	VV
	T <sub>A</sub> = 70 °C	1	5 <sup>a, b</sup>	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

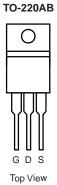
THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a, c</sup>	t ≤ 10 s	R <sub>thJA</sub>	38	46	°C/W	
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	20	25	0/10	

Notes:

b. t = 10 s.

c. Maximum under Steady State conditions is 85  $^{\circ}\text{C/W}.$ 

d. Based on T<sub>C</sub> = 25 °C.



# Pb-free RoHS

a. Surface mounted on 1" x 1" FR4 board.



SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = -250 \mu A$	- 30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		- 34		mV/	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			5.3		°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.0		- 2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 25 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gale voltage Drain Current	IDSS	$V_{DS}$ = - 30 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 5	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -10 \text{ V}, \text{ V}_{GS} = -10 \text{ V}$	- 30			Α	
	P	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10 A		8		mΩ	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8 A		11		1115.2	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 10 A		28		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			3950			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		455		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			390		1	
		$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -10 \text{ A}$		57	86		
Total Gate Charge	$Q_g$			29.5	5 45		
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 10 A		8		nC	
Gate-Drain Charge	Q <sub>gd</sub>			22		1	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.5	2.2	4.4	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			13	25		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, R <sub>L</sub> = 1.5 $\Omega$		12	24		
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong$ - 10 A, $V_{GEN}$ = - 10 V, $R_g$ = 1 $\Omega$		40	70		
Fall Time	t <sub>f</sub>			9	18		
Turn-On Delay Time	t <sub>d(on)</sub>			48	80	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_L$ = 1.5 $\Omega$		92	160		
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong$ - 10 A, $V_{GEN} =$ - 4.5 V, $R_g =$ 1 $\Omega$		34	60	1	
Fall Time	t <sub>f</sub>			19	35		
Drain-Source Body Diode Characteris	stics						
Continous Source-Drain Diode Current	١ <sub>s</sub>	T <sub>C</sub> = 25 °C			- 4.1		
Pulse Diode Forward Current	I <sub>SM</sub>				- 60	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 3 A, V <sub>GS</sub> = 0 V		- 0.75	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			27	45	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			16	27	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, \text{T}_J = 25 ^{\circ}\text{C}$		12		1	
Reverse Recovery Rise Time	t <sub>b</sub>			15		ns	

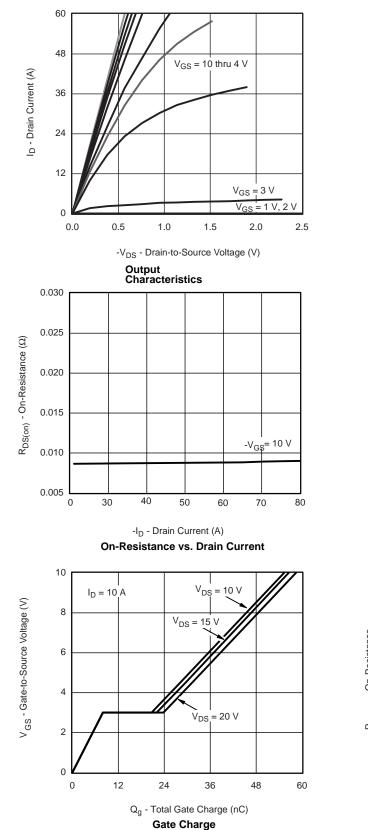
Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

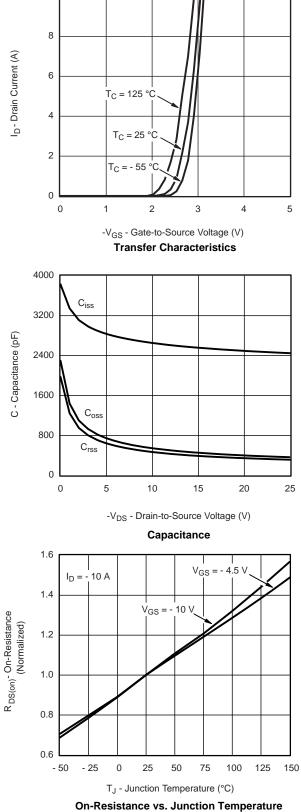
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



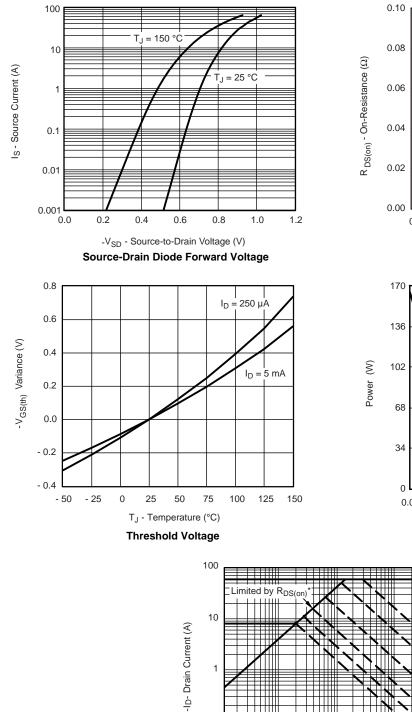
10

服务热线:400-655-8788

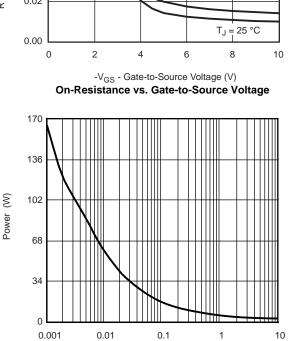


I<sub>D</sub> = 10 A

T<sub>J</sub> = 125 °C

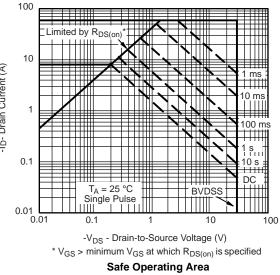


#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



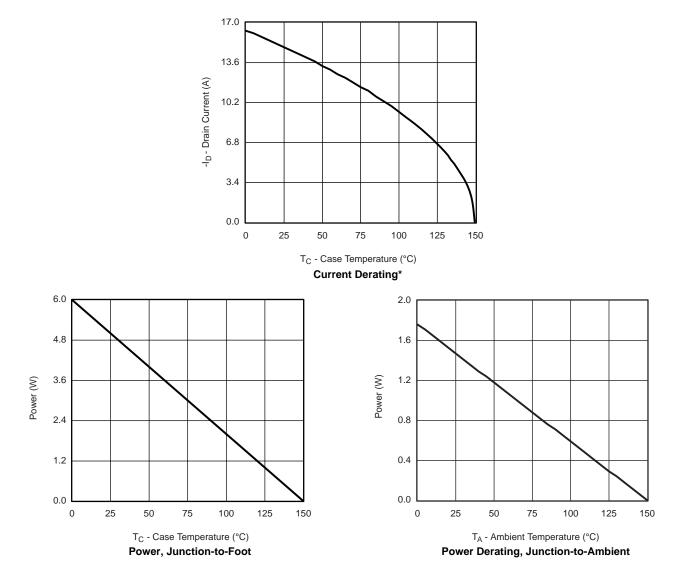
Single Pulse Power, Junction-to-Ambient

Time (s)





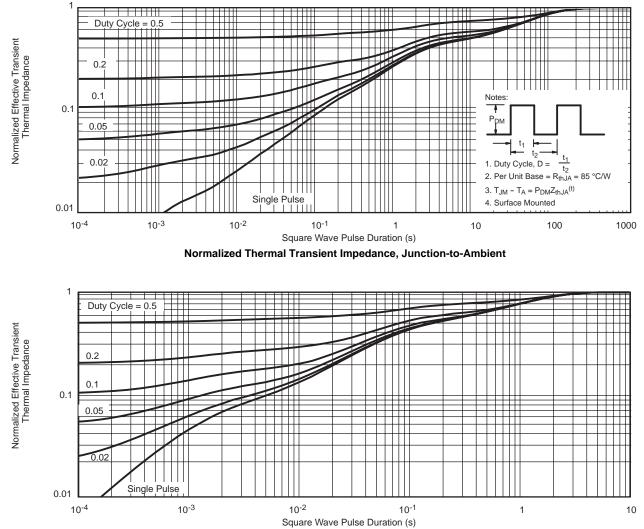
#### MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



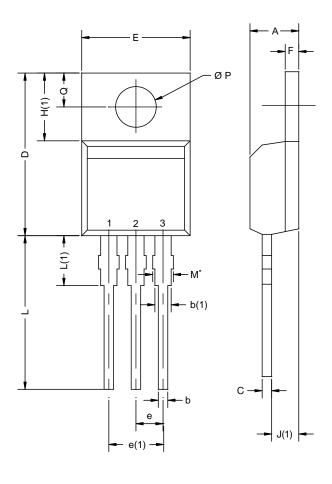
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot



## **TO-220AB**



	MILLIN		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
E	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØР	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: X12- DWG: 547	0208-Rev. N, 1	08-Oct-12		

#### Notes

\* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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