

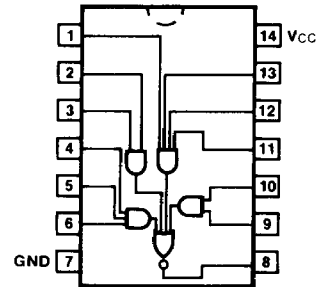
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54S/74S64 011604

4-2-3-2-INPUT AND-OR-INVERT GATE

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		V _{CC} = +5.0 V ±5%, T _A = 0°C to +70°C	V _{CC} = +5.0 V ±10%, T _A = -55°C to +125°C	
Plastic DIP (P)	A	74S64PC		9A
Ceramic DIP (D)	A	74S64DC	54S64DM	6A
Flatpak (F)	A	74S64FC	54S64FM	3I

**CONNECTION DIAGRAM
PINOUT A**



INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	54/74S (U.L.) HIGH/LOW
Inputs	1.25/1.25
Outputs	25/12.5

DC AND AC CHARACTERISTICS: See Section 3*

SYMBOL	PARAMETER	54/74S		UNITS	CONDITIONS	
		Min	Max		V _{IN} = Gnd	V _{CC} = Max
I _{CC} H	Power Supply Current		12.5	mA		
I _{CC} L			16		**	
t _{PLH} t _{PHL}	Propagation Delay	2.0 2.0	5.5 5.5	ns	Figs. 3-1, 3-4	

*DC limits apply over operating temperature range; AC limits apply at T_A = +25°C and V_{CC} = +5.0 V.

**I_{CC}L is measured with all inputs of one gate open and remaining inputs grounded.