

● General Description

The AGM1503EL combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$

This device is ideal for load switch and battery protection applications.

● Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

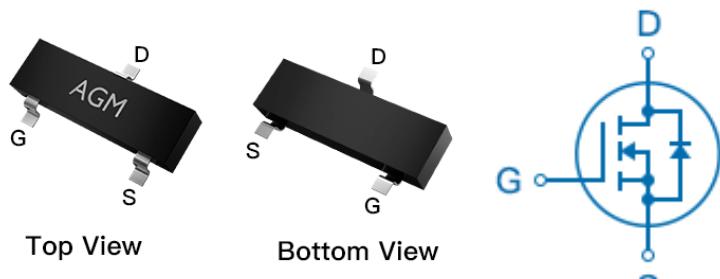
● Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDS(on)	ID
150V	350mΩ	3.0A

SOT-23 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
1503	AGM1503EL	SOT-23	178mm	8mm	3000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	150	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(TA=25°C) (Note 1)	3.0	A
	Drain Current-Continuous(TA=100°C)	2.0	A
IDM (pulse)	Drain Current-Pulsed (Note 2)	12	A
PD	Maximum Power Dissipation(TA=25°C)	1.7	W
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
R _{θJA}	Thermal Resistance Junction-ambient (Steady State) ¹	---	74	°C/W

Table 3. Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	150	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=150V, VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V, VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=250μA	1.2	2.0	2.2	V
gFS	Forward Transconductance	VDS=5V, ID=3A	--	--	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=3A	--	350	380	mΩ
		VGS=4.5V, ID=2A	--	420	450	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=25V, VGS=0V, F=1MHZ	--	265	--	pF
Coss	Output Capacitance		--	35	--	pF
Crss	Reverse Transfer Capacitance		--	30	--	pF
Rg	Gate resistance	f=1.0MHz	--	--	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V, VDS=50V, ID=3A, RGEN=1.8Ω	--	7.5	--	nS
tr	Turn-on Rise Time		--	6.0	--	nS
td(off)	Turn-Off Delay Time		--	21	--	nS
tf	Turn-Off Fall Time		--	9.0	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=50V, ID=2A	--	18	--	nC
Qgs	Gate-Source Charge		--	2.5	--	nC
Qgd	Gate-Drain Charge		--	4.0	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	3.0	A
VSD	Forward on Voltage	VGS=0V, IS=3A	--	--	1.2	V
trr	Reverse Recovery Time	Isd=3A, dl/dt=100A/μs, TJ=25°C	--	21	--	ns
Qrr	Reverse Recovery Charge		--	22	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

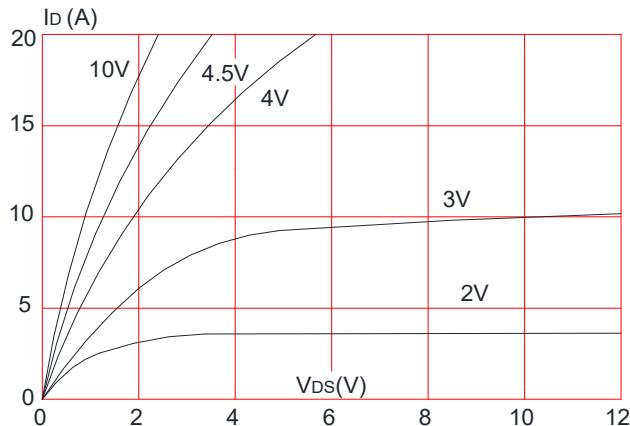
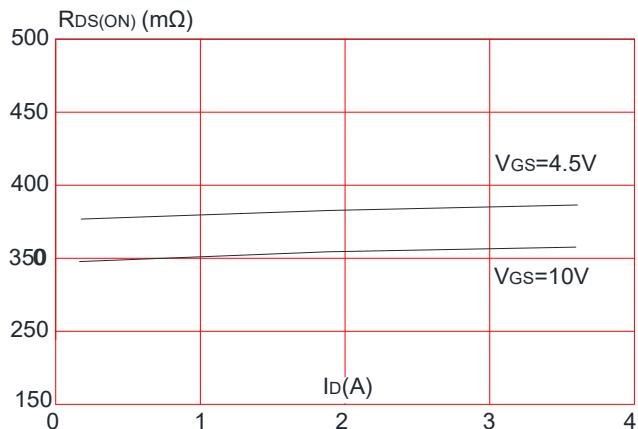
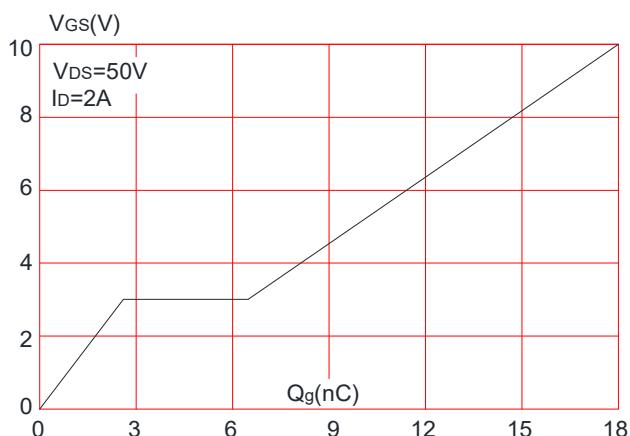
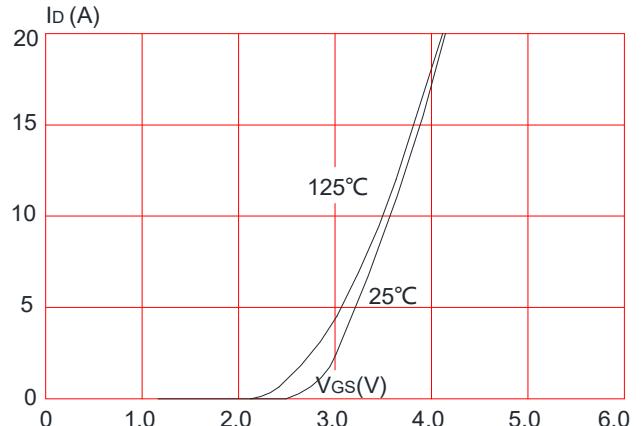
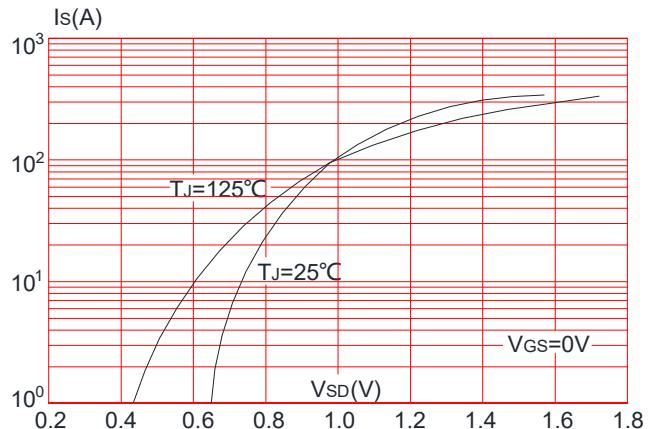
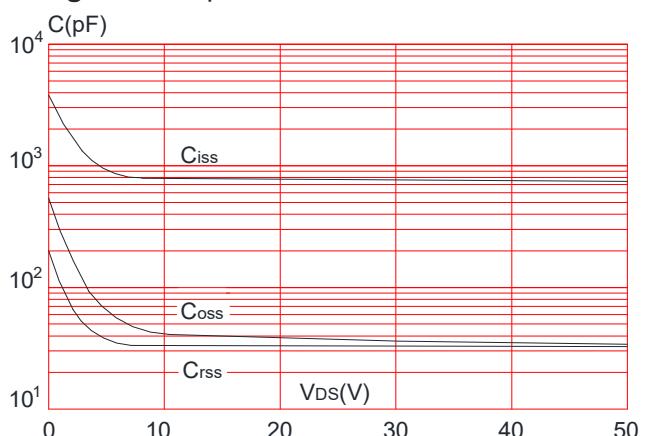
Figure1: Output Characteristics

Figure 3: On-resistance vs. Drain Current

Figure 5: Gate Charge Characteristics

Figure 2: Typical Transfer Characteristics

Figure 4: Body Diode Characteristics

Figure 6: Capacitance Characteristics


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

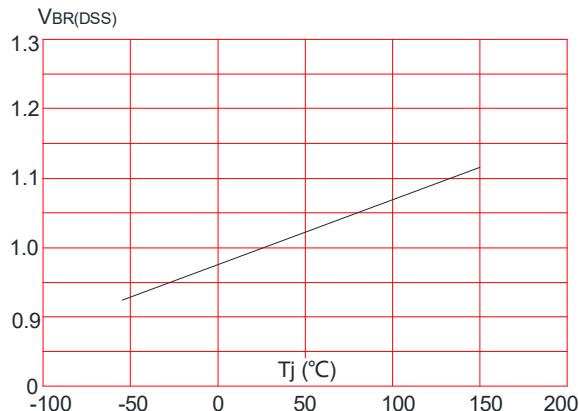


Figure 9: Maximum Safe Operating Area

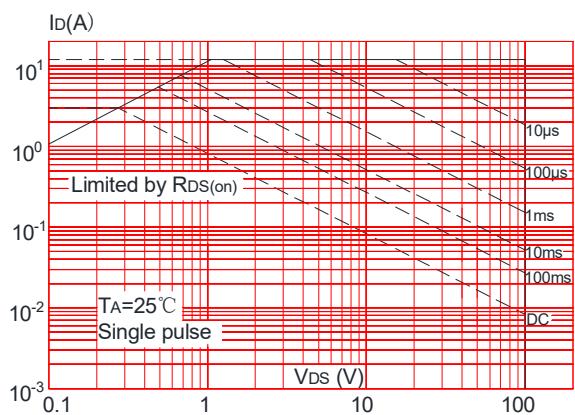


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

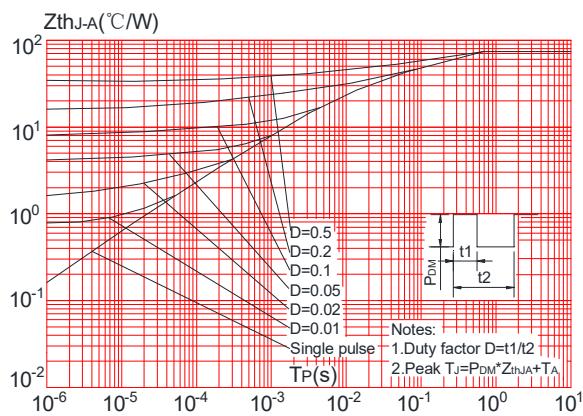


Figure 8: Normalized on Resistance vs. Junction Temperature

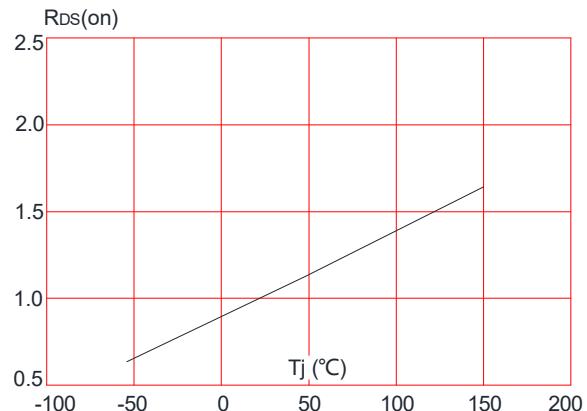
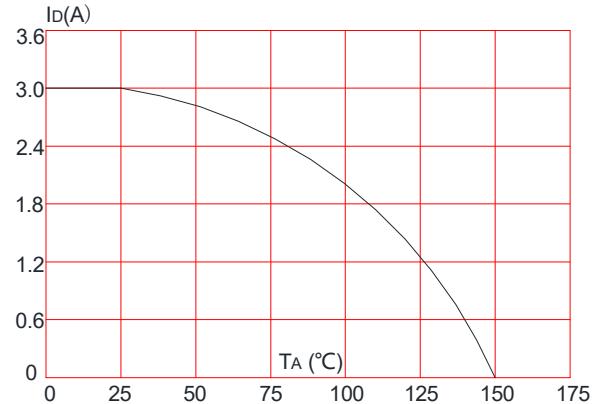
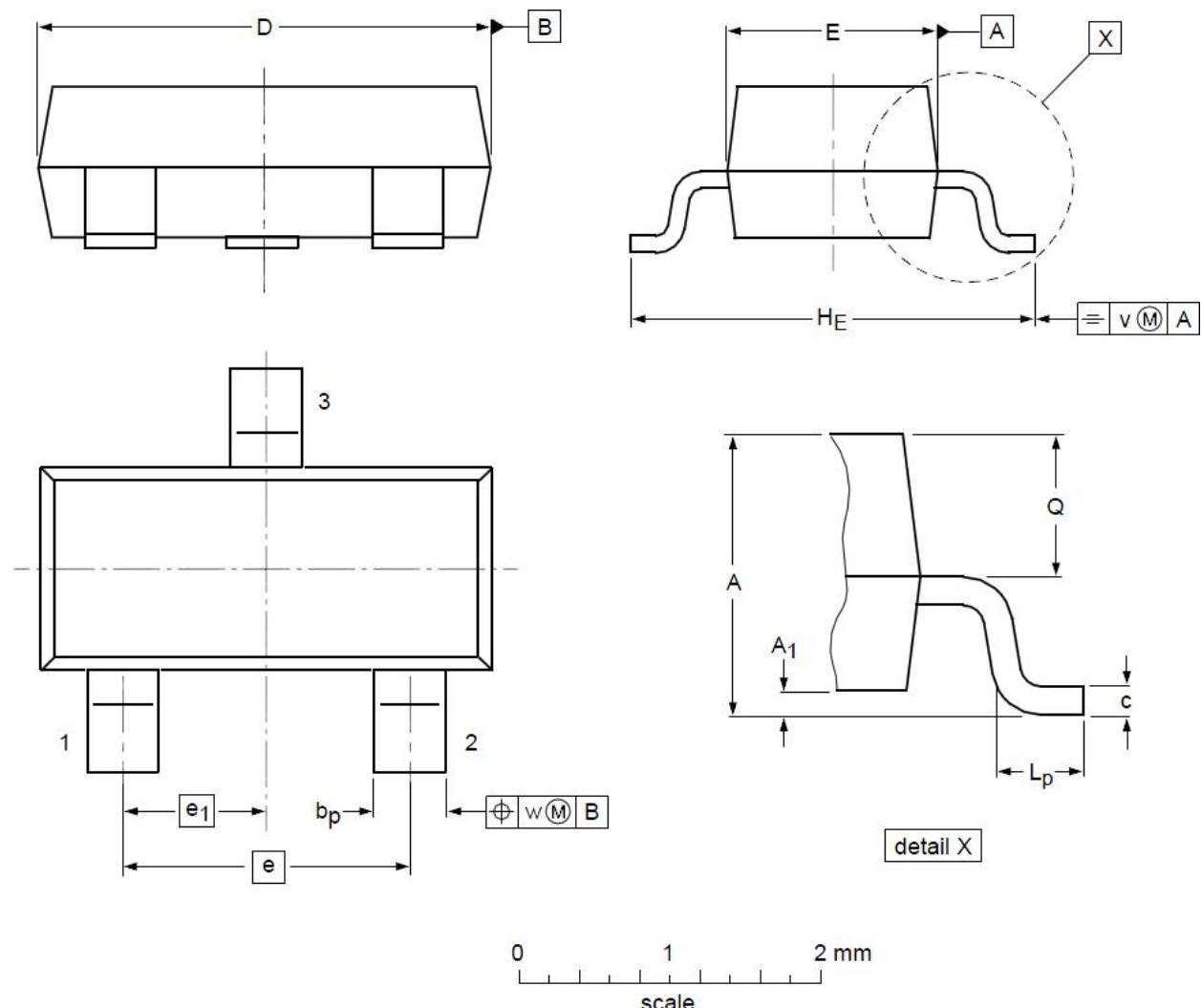


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature



Package Mechanical Data SOT-23



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.01	1.15	A₁	0.01	0.05	0.10
b_p	0.30	0.42	0.50	c	0.08	0.13	0.15
D	2.80	2.92	3.00	E	1.20	1.33	1.40
e	--	1.90	--	e₁	--	0.95	--
H_E	2.25	2.40	2.55	L_p	0.30	0.42	0.50
Q	0.45	0.49	0.55	v	--	0.20	--
w	--	0.10	--				

Disclaimer:

The information provided in this document is believed to be accurate and reliable. however, Shenzhen Core Control Electronics Technology Co., Ltd. does not assume any responsibility for the following consequences. Do not consider the use of such information or use beyond its scope.

The information mentioned in this document may be changed at any time without notice.

The products and information provided in this document do not infringe patents. Shenzhen Core Control Electronics Technology Co., Ltd. assumes no responsibility for any infringement of any other rights of third parties. The result of using such products and information.

This document is the first version issued on Dec. 10th, 2023. This document replaces all previously provided information.

AGMSEMI It is a registered trademark of Shenzhen Core Control Electronics Technology Co., Ltd.

Copyright © 2017 Shenzhen Core Control Electronics Technology Co., Ltd. all rights reserved.