

54LS365A/DM54LS365A/DM74LS365A **Hex TRI-STATE® Buffers**

General Description

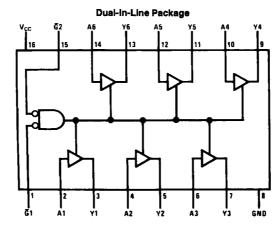
This device contains six independent gates each of which performs a non-inverting buffer function. The outputs have the TRI-STATE feature. When enabled, the outputs exhibit the low impedance characteristics of a standard LS output with additional drive capability to permit the driving of bus lines without external resistors. When disabled, both the output transistors are turned off presenting a high-impedance state to the bus line. Thus the output will act neither as a significant load nor as a driver. To minimize the possibility

that two outputs will attempt to take a common bus to opposite logic levels, the disable time is shorter than the enable time of the outputs.

Features

■ Alternate Military/Aerospace device (54LS365A) is available. Contact a National Semiconductor Sales Office/Distributor for specifications.

Connection Diagram



TL/F/8427-1

Order Number 54LS365ADMQB, 54LS365AFMQB, 54LS365ALMQB, DM54LS365AJ, DM54LS365AW, DM74LS265AM or DM74LS365AN See NS Package Number E20A, J16A, M16A, N16E or W16A

Function Table

Y = A

	Input	Output	
G 1	G2	A	Υ
н	Х	х	Hi-Z
X	H	x	Hi-Z
L	L	н	Н
L	L	L	L

H = High Logic Level

L = Low Logic Level

X = Either Low or High Logic Level

Hi-Z = TRI-STATE (Outputs are disabled)

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V Input Voltage 7V

Operating Free Air Temperature Range

Storage Temperature Range -65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54LS365A			DM74LS365A			Units
		Min	Nom	Max	Min	Nom	Max	Oilles
Vcc	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage	_		0.7			0.8	V
Юн	High Level Output Current			-1			-2.6	mA
loL	Low Level Output Current			12			24	mA
T _A	Free Air Operating Temperature	55		125	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	V_{CC} = Min, I_{I} = -18 mA				-1.5	٧
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max$ $V_{IL} = Max, V_{IH} = Min$		2.4	3.4		٧
V _{OL}	V _{OL} Low Level Output	$V_{CC} = Min, I_{OL} = Max$ $V_{IL} = Max, V_{IH} = Min$ DM54 DM74			0.25	0.4	V
l	Voltage				0.35	0.5	
		I _{OL} = 12 mA, V _{CC} = Min	DM74		0.25	0.4	ĺ
1	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 7V				0.1	mA
IIH	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$				20	μА
[†] I∟	I _{IL} Low Level Input Current	V _{CC} = Max, V _I = 0.5V (Note 4)	A Input			-20	μΑ
		V _{CC} = Max, V _I = 0.4V (Note 5)	A Input			-0.4	mA
	ļ	V _{CC} = Max, V _I = 0.4V	G Input			-0.4	1
lozh	Off-State Output Current with High Level Output Voltage Applied	$V_{CC} = Max, V_O = 2.4V$ $V_{IH} = Min, V_{IL} = Max$				20	μΑ
lozL	Off-State Output Current with Low Level Output Voltage Applied	$V_{CC} = Max, V_O = 0.4V$ $V_{IH} = Min, V_{IL} = Max$				-20	μΑ
los	Short Circuit	V _{CC} = Max DM54		-20		-100	m/
Output Current	(Note 2)	DM74	-20		-100	1 ""^	
lcc	Supply Current	V _{CC} = Max (Note 3)			14	24	mA

Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Note 3: Icc is measured with the DATA inputs grounded and the OUTPUT CONTROLS at 4.5V.

Note 4: Both G inputs are at 2V.

Note 5: Both G inputs at 0.4V.

Symbol	Parameter	$R_L = 667\Omega$				
		C _L = 50 pF		C _L = 150 pF		Units
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time Low to High Level Output		16		25	ns
tpHL	Propagation Delay Time High to Low Level Output		16		25	ns
^t PZH	Output Enable Time to High Level Output		30		40	ns
t _{PZL}	Output Enable Time to Low Level Output		30		40	ns
t _{PHZ}	Output Disable Time from High Level Output (Note 6)		20			ns
t _{PLZ}	Output Disable Time from Low Level Output (Note 6)		20			ns

Note 6: C_L = 5 pF.