



SMP20N65

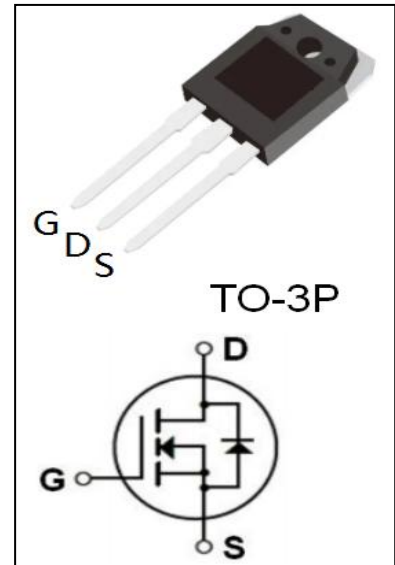
650V N-Channel MOSFET

● **Features:**

- 20.0A, 650V, $R_{DS(on)(Typ)} = 380m\Omega @ V_{GS}=10V$
- Low Gate Charge
- Low C_{rSS}
- 100% Avalanche Tested
- Fast Switching
- Improved dv/dt Capability

● **Application:**

- High Frequency Switching Mode Power Supply
- Active Power Factor Correction



Absolute Maximum Ratings($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{DSS}	Drain-Source Voltage	650	V
I_D	Drain Current - Continuous($T_c=25^\circ C$) - Continuous($T_c=100^\circ C$)	20.0*	A
		12.6*	A
I_{DM}	Drain Current -Pulsed (Note1)	80.0*	A
V_{GSS}	Gate-Source Voltage	± 30	V
E_{AS}	Single Pulsed Avalanche Energy (Limit Reference Value) (Note2)	1078	mJ
I_{AR}	Avalanche Current (Note1)	14.0	A
E_{AR}	Repetitive Avalanche Energy (Note1)	33	mJ
dv/dt	Peak Diode Recovery dv/dt (Note3)	4.5	V/ns
P_D	Power Dissipation($T_C =25^\circ C$) -Derate above $25^\circ C$	290	W
		2.32	W/ $^\circ C$
T_j	Operating Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-55 to+150	$^\circ C$

* Drain Current Limited by Maximum Junction Temperature.

Thermal Characteristics

Symbol	Parameter	Max	Unit
$R_{\theta JC}$	Thermal Resistance,Junction to Case	0.43	$^\circ C /W$
$R_{\theta JA}$	Thermal Resistance,Junction to Ambient	50	$^\circ C /W$



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Electrical Characteristics(Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV_{DSS}	Drain-source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	650	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D=250\mu A$ (Referenced to 25°C)	--	0.68	--	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=650V, V_{GS}=0V$	--	--	1	μA
		$V_{DS}=520V, T_c=125^\circ C$	--	--	10	μA
I_{GSSF}	Gate-Body Leakage Current,Forward	$V_{GS}=+30V, V_{DS}=0V$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current,Reverse	$V_{GS}=-30V, V_{DS}=0V$	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=10.0A$	--	380	450	m Ω
g_{FS}	Forward Transconductance	$V_{DS}=20V, I_D=10.0A$ (Note4)	--	18	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$	--	3050	--	pF
C_{oss}	Output Capacitance		--	240	--	pF
C_{rss}	Reverse Transfer Capacitance		--	9	--	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 325V, I_D = 20A,$ $R_G = 25\Omega$ (Note4,5)	--	27.2	--	ns
t_r	Turn-On Rise Time		--	44.5	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	82.5	--	ns
t_f	Turn-Off Fall Time		--	44.4	--	ns
Q_g	Total Gate Charge	$V_{DS} = 520V, I_D = 20A,$ $V_{GS} = 10V$ (Note4,5)	--	55.2	--	nC
Q_{gs}	Gate-Source Charge		--	13.9	--	nC
Q_{gd}	Gate-Drain Charge		--	22.8	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	20	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	80	A
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0V, I_S = 20A$	--	--	1.4	V
t_{rr}	Reverse Recovery Time	$V_{GS} = 0V, I_S = 20A,$ $dI_F/dt = 100A/\mu s$ (Note4)	--	593	--	ns
Q_{rr}	Reverse Recovery Charge		--	7.62	--	μC

Notes:

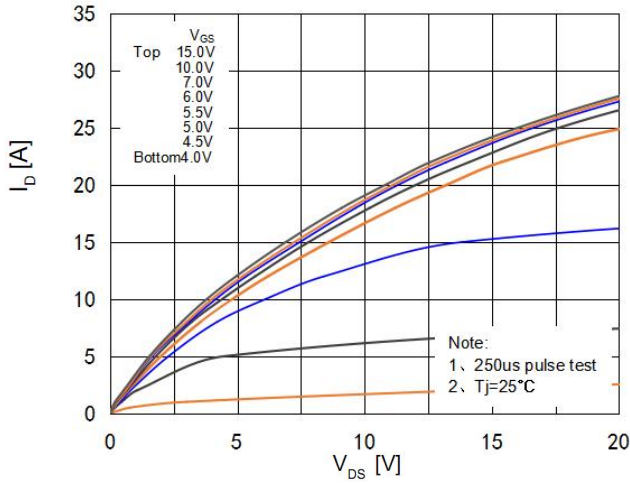
- 1、Repetitive Rating:Pulse Width Limited by Maximum Junction Temperature.
- 2、L = 10.0mH, $I_{AS} = 14.0A$, $V_{DD} = 100V$, $R_G = 25\Omega$, Starting $T_J = 25^\circ C$.
- 3、 $I_{SD} \leq 20.0A$, $di/dt \leq 200A/\mu s$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ C$.
- 4、Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
- 5、Essentially Independent of Operating Temperature.



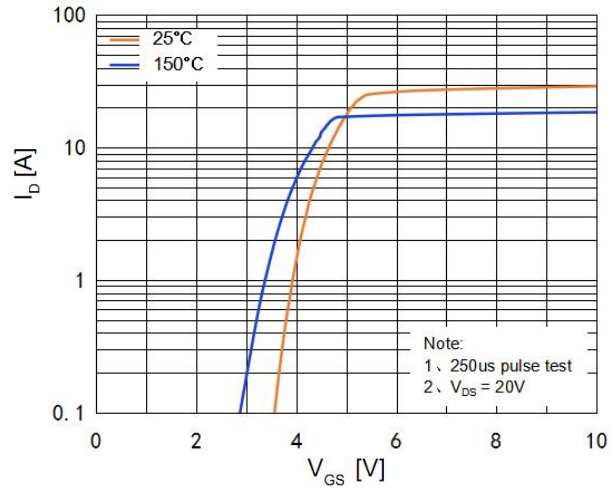
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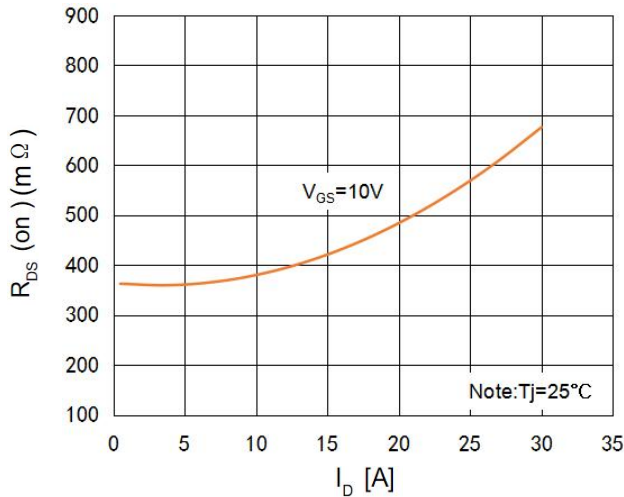
On-Regin Characteristics



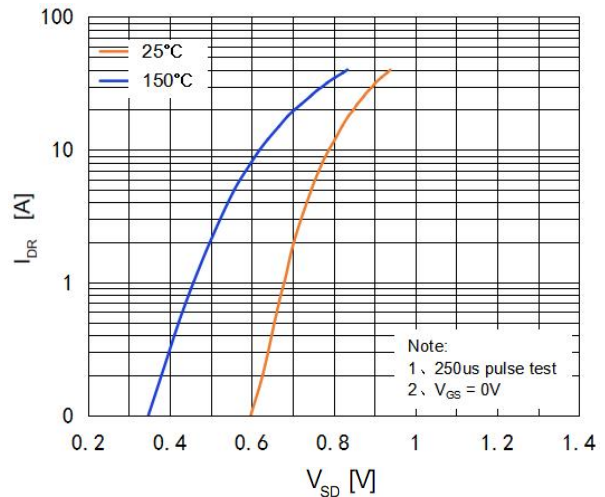
Transfer Characteristics



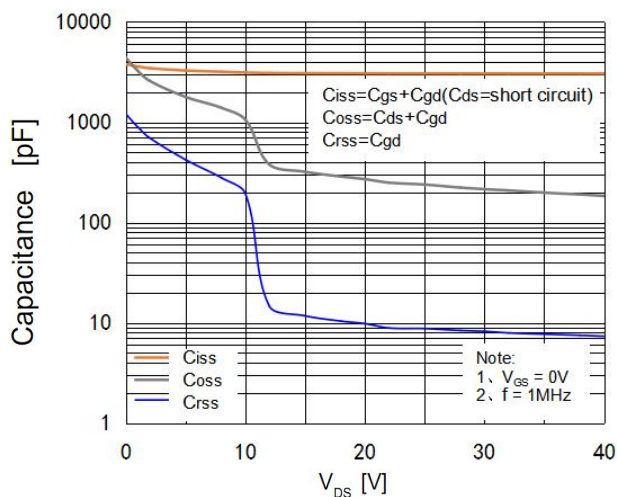
On-Resistance Variation vs. Drain Current and Gate Voltage



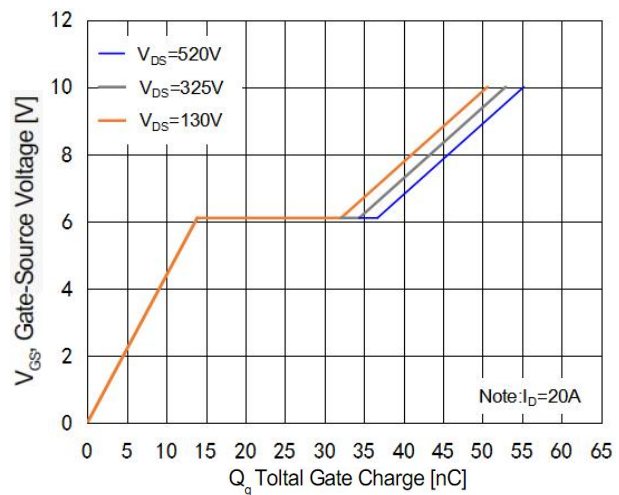
Body Diode Forward Voltage Variation vs. Source Current and Temperature



Capacitance Characteristics



Gate Charge Characteristics

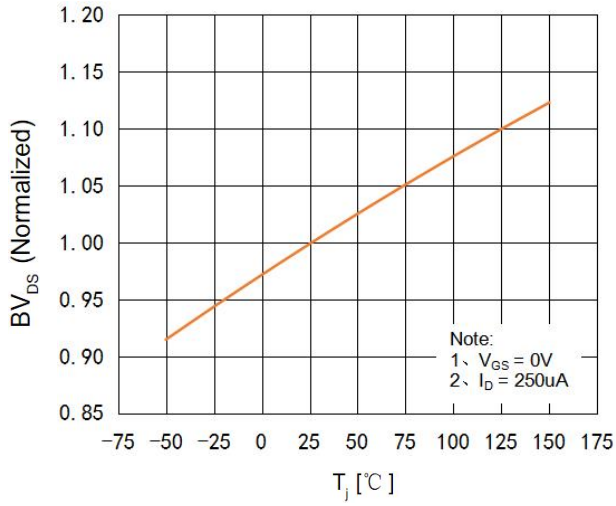




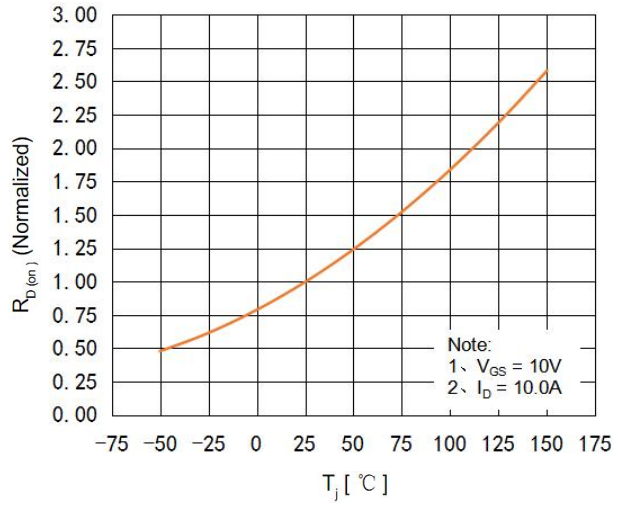
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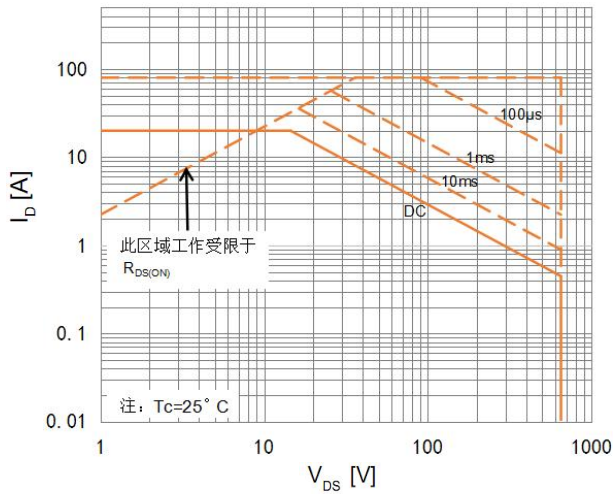
Breakdown Voltage Variation vs. Temperature



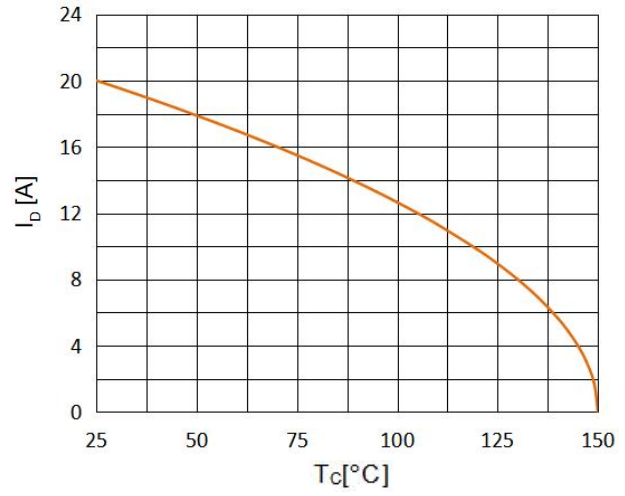
On-Resistance Variation vs. Temperature



Maximum Safe Operating Area



Maximum Drain Current Vs. Case Temperature





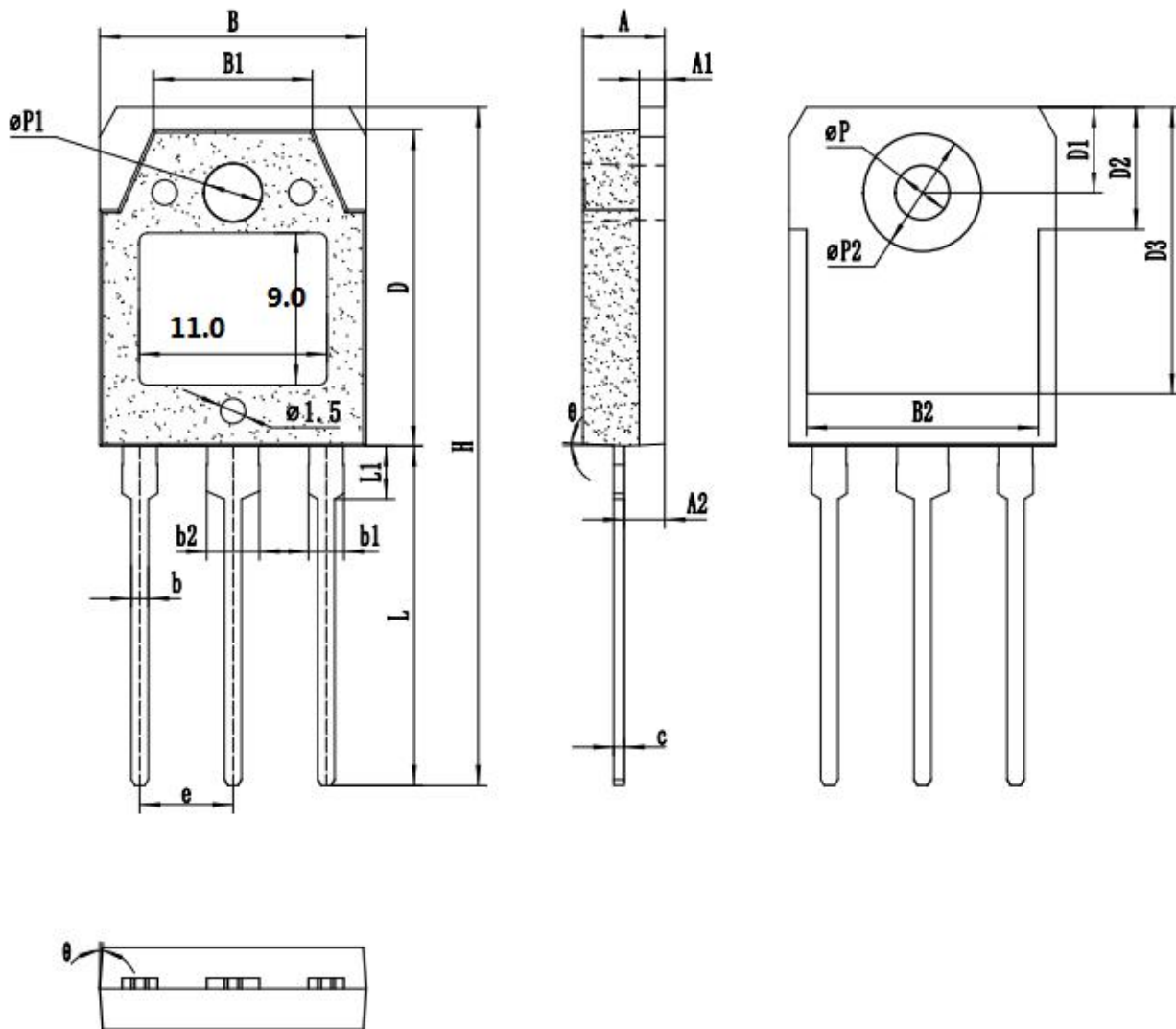
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TO-3P Package Dimensions

UNIT: mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	4.50	4.80	5.10	D	18.00	18.50	19.00
A1	1.40	1.50	1.60	D1	4.60	5.00	5.40
A2	2.10	2.40	2.70	D2	6.70	7.10	7.50
b	0.80	1.00	1.20	D3	16.20	16.70	17.20
b1	1.90	2.10	2.30	L1	2.70	3.10	3.50
b2	2.90	3.10	3.30	L	19.20	20.20	21.20
e		5.45		H	38.40	39.90	41.40
B	15.20	15.70	16.20	ΦP	2.90	3.15	3.40
B1	9.10	9.40	9.70	ΦP1	3.15	3.40	3.65
B2	13.20	13.60	14.00	ΦP2	6.70	7.00	7.30
c	0.50	0.60	0.70	θ	3°	5°	7°





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注意事项:

- 1、在电路设计时请不要超过器件的最大额定值，否则会影响整机的可靠性。
- 2、MOSFET产品为静电敏感型器件，使用时应注意采取防静电保护措施，如佩戴防静电手环、设备接地等。
- 3、如需安装散热片，请注意控制扭力大小及散热片的平整度。
- 4、该规格书由华科公司制作，并可能不定期更改，恕不另行通知。
- 5、如有疑问，请及时联系我司销售代表。

版本履历表:

序号	版本号	修改时间	修改记录
1	V1.0	2022-12-20	首次发行