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**FAIRCHILD**  
SEMICONDUCTOR™

## DM74S05 Hex Inverters with Open-Collector Outputs

### General Description

This device contains six independent gates each of which performs the logic INVERT function. The open-collector outputs require external pull-up resistors for proper logical operation.

### Pull-Up Resistor Equations

$$R_{MAX} = \frac{V_{CC}(\text{Min}) - V_{OH}}{N_1(I_{OH}) + N_2(I_{IH})}$$

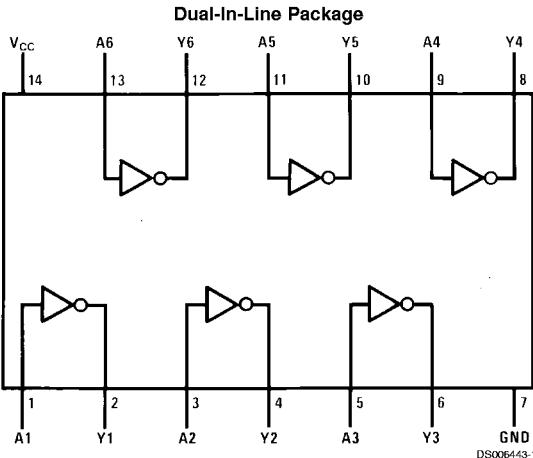
$$R_{MIN} = \frac{V_{CC}(\text{Max}) - V_{OL}}{I_{OL} - N_3(I_{IL})}$$

Where:  $N_1(I_{OH})$  = total maximum output high current for all outputs tied to pull-up resistor

$N_2(I_{IH})$  = total maximum input high current for all inputs tied to pull-up resistor

$N_3(I_{IL})$  = total maximum input low current for all inputs tied to pull-up resistor

### Connection Diagram



Order Number DM74S05M or DM74S05N  
See Package Number M14A or N14A

### Function Table

$$Y = \bar{A}$$

Input	Output
A	Y
L	H
H	L

H = High Logic Level  
L = Low Logic Level

### Absolute Maximum Ratings (Note 1)

Supply Voltage	7V	Operating Free Air Temperature Range	0°C to +70°C
Input Voltage	5.5V	Storage Temperature Range	-65°C to +150°C
Output Voltage	7V		

### Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
$V_{CC}$	Supply Voltage	4.75	5	5.25	V
$V_{IH}$	High Level Input Voltage	2			V
$V_{IL}$	Low Level Input Voltage			0.8	V
$V_{OH}$	High Level Output Voltage			5.5	V
$I_{OL}$	Low Level Output Current			20	mA
$T_A$	Free Air Operating Temperature	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

### Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
$V_I$	Input Clamp Voltage	$V_{CC} = \text{Min}$ , $I_I = -18 \text{ mA}$			-1.2	V
$I_{CEX}$	High Level Output Current	$V_{CC} = \text{Min}$ , $V_O = 5.5V$ $V_{IL} = \text{Max}$			250	μA
$V_{OL}$	Low Level Output Voltage	$V_{CC} = \text{Min}$ , $I_{OL} = \text{Max}$ $V_{IH} = \text{Min}$			0.5	V
$I_I$	Input Current @ Max Input Voltage	$V_{CC} = \text{Max}$ , $V_I = 5.5V$			1	mA
$I_{IH}$	High Level Input Current	$V_{CC} = \text{Max}$ , $V_I = 2.7V$			50	μA
$I_{IL}$	Low Level Input Current	$V_{CC} = \text{Max}$ , $V_I = 0.5V$			-2	mA
$I_{CCH}$	Supply Current with Outputs High	$V_{CC} = \text{Max}$		9	19.8	mA
$I_{CCL}$	Supply Current with Outputs Low	$V_{CC} = \text{Max}$		30	54	mA

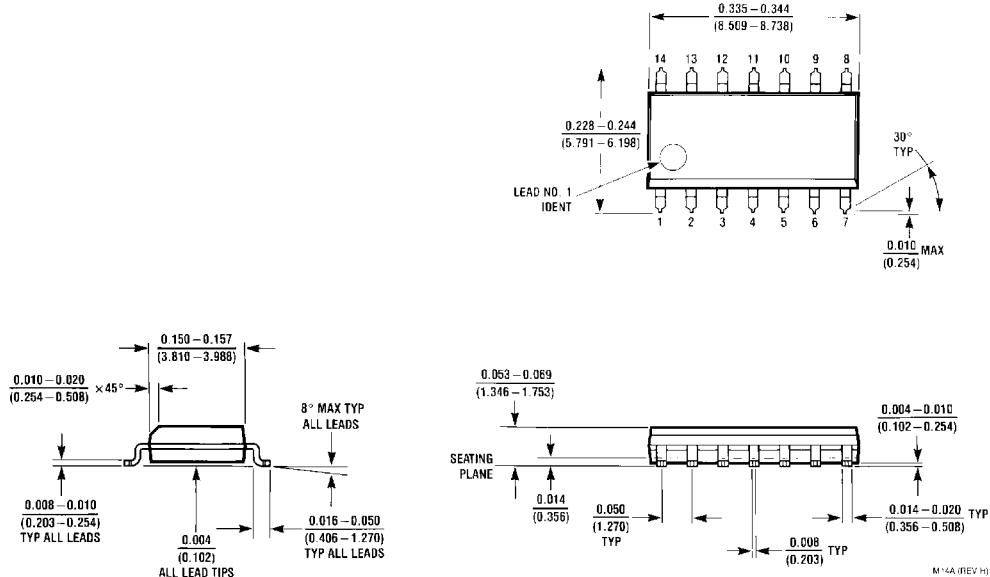
### Switching Characteristics

at  $V_{CC} = 5V$  and  $T_A = 25^\circ\text{C}$  (See for Test Waveforms and Output Load)

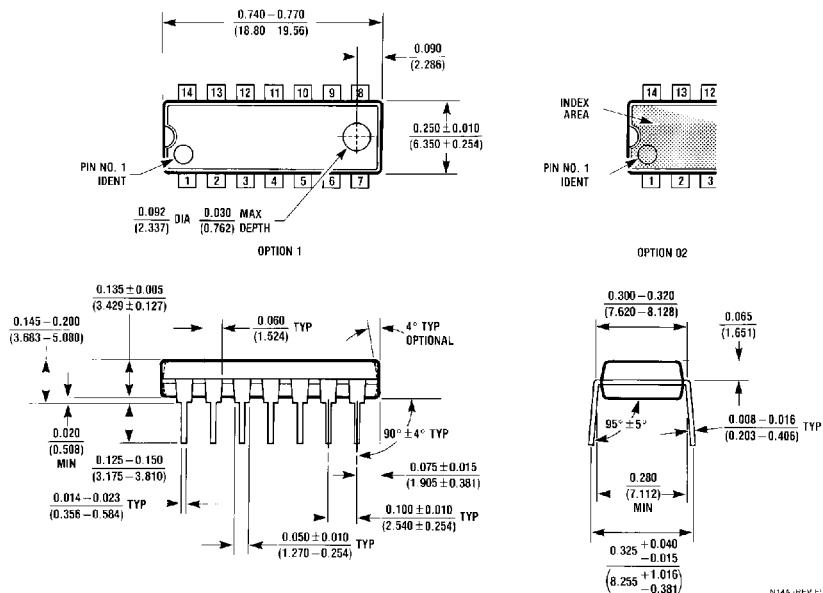
Symbol	Parameter	$R_L = 280\Omega$				Units	
		$C_L = 15 \text{ pF}$		$C_L = 50 \text{ pF}$			
		Min	Max	Min	Max		
$t_{PLH}$	Propagation Delay Time Low to High Level Output	2	7.5	3	11	ns	
$t_{PHL}$	Propagation Delay Time High to Low Level Output	2	7	3	11	ns	

Note 2: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25^\circ\text{C}$ .

**Physical Dimensions** inches (millimeters) unless otherwise noted



**14-Lead Small Outline Molded Package (M)**  
Order Number DM74S05M  
Package Number M14A



**14-Lead Molded Dual-In-Line Package (N)**  
Order Number DM74S05N  
Package Number N14A