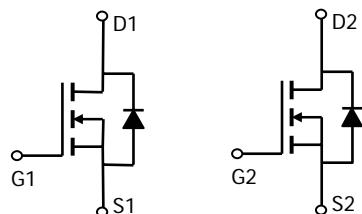
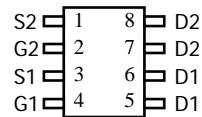


General Description

The AO4800 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. The two MOSFETs make a compact and efficient switch and synchronous rectifier combination for use in buck converters.



Features

- $V_{DS(V)} = 30V$
- $I_D = 6.9A$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 27m\Omega$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 32m\Omega$ ($V_{GS} = 4.5V$)
- $R_{DS(ON)} < 50m\Omega$ ($V_{GS} = 2.5V$)

Absolute Maximum Ratings $T_A = 25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum		Units
Drain-Source Voltage	V_{DS}	30		V
Gate-Source Voltage	V_{GS}	± 12		V
Continuous Drain Current ^A	I_D	6.9		A
$T_A = 70^\circ C$		5.8		
Pulsed Drain Current ^B	I_{DM}	40		
Power Dissipation	P_D	2		W
$T_A = 70^\circ C$		1.44		
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150		°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	48	62.5	°C/W
Steady-State		74	110	°C/W
Maximum Junction-to-Lead ^C	$R_{\theta JL}$	35	40	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24\text{V}, V_{GS}=0\text{V}$ $T_J=55^\circ\text{C}$	0.002	1		μA
					5	
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			100	nA
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.7	1	1.4	V
$I_{D(\text{ON})}$	On state drain current	$V_{GS}=4.5\text{V}, V_{DS}=5\text{V}$	25			A
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=6.9\text{A}$ $T_J=125^\circ\text{C}$	22.6	27		$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=6.0\text{A}$	33	40		
		$V_{GS}=2.5\text{V}, I_D=5\text{A}$	27	32		$\text{m}\Omega$
			42	50		$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS}=5\text{V}, I_D=5\text{A}$	12	16		S
V_{SD}	Diode Forward Voltage	$I_S=1\text{A}$		0.71	1	V
I_S	Maximum Body-Diode Continuous Current				3	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=15\text{V}, f=1\text{MHz}$		858	1050	pF
C_{oss}	Output Capacitance			110		pF
C_{rss}	Reverse Transfer Capacitance			80		pF
R_g	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$		1.24	3.6	Ω
SWITCHING PARAMETERS						
Q_g	Total Gate Charge	$V_{GS}=4.5\text{V}, V_{DS}=15\text{V}, I_D=6.9\text{A}$		9.6	12	nC
Q_{gs}	Gate Source Charge			1.65		nC
Q_{gd}	Gate Drain Charge			3		nC
$t_{D(\text{on})}$	Turn-On Delay Time	$V_{GS}=10\text{V}, V_{DS}=15\text{V}, R_L=2.2\Omega, R_{\text{GEN}}=3\Omega$		3.2	4.8	ns
t_r	Turn-On Rise Time			4.1	6.2	ns
$t_{D(\text{off})}$	Turn-Off Delay Time			26.3	40	ns
t_f	Turn-Off Fall Time			3.7	5.5	ns
t_{rr}	Body Diode Reverse Recovery time	$I_F=5\text{A}, dI/dt=100\text{A}/\mu\text{s}$		15.5	20	ns
Q_{rr}	Body Diode Reverse Recovery charge	$I_F=5\text{A}, dI/dt=100\text{A}/\mu\text{s}$		7.9	12	nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user's specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.

D. The static characteristics in Figures 1 to 6 are obtained using 80 μs pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The SOA curve provides a single pulse rating.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

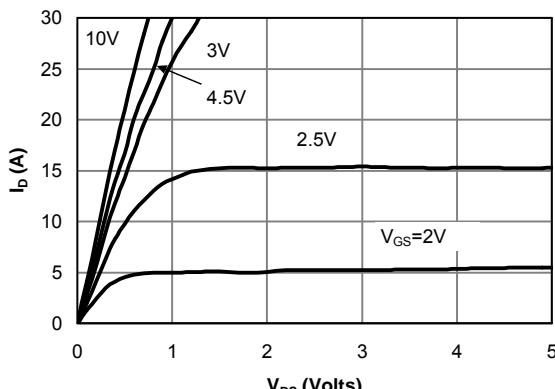


Fig 1: On-Region Characteristics

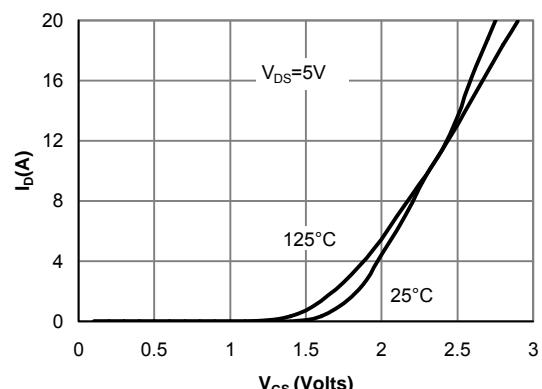


Figure 2: Transfer Characteristics

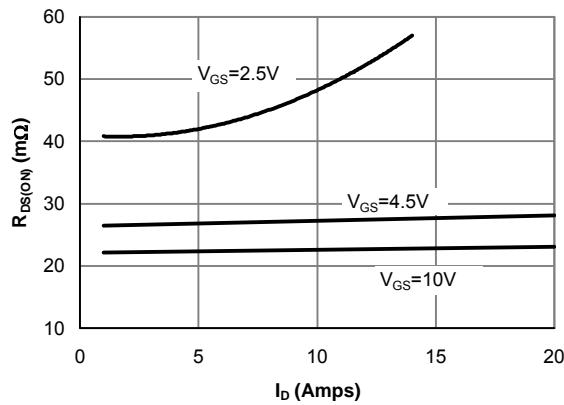


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

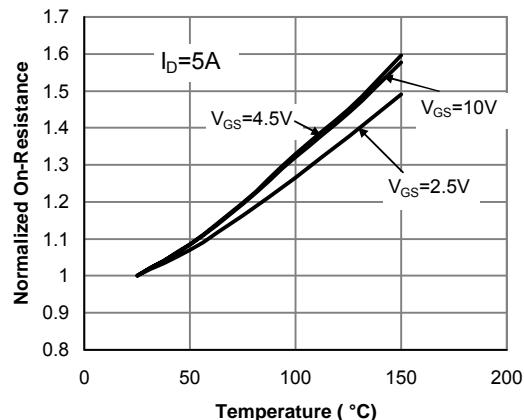


Figure 4: On-Resistance vs. Junction Temperature

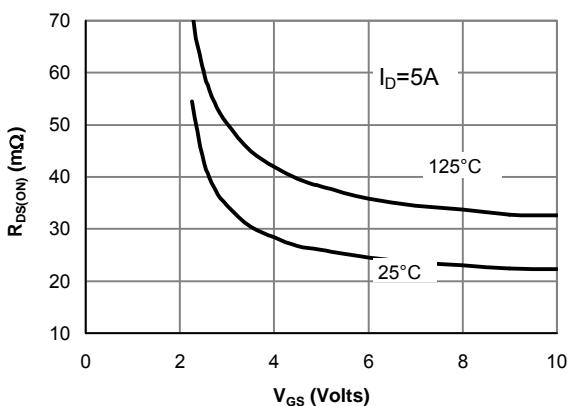


Figure 5: On-Resistance vs. Gate-Source Voltage

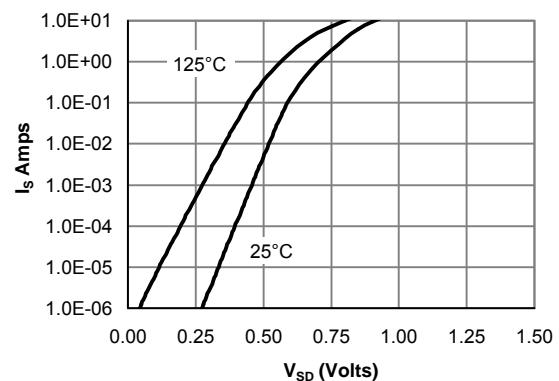
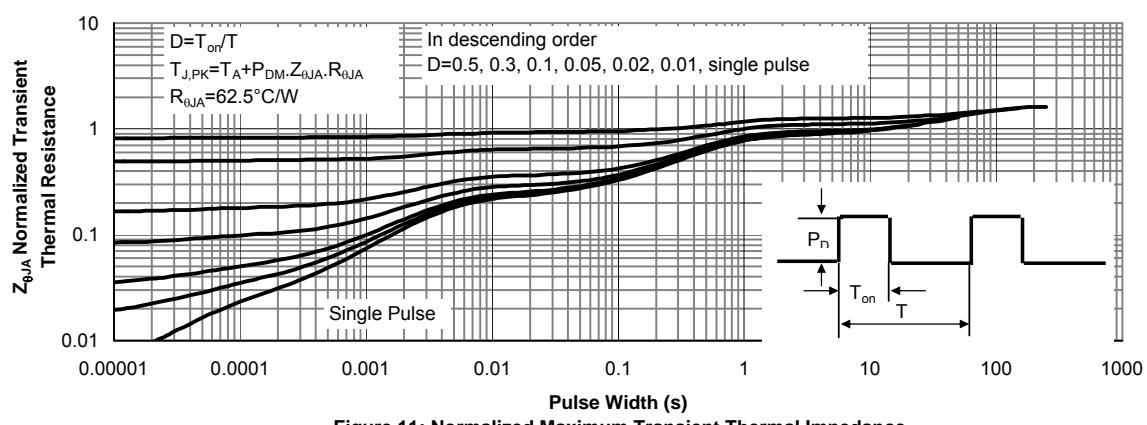
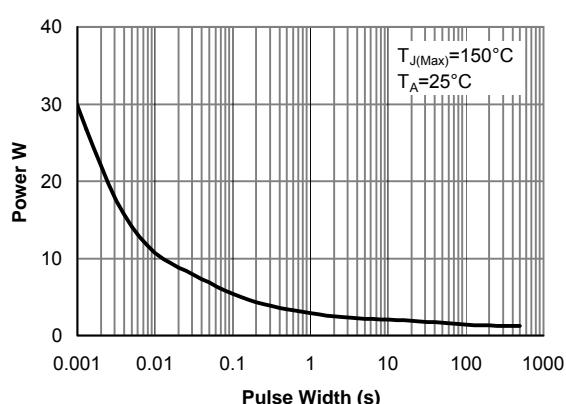
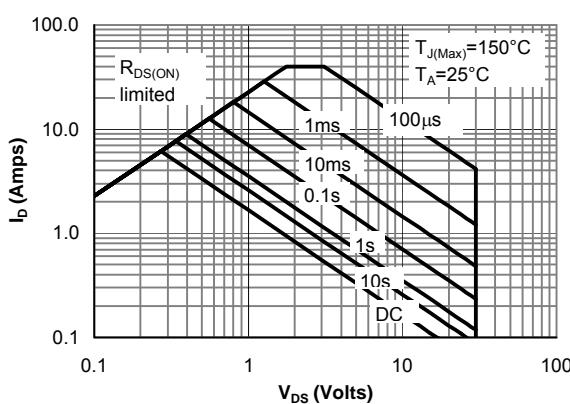
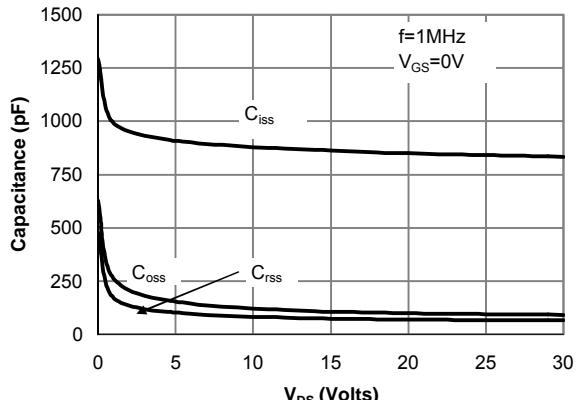
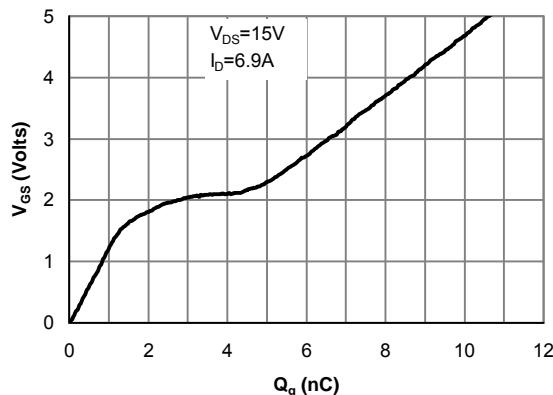


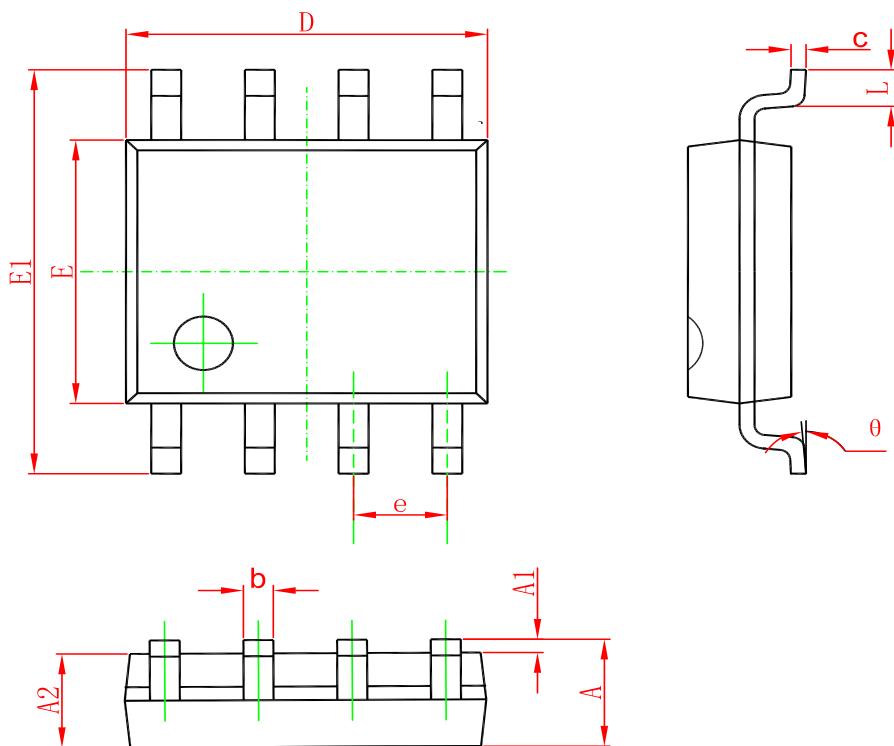
Figure 6: Body diode characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

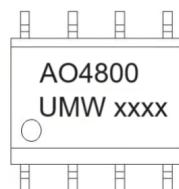


PACKAGE OUTLINE DIMENSIONS

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

Marking**Ordering information**

Order code	Package	Baseqty	Deliverymode
UMW AO4800	SOP-8	3000	Tape and reel