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# AGC TOUCH CONTROL LIGHT SWITCH with SOFT-OFF

## FEATURES:

- · Touch or pushbutton control of incandescent lamps.
- Touch Sensitivity guaranteed to 600pF Touch Plate Capacitance.

- Touch Operation is independent of line plug polarity.
- •.See **OPERATION** description below.
- 50/60 Hz Line Frequency.
- 5V Operation (VDD VSS).
- LS7540 (DIP); LS7540-S (SOIC) See Figure 1 -

#### **APPLICATIONS:**

• Wall switches and adapter modules for converting small table lamps and large floor lamps to touch control.

## **DESCRIPTION:**

The LS7540 is a CMOS integrated circuit for controlling the brightness of incandescent lamps by touch (**Figures 5, 6, 8**) or pushbutton (**Figures 7, 9**). When the LS7540 TOUCH input is connected to a lamp body, any metallic part of the lamp becomes a touch plate. A built-in AGC circuit allows for a wide range of lamp sizes to be used, ranging from small table lamps to large floor lamps. The brightness is controlled by varying the conduction angle of a triac in series with the lamp and triggered by the TRIG/ output. Between off and maximum, there are 124 steps of brightness levels, level zero being off and level 124 being maximum. The phase reference of the TRIG/ signal with respect to the AC line is maintained through the SYNC input.

# OPERATION

The On/Off and brightness control of the lamp is described below:

- In the off-state, a single touch will turn the lamp to full-on.
- At full-on, a single touch will start a **soft-off** dim-cycle causing the brightness to diminish gradually until the lamp turns off. The **soft-off** feature provides light for the User to exit the area. The **soft-off** dim-cycle time is programmed by an external RC con nected to Pin 6. With no RC connected to Pin 6, the **soft-off** dimcycle time = 0.
- During a dim-cycle a single touch will stop the dim-cycle and lock in the brightness present at that time. Another touch will restart the dim-cycle from the present brightness causing the brightness to ramp down towards off again. The start and stop of dim-cycle can be repeated over and over again until the lamp turns off. This feature gives the User full control in setting the light intensity.

# INPUT/OUTPUT DESCRIPTION:

#### CLOCK Input (Pin 1)

An external resistor connected between this input and VDD, along with an internal capacitor and oscillator stage, generates a clock which is used for all timing functions. The recommended value of this resistor for 50Hz and 60Hz operation is specified in the **ELECTRICAL CHARACTERISTICS**.

**PIN ASSIGNMENT** TOP VIEW CLOCK 8 TRIG/ 1 ۲ Vss (-V) SYNC LS754( VDD (+V) 3 6 RAMP TOUCH 5 CONTROL FIGURE 1

**LS7540** 

November 2007

# SYNC Input (Pin 2)

50Hz or 60Hz AC input for zero crossing detection.

# **VDD** (Pin 3)

Supply voltage positive terminal.

# TOUCH Input (Pin 4)

Input for sensing that a touch has been made on a lamp surface or other touch plate. This action alters the state of the TRIG/ output as described in the Touch Operation section of the General Description.

#### CONTROL I/O (Pin 5)

An external R-C network connected between this pin and VDD establishes the controlling feedback for the AGC Loop.

#### RAMP Input/Output (Pin 6)

The ramp down rate of the brightness in the dimming cycle is regulated by a resistor-capacitor (RC) pair connected to the RAMP input. The ramp rate is equal to 4TRc per step, where TRc = 0.48RC. Total ramp down time (TT) from maximum brightness to off is equal to 496TRc.

Example: R = 200k, C = 0.22uF;  $TRC = 0.48 \times (200 \times 10^3) \times (0.22 \times 10^{-6}) = 0.02$  sec. Ramp rate = 4 x TRC = 0.08 sec/step; Total ramp time, TT = 496 x TRC = 9.92 sec.

Vss (Pin 7)

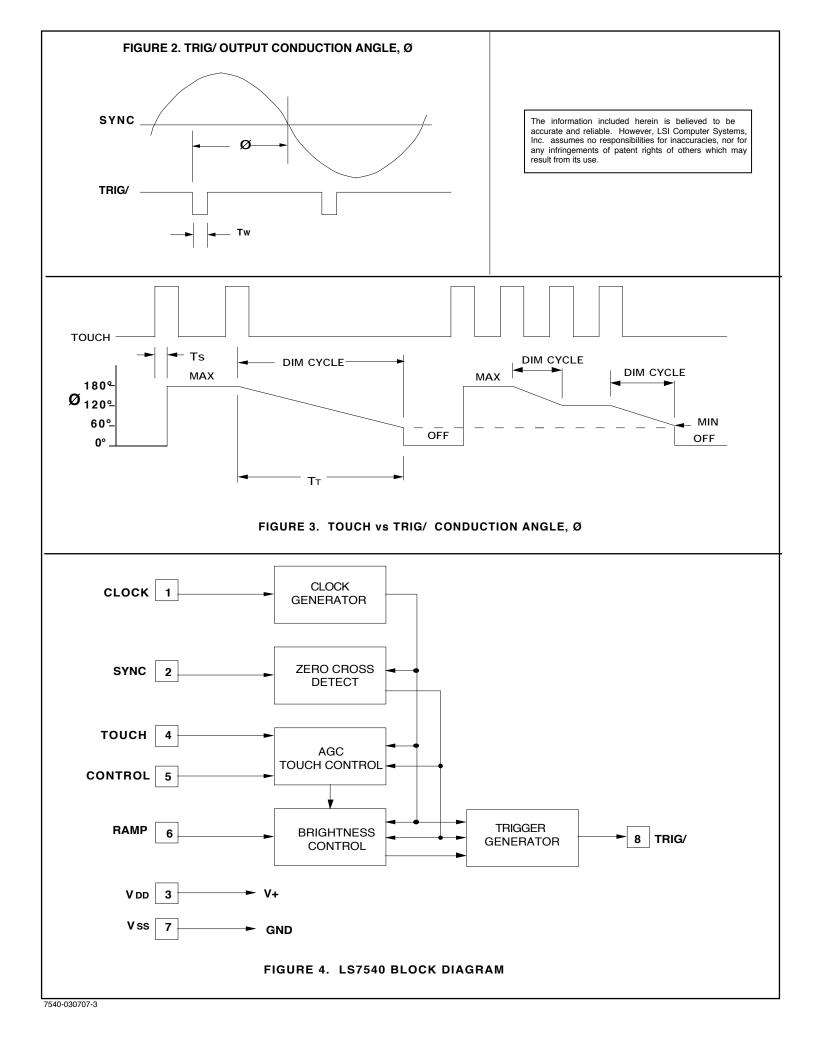
Supply voltage negative terminal.

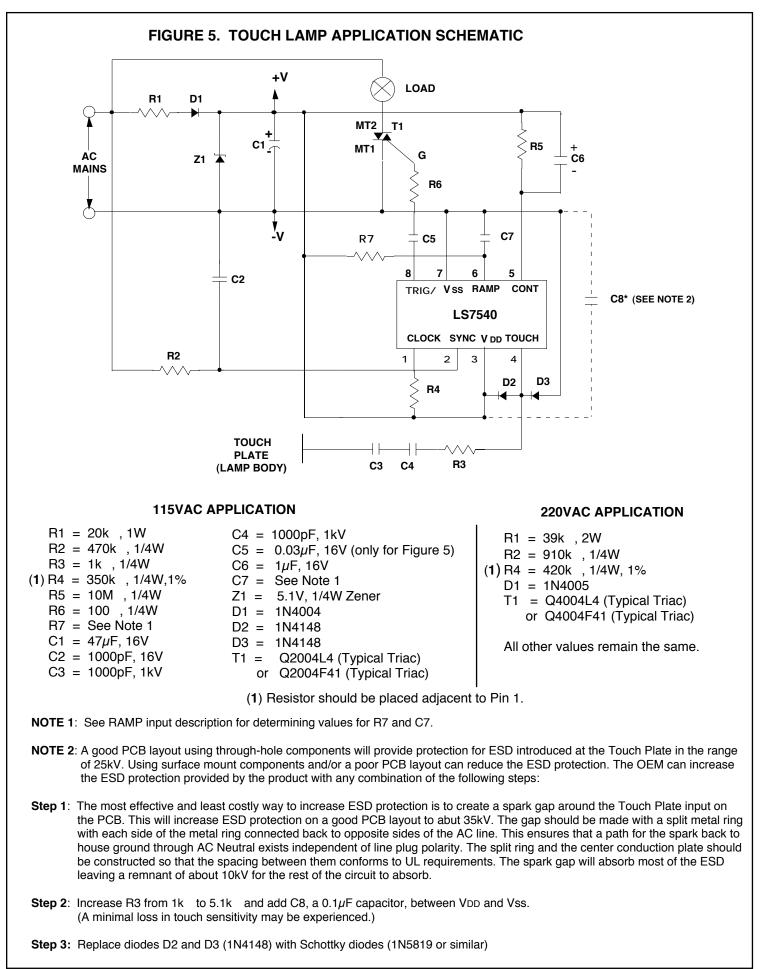
#### TRIG/ Output (Pin 8)

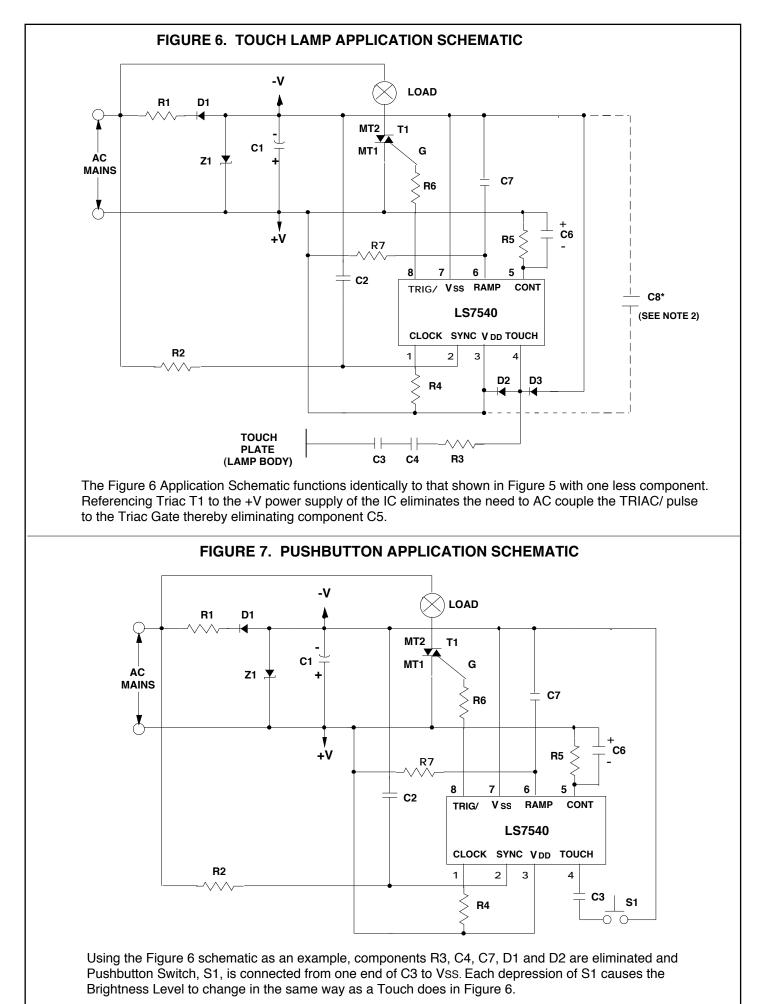
The TRIG/ output produces a negative pulse every half cycle of the AC line period to turn on a triac in series with the lamp or any other load device. The brightness of the lamp is regulated by regulating the conduction angle,  $\emptyset$  (See Fig. 2) of the TRIG/ signal. The conduction angles at maximum brightness (level 124) and mimimum brightness (level 1) are 162° and 50° with 0.91° incremental steps.

ABSOLUTE MAXIMUM RATINGS:						
PARAMETER			SYMBOL		VALUE	UNIT
DC supply voltage Any input voltage Operating temperature Storage temperature			VDD - VSS VIN TA TSTG	-	+6 ).3 to VDD + 0 20 to +85 65 to +150	0.3 V °C °C
ELECTRICAL AND TRANSIENT CHARACTERISTICS: ( All voltages referenced to Vss. TA = +25°C unless otherwise specified.)						
PARAMETER	SYMBOL	MIN	ТҮР	MAX	UNIT	CONDITIONS
Supply Voltage Supply Current	Vdd Idd	+4.5 -	5.0 -	+5.5 500	V uA	- Output off, VDD = +5.0V
<b>TRIG/</b> Sink Current Vo = VDD - 3V	lo	-50	-	-	mA	VDD = +5.0V
<b>TRIG/</b> Source Current Vo = VDD - 0.2V	ю	+0.1	-	-	mA	VDD = +5.0V
TRIG/ Pulse Width	Tw -	-	105 126	-	μsec μsec	RC = 300k ,60Hz RC = 360k ,50Hz
CLOCK Resistor	-	-	350 420	-	k k	60Hz 50Hz
CONTROL Resistor CONTROL Capacitor	-	-	10 1	-	M µF	
Touch Plate Capacitance	-	-	-	600	pF	-
Touch Duration	Ts Ts	67 80	-	-	ms ms	60Hz 50Hz
Conduction Angle	Ø	50	-	162	deg	-
Ø incremental steps (Note 1)	Ø	-	0.91	-	deg	-
RAMP Resistor RAMP Capacitor	R C	2 200	-	no limit no limit	k pF	-
RAMP RC RAMP RC	TRC TRC	25 30	-	no limit no limit	μs μs	60Hz 50Hz
RAMP Rate	-	-	4TRC	-	sec/step	-
Max to off slew time	Тт	-	496TRC	-	sec	-

**Note 1**: Total number of steps = 124.







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