

Quad high speed differential line receiver

26LS32/26LS32A

FEATURES

- Input voltage range of 30 volts differential for 26LS32 and 25V differential for 26LS32A
- $\pm 0.2V$ sensitivity over the input voltage range of $-7V$ to $+7V$
- 6k minimum input impedance
- 60mV input hysteresis
- The 26LS32/32A meets all the requirements of RS-422 and RS-423
- Operation from single +5V
- Fail safe input-output relationship. Output always high when inputs are open.
- Three-state drive, with choice of complementary output enables, for receiving directly onto a data bus
- Three-state outputs disabled during power up and power down

ORDERING INFORMATION

DESCRIPTION	ORDER CODE	PACKAGE DESIGNATOR*
16-Pin Ceramic DIP	26LS32/BEA 26LS32A/BEA	GDIP1-T16
16-Pin Flat Pack	26LS32/BFA 26LS32A/BFA	GDFP2-F16
20-Pin Ceramic LLCC	26LS32/B2A 26LS32A/B2A	CQCC2-N20

* MIL-STD 1835 or Appendix A of 1995 Military Data Handbook

FUNCTION TABLE (EACH RECEIVER)

DIFFERENTIAL INPUT	ENABLES		OUTPUT
	EN	EN	
$V_{ID} \geq V_{TH}$	H	X	H
$V_{TL} \leq V_{ID} \leq V_{TH}$	H	X	?
$V_{ID} \leq V_{TL}$	X	L	?
X	L	H	Z

H = High level
 L = Low level
 X = Irrelevant
 Z = High impedance (off)
 ? = Indeterminate
 EN = Enable
 EN = Enable

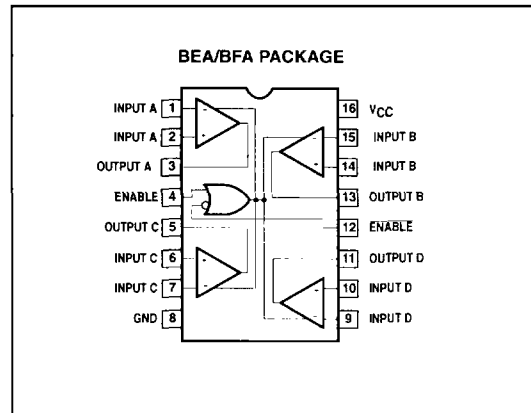
DESCRIPTION

The 26LS32/32A is a quad line receiver designed to meet all of the requirements of RS-422 and RS-423 and Federal Standards 1020 and 1030 for balanced and unbalanced digital data transmission.

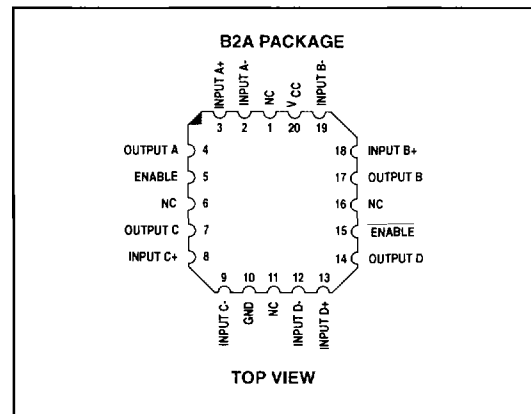
The 26LS32/32A features an input sensitivity of $\pm 200mV$ over the common mode input range of $\pm 7V$.

The 26LS32/32A provides an enable and disable function common to all four receivers. Both the parts feature 3-State outputs with 8mA sink capability and incorporates a fail-safe input-output relationship which forces the outputs high when the inputs are open.

PIN CONFIGURATION



LLCC LEAD CONFIGURATION



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ABSOLUTE MAXIMUM RATINGS¹

SYMBOL	PARAMETER	RATING	UNIT
V _{CC}	Power supply	7	V
V _{EN}	Enable voltage	7	V
I _O	Output sink current	50	mA
V _{CMV}	Common mode range	±25	V
V _{TH}	Differential input voltage	±30	V
T _{STG}	Storage temperature range	-65 to +150	°C

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	LIMITS			UNIT
		MIN	NOM	MAX	
V _{CC}	Supply voltage	4.5	5.0	5.5	V
T _{amb}	Operating free-air temperature range	-55		+125	°C

DC ELECTRICAL CHARACTERISTICS

(Over recommended operating temperature and supply voltage range unless otherwise specified.)

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP ²	MAX	
V _{TH}	Differential input voltage	V _{OUT} = V _{OL} or V _{OH} , -7V ≤ V _{CM} ≤ +7V	-0.2	0.06	+0.2	V
R _{IN}	Input resistance ³	V _{CC} = Nom, -15V ≤ V _{CM} ≤ +15V, (One input AC ground)	6.0	9.8		kΩ
I _{IN}	Input current	V _{IN} = +15V, V _{CC} = Nom Other input ⁷ -15V ≤ V _{IN} ≤ +15V			2.3	mA
I _{IN}	Input current	V _{IN} = -15V, V _{CC} = Nom Other input ⁸ -15V ≤ V _{IN} ≤ +15V			-2.8	mA
V _{OH}	Output High voltage	V _{CC} = MIN, I _{OH} = -440μA ΔV _{IN} = +1.0V, VENABLE = 0.8V	2.5	3.4		V
V _{OL}	Output Low voltage	V _{CC} = MIN, VENABLE = 0.8V, ΔV _{IN} = -1.0V	I _{OL} = 4.0mA	0.3	0.4	V
			I _{OL} = 8.0mA		0.45	V
V _{IL}	Enable Low voltage	V _{CC} = 5.5V			0.8	V
V _{IH}	Enable High voltage	V _{CC} = 5.5V	2.0			V
V _I	Enable clamp voltage	V _{CC} = MIN, I _{IN} = -18mA			-1.5	V
I _O	Off state (high impedance) output current	V _{CC} = MAX	V _O = 2.4V		20	μA
			V _O = 0.4V		-20	μA
I _{IL}	Enable Low current	V _{IN} = 0.4V, V _{CC} = MAX		-0.2	-0.36	mA
I _{IH}	Enable High current	V _{IN} = 2.7V, V _{CC} = MAX		0.5	20	μA
I _I	Enable input High current	V _{IN} = 5.5V, V _{CC} = MAX		1	100	μA
I _{SC}	Output short circuit current	V _{CC} = MAX, ΔV _{IN} = +1V, V _{OUT} = 0V	-15	-60	-85	mA
I _{CC}	Power supply current	V _{CC} = MAX; All V _{IN} = GND, outputs disabled		52	70	mA
V _H	Input hysteresis	T _{amb} = 25°C, V _{CC} = 5.0V, V _{CM} = 0V		60		mV

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AC ELECTRICAL CHARACTERISTICS $T_{amb} = +25^{\circ}\text{C}$, $V_{CC} = 5.0\text{V}$

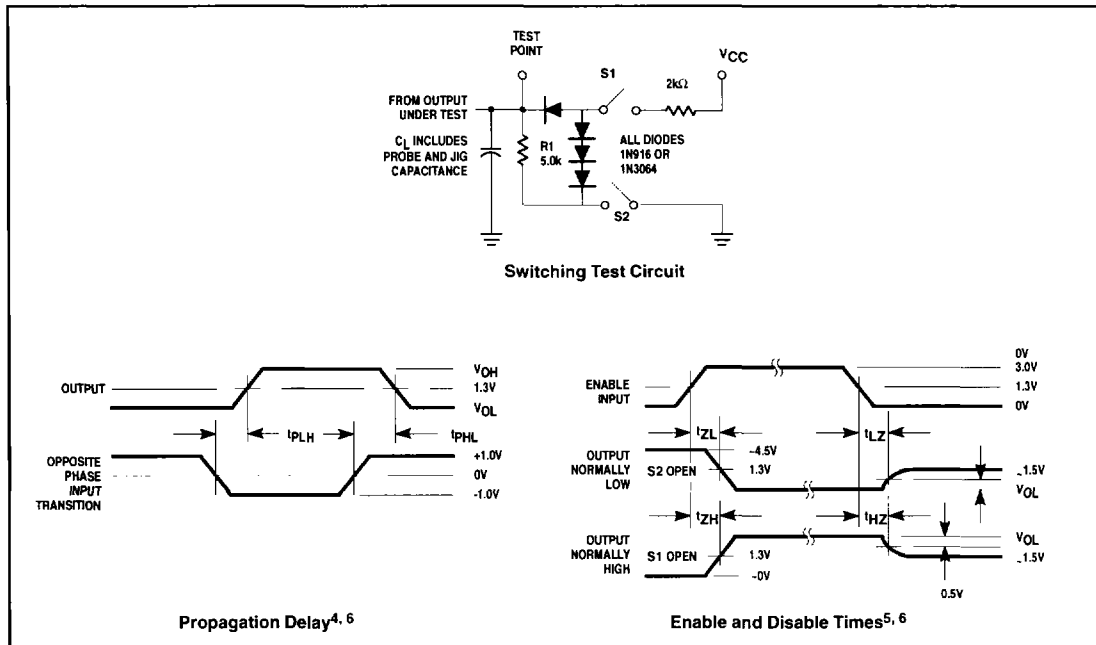
SYMBOL	PARAMETER	TEST CONDITIONS	26LS32 LIMITS			26LS32A LIMITS		UNIT
			MIN	TYP ¹	MAX	MIN	MAX	
t_{PLH}	Input to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		9	25		35	ns
t_{PHL}	Input to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		10	25		35	ns
t_{LZ}	Enable to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		15	30		40	ns
t_{HZ}	Enable to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		12	22		30	ns
t_{ZL}	Enable to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		8	22		25	ns
t_{ZH}	Enable to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		8	22		25	ns

AC ELECTRICAL CHARACTERISTICS $-55^{\circ}\text{C} \leq T_{amb} \leq +125^{\circ}\text{C}$, $V_{CC} = 5.0\text{V}$

SYMBOL	PARAMETER	TEST CONDITIONS	26LS32 LIMITS		26LS32A LIMITS		UNIT
			MIN	MAX	MIN	MAX	
t_{PLH}	Input to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		38		53	ns
t_{PHL}	Input to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		38		53	ns
t_{LZ}	Enable to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		45		60	ns
t_{HZ}	Enable to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		33		45	ns
t_{ZL}	Enable to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		33		38	ns
t_{ZH}	Enable to output	See switching test circuit and waveforms. $C_L = 15\text{pF}$		33		38	ns

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NOTES:

1. Stresses above those listed under "Absolute Maximum Ratings" may cause malfunction or permanent damage to the device.
2. Typical values are at $T_{amb} = +25^{\circ}C$, $V_{CC} = 5.0V$.
3. This parameter is guaranteed by correlation, but not tested.
4. Diagram shown for Enable Low.
5. S1 and S2 of load circuit are closed except where shown.
6. Pulse Generator for all pulses: Rate $\leq 1.0MHz$, $Z_o = 50\Omega$, $t_r \leq 15ns$, $t_f \leq 6.0ns$.
7. For 26LS32A other input $-10V \leq V_{IN} \leq +15V$.
8. For 26LS32A other input $-15V \leq V_{IN} \leq +10V$.