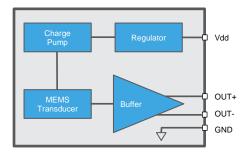
# ANALOG BOTTOM PORT SISONIC™ MICROPHONE



The SPH8878LR5H-1 is a high-performance, bottom port analog silicon microphone, supporting both single-ended and differential modes. Using Knowles' proven high performance SiSonic™ MEMS technology, the SPH8878LR5H-1 consists of an acoustic sensor, a low noise input buffer, and an output amplifier. The microphone has a flat frequency response with low LFRO and phase distortion enabling superior noise cancelling algorithm performance. Its high, 67dB(A) SNR and 134 dB SPL AOP provides a large, distortion-free dynamic range for high-quality voice pickup.

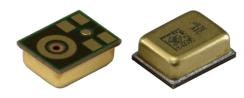


## ABSOLUTE MAXIMUM RATINGS

Table 1: Absolute Maximum Ratings

Parameter	Absolute Maximum Rating	Units
Vdd to Ground	-0.5, +5.0	V
OUT+, OUT- to Ground	-0.3, Vdd+0.3	V
Input Current	±5	mA
Storage Temperature	-40 to +125	°C
Operating Temperature	-40 to +85	°C

Stresses exceeding these "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation at these or any other conditions beyond those indicated under "Acoustic & Electrical Specifications" is not implied. Exposure beyond those indicated under "Acoustic & Electrical Specifications" for extended periods may affect device reliability.



#### **PRODUCT FEATURES**

- Flat Frequency Response for ANC
- Low phase disortion
- High SNR and High AOP
- Bottom Port
- Ultra-Stable Performance
- Matched Sensitivity
- LGA Package
- Omnidirectional
- Standard SMD Reflow
- Wide Temperature Operating Range

#### TYPICAL APPLICATIONS

- Smart Speakers
- ANC Headsets
- Beam Forming Applications
- Proximity Detection





# ACOUSTIC & ELECTRICAL SPECIFICATIONS<sup>1</sup>

#### Table 2: Normal Mode Microphone Specifications

Test Conditions: 23 ±2°C, 55±20% R.H., Vdd=2.75V, no load, unless otherwise indicated

Parameter	Symbol	Conditions	Min	Тур	Max	Units
Supply Voltage	Vdd		2.3	2.75	3.6	V
Committee Committee		Vdd = 3.6 V	-	265	-	
Supply Current	Idd	Vdd = 2.75 V	-	250	-	- μΑ
Concitivity	S	94 dB SPL @ 1kHz, Single-Ended	-45	-44	-43	- dBV/Pa
Sensitivity	5	94 dB SPL @ 1kHz, Differential	-39	-38	-37	аву/Ра
Signal to Naise Datio	SNR	94 dB SPL @ 1kHz, A-weighted, Single-Ended Mode	-	67	-	- dBV/Pa
Signal to Noise Ratio	SINK	94 dB SPL @ 1kHz, A-weighted, Differential Mode	-	67	-	dBV/Pa
Noor I litropopio CND		94 dB SPL, @ 19 kHz , BW = 18.5 - 20.0 kHz, SE	-	79	-	dD.
Near-Ultrasonic SNR		94 dB SPL, @ 19 kHz , BW = 18.5 - 20.0 kHz, D	-	79	-	- dB
Total Harmonia Distortion	TUD	94 dB SPL @ 1 kHz	-	0.05	-	%
Total Harmonic Distortion	THD	115 dB SPL @ 1 kHz	-	0.4	-	%
		1% THD @ 1 kHz, S = typ	-	124	-	dB SPL
Acoustic Overload Point	AOP	10% THD @ 1 kHz, S = typ	-	134	-	dB SPL
Low Frequency Rolloff	LFRO	-3dB relative to 1 kHz	-	7	-	Hz
High Frequency Flatness		+3dB relative to 1 kHz	-	19	-	kHz
Resonant Frequency Peak	Fres		-	36	-	kHz
Power Supply Pojection Potio	PSRR	200 mVpp sinewave @ 1 kHz, Single-Ended Mode	-	86	-	- dB
Power Supply Rejection Ratio	PSKK	200 mVpp sinewave @ 1 kHz, Differential Mode	-	85	-	UD
Power Supply Rejection	PSR+N	200 mVpp 7/8 duty cycle rectangular waveform @ 217 Hz, A-weighted, BW = 20 kHz, SE	-	-103	-	dBV(A)
Tower Supply Rejection	TORTH	200 mVpp 7/8 duty cycle rectangular waveform @ 217 Hz, A-weighted, BW = 20 kHz, Differential	-	-103	-	dbv(A)
DC Output		Vdd = 2.75V	-	0.69	-	V
DC Offset		OUT+ to OUT-	-	-	±20	mV
Output Impedance	Zout	@ 1 kHz	-	380	-	Ω
Output Load	Cload			-	-	pF
Output Load	Rload	AC-coupled	10	-	-	kΩ
Sensitivity Drop		Vdd(min) ≤ Vdd ≤ Vdd(max)	-	-	±0.25	dB
Directivity			Omnidirectional			
Polarity		Increasing sound pressure Increasing Output Voltage		ge		
Startup Time		S within 1 dB of final value, outputs AC coupled	7	-	15	ms





Table 3: Low-Power Mode Microphone Specifications

Test Conditions: 23 ±2°C, 55±20% R.H., Vdd=1.8V, no load, unless otherwise indicated

Parameter	Symbol	Conditions	Min	Тур	Max	Units	
Supply Voltage	Vdd		1.6	1.8	1.9	V	
Comments Comment		Vdd = 1.8 V	-	100	-	_	
Supply Current	Idd	Vdd = 1.6 V	-	95	-	- μΑ	
Sensitivity	S	94 dB SPL @ 1kHz, Single-Ended	-45	-44	-43	- dBV/Pa	
Sensitivity	3	94 dB SPL @ 1kHz, Differential	-39	-38	-37	иву/Ра	
Signal to Naisa Batia	SNR	94 dB SPL @ 1kHz, A-weighted, Single-Ended Mode	-	66	-	- dBV/Pa	
Signal to Noise Ratio	SINK	94 dB SPL @ 1kHz, A-weighted, Differential Mode	-	64.5	-	UDV/Fa	
Noor I Iltranopio CND		94 dB SPL, @ 19 kHz , BW = 18.5 - 20.0 kHz, SE	-	78	-	- dB	
Near-Ultrasonic SNR		94 dB SPL, @ 19 kHz , BW = 18.5 - 20.0 kHz, D	-	76.5	-	UD UD	
Total Harmonic Distortion	THD	94 dB SPL @ 1 kHz	-	0.05	-	%	
Total Harmonic Distortion	וחט	115 dB SPL @ 1 kHz	-	0.4		%	
		1% THD @ 1 kHz, S = typ	-	125	-	dB SPL	
Acoustic Overload Point	AOP	10% THD @ 1 kHz, S = typ	-	131.5	-	dB SPL	
Low Frequency Rolloff	LFRO	-3dB relative to 1 kHz	-	7	-	Hz	
High Frequency Flatness		+3dB relative to 1 kHz	-	19	-	kHz	
Resonant Frequency Peak	Fres		-	36	-	kHz	
Power Supply Rejection Ratio	PSRR	200 mVpp sinewave @ 1 kHz, Single-Ended Mode	-	73	-	dP	
Power Supply Rejection Ratio	FORK	200 mVpp sinewave @ 1 kHz, Differential Mode	-	73	-	- dB	
Power Supply Rejection	PSR+N	200 mVpp 7/8 duty cycle rectangular waveform @ 217 Hz, A-weighted, BW = 20 kHz, SE	-	-97	-	dBV(A)	
Tower cupply regestion		200 mVpp 7/8 duty cycle rectangular waveform @ 217 Hz, A-weighted, BW = 20 kHz, Differential	-	-96	-	dBV(//)	
DC Output		Vdd = 1.8V	-	0.69	-	V	
DC Offset		OUT+ to OUT-		-	±20	mV	
Output Impedance	Zout	@ 1 kHz	-	-	380	Ω	
Output Load	Cload		-	-	-	pF	
Output Load	Rload	AC-coupled	10	-	-	kΩ	
Sensitivity Drop		Vdd(min) ≤ Vdd ≤ Vdd(max)	-	-	±0.25	dB	
Directivity			Omnidirectional				
Polarity		Increasing sound pressure	Increasing Output Voltage				
Startup Time		S within 1 dB of final value, outputs AC coupled	<i>-</i>	-	15	ms	

 $<sup>^{\</sup>rm 1}\,{\rm Sensitivity}$  and Supply Current are 100% tested.





Figure 1: Typical Single-Ended Application Circuit

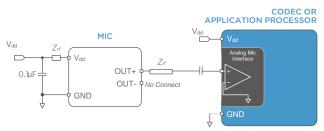
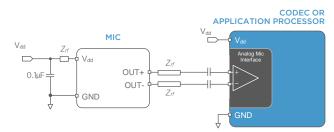


Figure 2: Typical Differential Mode Application Circuit



#### **NOTES**

All Ground pins must be connected to ground.

If necessary to improve RF performance, optional series components (resistors, ferrites, etc.) should be placed closest to the microphone pads.

Capacitors near the microphone should not contain Class 2 dielectrics due to their piezoelectric effect.

Bypass capacitors should be placed next to each Vdd pin for best performance. Follow the codec manufacturer's recommendations for circuitry and layout.

## PERFORMANCE CURVES

Test Conditions: 23 ±2°C, 55±20% R.H., Vdd=2.75V (NM) / 1.8V (LPM), no load, unless otherwise indicated

Figure 3: Typical Free Field Magnitude Response and Masks

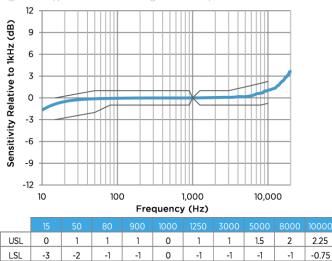


Figure 5: Typical Phase Response and Group Delay

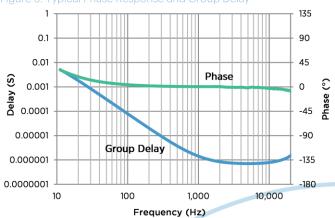


Figure 4: Typical THD vs Input SPL

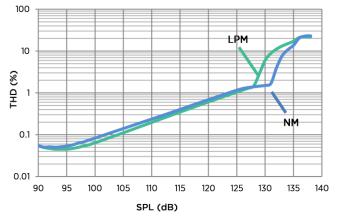
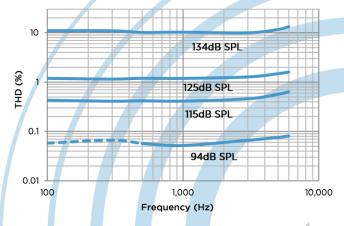


Figure 6: Typical THD vs Input Frequency



knowles.com | DATASHEET SPH8878LR5H-1 Rev A © 2021, Knowles Electronics, LLC, Itasca, IL, USA, All Rights Reserved. SiSonic, Knowles and the logo are trademarks of Knowles Electronics, LLC.



Figure 7: Typical Free Field Ultrasonic Response

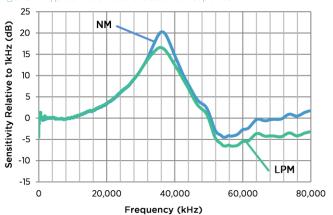


Figure 10: Typical Idd vs Vdc

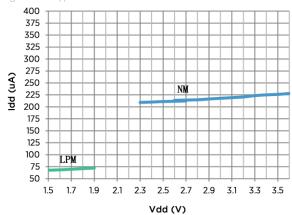


Figure 8: Noise Floor Power Spectral Density (LPM)

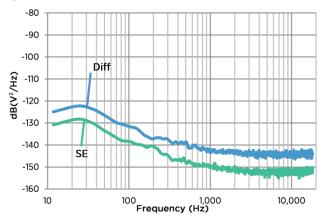


Figure 11: Noise Floor Power Spectral Density (NM)

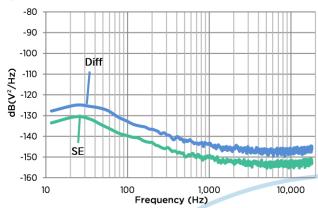


Figure 9: Typical PSRR (LPM)

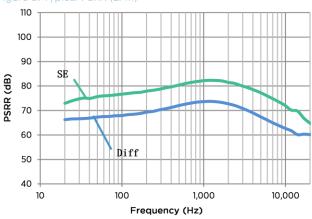
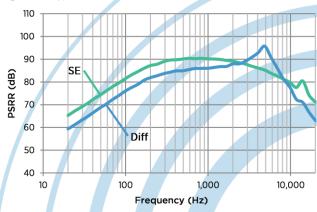


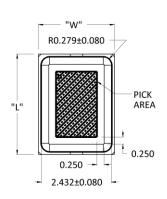
Figure 12: Typical PSRR (NM)

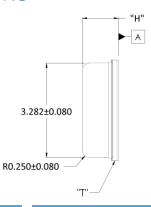


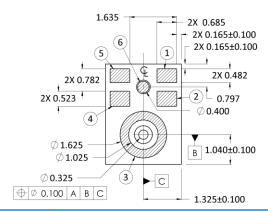




## **MECHANICAL SPECIFICATIONS**



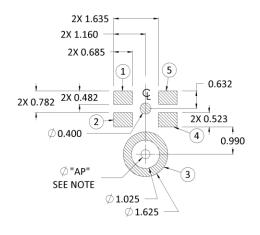




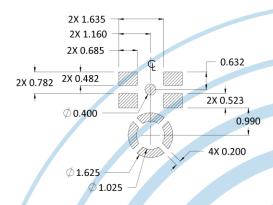
Item	Dimension	Tolerance
Length (L)	3.50	±0.10
Width (W)	2.65	±0.10
Height (H)	1.26	±0.10
Acoustic Port (AP)	Ø0.325	±0.05
PCB Thickness (T)	0.25	±0.05

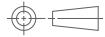
Pin#	Pin Name	Туре	Description
1	OUT (+)	Signal	Output
2	GROUND	Power	Ground
3	GROUND	Power	Ground
4	GROUND	Power	Ground
5	Vdd	Power	Power Supply
6	OUT (-)	Signal	Output

## **Example Land Pattern**



## **Example Solder Stencil Pattern**





Pick Area only extends to 0.25 mm of any edge or hole unless otherwise specified.

Dimensions are in millimeters unless otherwise specified.

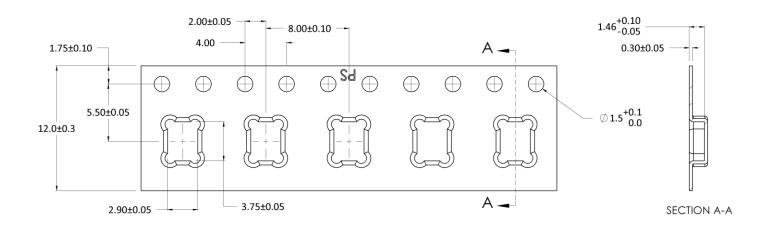
Tolerance is  $\pm 0.15$ mm unless otherwise specified.

In the acoustic path, the recommended PCB Hole Diameter is  $0.6 \le D \le 1.0$ mm, the recommended Gasket Cavity Diameter is  $D \ge 1.0$ mm and the recommended Case Hole Diameter is  $1.0 \le D \le 1.5$ mm. Further optimizations based on application should be performed.



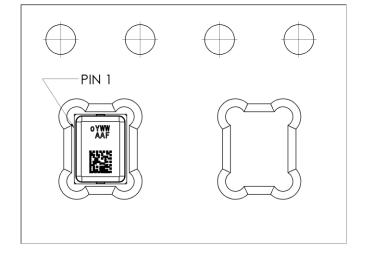


## PACKAGING & MARKING DETAIL



Model Number	Suffix	Reel Diameter	Quantity Per Reel
SPH8878LR5H-1	-6	13"	4800

Component	Surface Resistance (ohms)
Reel	10 <sup>5</sup> - 10 <sup>9</sup>
Carrier Tape	10 <sup>5</sup> - 10 <sup>9</sup>
Cover Tape	10 <sup>4</sup> - 10 <sup>10</sup>



Date Code YWW:

Y: Last digit of year

WW: Work week

AA = Internal KN Code F = Factory Location:

M: Knowles Factory KEM3

C: Knowles Factory KES2

P: Knowles KEI

2D barcode "ABCDEFGHIJKLMNOP":

Unique Job Identification Number for product traceability

#### **NOTES**

Dimensions are in millimeters unless otherwise specified.

Vacuum pickup only in the pick area indicated in Mechanical Specifications.

Tape & reel per EIA-481.

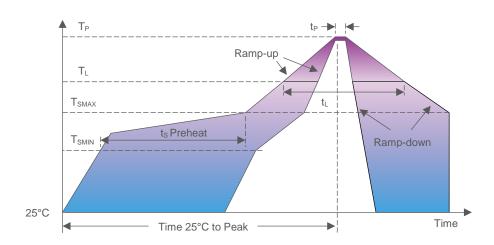
Labels applied directly to reel and external package.

Shelf life: Twelve (12) months when devices are stored in the factory-supplied, unopened ESD moisture sensitive bag under the maximum environmental conditions of 30°C, 70% R.H.





## RECOMMENDED REFLOW PROFILE



Profile Feature	Pb-Free
Average Ramp-up rate (T <sub>SMAX</sub> to T <sub>P</sub> )	3°C/second max.
Preheat  Temperature Min (T <sub>SMIN</sub> )  Temperature Max (T <sub>SMAX</sub> )  Time (T <sub>SMIN</sub> to T <sub>SMAX</sub> ) (t <sub>S</sub> )	150°C 200°C 60-180 seconds
Time maintained above:  Temperature (T <sub>L</sub> ) Time (t <sub>L</sub> )	217°C 60-150 seconds
Peak Temperature (T <sub>P</sub> )	260°C
Time within 5°C of actual Peak Temperature (t <sub>P</sub> )	20-40 seconds
Ramp-down rate (T <sub>P</sub> to T <sub>SMAX</sub> )	6°C/second max
Time 25°C to Peak Temperature	8 minutes max

#### NOTES:

Based on IPC/JEDEC J-STD-020 Revision C.

All temperatures refer to topside of the package, measured on the package body surface.

The actual reflow profile used should be optimized based on the reflow requirements of all components, board design, solder paste formulation and reflow equipment used. Details of recommended handling and manufacturing processes can be found in AN25 SMT Manufacturing Guidelines for SiSonic™ Microphones.

#### **ADDITIONAL NOTES**

- (A) MSL (moisture sensitivity level) Class 1.
- (B) Maximum of 3 reflow cycles is recommended.
- (C) In order to minimize device damage:
  - Do not board wash or clean after the reflow process.
  - Do not brush board with or without solvents after the reflow process.
  - Do not directly expose to ultrasonic processing, welding, or cleaning.
    Do not insert any object in port hole of device at any time.
  - Do not apply over 30 psi of air pressure into the port hole.
  - Do not pull a vacuum over port hole of the microphone.
  - Do not apply a vacuum when repacking into sealed bags at a rate faster than 0.5 atm/sec.
  - Do not directly expose to vapor phase soldering.





## **MATERIALS STATEMENT**

Meets the requirements of the European RoHS directive 2011/65/EC as amended.

Meets the requirements of the industry standard IEC 61249-2-21:2003 for halogenated substances and Knowles Green Materials Standards Policy section on Halogen-Free.

Product is Beryllium Free according to limits specified on the Knowles Hazardous Material List (HSL for Products).

Ozone depleting substances are not used in the product or the processes used to make the product, including compounds listed in Annex A, B, and C of the "Montreal Protocol on Substances That Deplete the Ozone Layer.

## **RELIABILITY SPECIFICATIONS**

Test	Description
Thermal Shock	100 cycles of air-air thermal shock from -40°C to +125°C with 15 minute soaks (IEC 68-2-14)
High Temperature Storage	+105°C, 1,000 hours per JESD22-A103 (See Notes)
Low Temperature Storage	-40°C, 1,000 hours per JESD22-A119 (See Notes)
High Temperature Bias	+140°C, 168 hours (equivalent to +105°C, 1,000 hours per JESD22-A108)
Low Temperature Bias	-55°C, 168 hours (equivalent to -40°C, 1,000 hours per JESD22-A108)
Temperature/Humidity Bias	+85°C/85% R.H., 1,000 hours (JESD22-A101A-B)
Vibration	16 minutes in each X, Y, Z axis from 20 to 2,000 Hz with peak acceleration of 20g (MIL STD-883e, Method 2007.2, Condition A)
ESD-HBM	3 discharges at ±2kV direct contact to I/O pins (ANSI/ESDA/JEDEC JS-001-2014)
ESD-HMM	10 discharges at ±8kV direct contact to lid when unit is grounded (ANSI/ESD SP5.6-2009)
ESD-CDM	3 discharges at ±500V (ANSI/ESDA/JEDEC JS-002-2014)
Reflow	5 reflow cycles with peak temperature of +260°C (JEDEC 22-A113F)
Mechanical Shock	3 pulses of 12,000g in each of the X, Y, and Z directions (IEC 68-2-27 Test Ea)

#### NOTES

Microphones meet all acoustic and electrical specifications before and after reliability testing, except sensitivity which can deviate up to 3dB.

After 3 reflow cycles, the sensitivity of the microphones shall not deviate more than 1 dB from its initial value

Temperature Storage testing is covered by Temperature Bias testing as Ta = Tj for Knowles Microphones.





# **SPECIFICATION REVISIONS**

Revision	Specification Changes	Date
А	Initial Release (ECR-21-4397)	6/10/21

Information contained herein is subject to change without notice. It may be used by a party at their own discretion and risk. We do not guarantee any results or assume any liability in connection with its use. This publication is not to be taken as a license to operate under any existing patents.

