

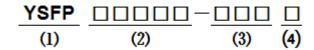
#### Features

- Assemblage design, sturdy structure.
- High inductance, high current, low magnetic loss, low ESR, small parasitic capacitance.
- Flat wire winding, achieve alow D.C.Resistance.
- Temperature rise current and saturation current is less influenced by environment.
- Operating temperature range:-40°C ~ +125°C.

#### Applications

- Low profile, high current power supplies.
- Battery powered devices.
- DC/DC converters in distributed power systems.
- DC/DC converters for field programmable gate array.

#### Product Identification



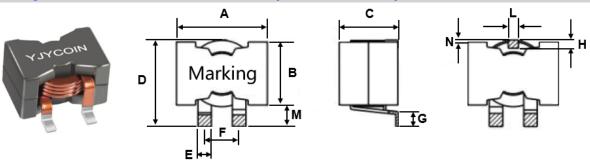
(1) : Type

(2): Dimensions

(3): Inductance value

(4): Inductance Tolerance: M=±20%,K=±10%,J=±5%

### Shapes and Dimensions (Unit: mm)



TYPE	A Max.	B Max.	С	D Max.	E	F	G Min.	Н	٦	М	Z
YSFP2918S	28.0	19.7	18.5±0.5	27.0	4.0±0.3	10±0.5	3.8	3.5	3.0	6.5±1.0	0.5

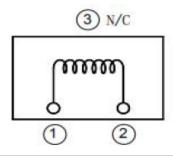


### **■** Electrical requirements

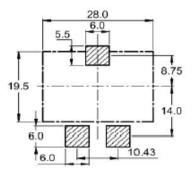
Part Number	L (uH)	Test Freq.	DCR Max.(mΩ)	I sat (A)	I rms (A)
YSFP2918S-470M	47±20%	100KHz/0.25V	2.5	6.5	30

- ※ All test data is based on 25 °C ambient.
- % DC current(A) that will cause an approximate  $\Delta$ T40 °C.
- \* DC current(A) that will cause L0 to drop approximately 30% Typ.
- ※ The part temperature (ambient + temp rise) should not exceed 125℃ under worst case operating conditions. Circuit design,component.PWB trace size and thickness,airflow and other cooling provision all affect the part temperature.Part temperature should be verified in the den application.

#### **■** Electrical schematics

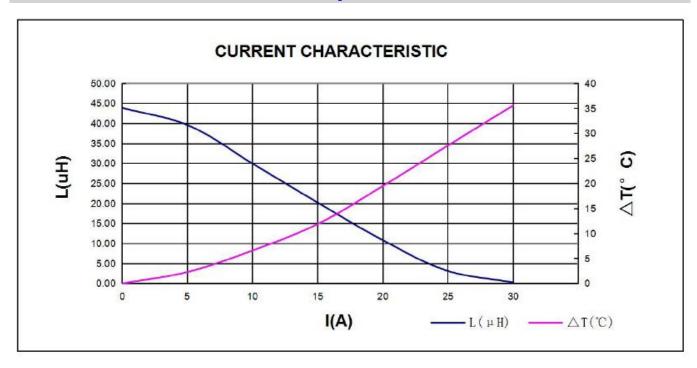


#### Recommended PCB Layout





## ■ Saturation current VS temperature rise current curve



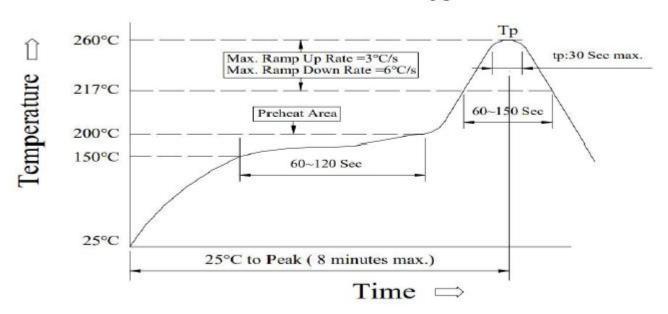


## Reliability

Item	Specification and Requirement	Test Method		
	Tamainala ana must baya 050/ min addan	Solder heat proof:		
Solder a bility test	Terminals area must have 95% min solder	①Preheating:160±10℃ for 90 seconds		
	coverage	②Retention time:245±5℃ for 2±0.5 seconds		
		① Vibration frequency:(10Hz to 55Hz to		
	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	10Hz) in 60 seconds as a period		
Vibration test	Inductance change:Within±5% Without	② Vibration time: Reriod cycled for 2 hours		
	Mechanical damage such as break	in each of 3 mutual perpendicular directions.		
		③ Amplitude:1.5mm Max.		
		① Peak value:100G.		
01	Inductance change: Within±5% Without	② Duration of pulse:11ms.		
Shock test	Mechanical damage such as break	③ Times in each positive and negative		
		direction of 3 mutual perpendicular directions		
		① Repeat 100 cycle as follow (-55±2℃		
		30±3 minutes),Room temperature,5 minutes		
The amount of the sale	Inductance change: Within±5% Without	(+125±2℃,30±3 minutes)		
Thermal shock	Mechanical damage such as break	② Recovery:48+4/-0 hours of recovery		
		Under the standard condition after the test.		
		(see Note 1)		
lligh tomporature	Industrance change, Within LEO/ Without	① Environment condition:85±2°C		
High temperature	Inductance change: Within±5% Without	Applied current:Rated current		
life test	Mechanical damage such as break	② Duration:1000+4/-0 hours(see Note 1)		
		① Environment condition:60±2°C		
Humidity	Inductance change: Within±5% Without	Humidity:90-95%		
Resistance	Mechanical damage such as break	Applied current:Rated current		
		② Duration:1000+4/-0 hours(see Note 1)		
Low temperature	Inductance change: Within±5% Without	Store temperature -55±±2℃ for total		
life test	Mechanical damage such as break	1000+4/-0 hours		
High temperature	Inductance change: Within±5% Without	Store temperature +125±2°ℂfor total		
life test	Mechanical damage such as break	1000+4/-0 hours		

#### **■ Reflow Profile**

#### Power Choke Coil Type



### **■ Reflow Soldering Method**

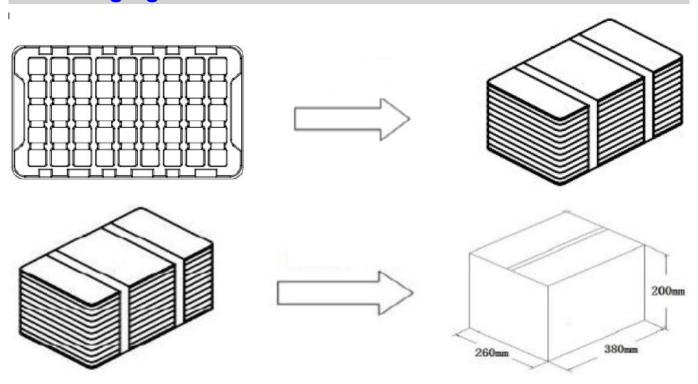
Poflow Soldering	Tp:255 ~ 260°C Max. 30 seconds(tp)			
Reflow Soldering	217℃ 60 ~ 150 seconds			
Pre-Heat	150 ~ 200°C 60 ~ 120 seconds			
Time 25℃ to peak temperature	8 minutes Max.			

### Soldering iron method

350±5°C Max.3 seconds.



## Packaging



Product Series	Quantity/Tray	Quantity/Carton
YSFP2918S	40 PCS	280 PCS