

Octal Buffer / Driver With 3-State Outputs

1 FEATURES

- **Power-Supply Range: 2.0V to 5.5V**
- **High-Current 3-State outputs Interface**
- **Low Power Consumption: 5 μ A ICC(Max)**
- **TTL Input are Compatible**
- **± 16 mA Output Drive at 5V**
- **Low Input Current of 2 μ A Max**
- **Extended Temperature: -40°C to 125°C**
- **Micro SIZE PACKAGES: TSSOP-20, SOIC-20(SOP20)**

2 APPLICATIONS

- **Servers**
- **Smart Grids**
- **Network Switches**
- **Infotainment**
- **Surveillance Cameras**

3 DESCRIPTIONS

The RS540T is an 8-bit inverting buffer/line driver with 3-state outputs. The device features two output enables ($\overline{OE}1$ and $\overline{OE}2$). A High on \overline{OE} causes the associated outputs to assume a high-impedance OFF-state.

Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times. This device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

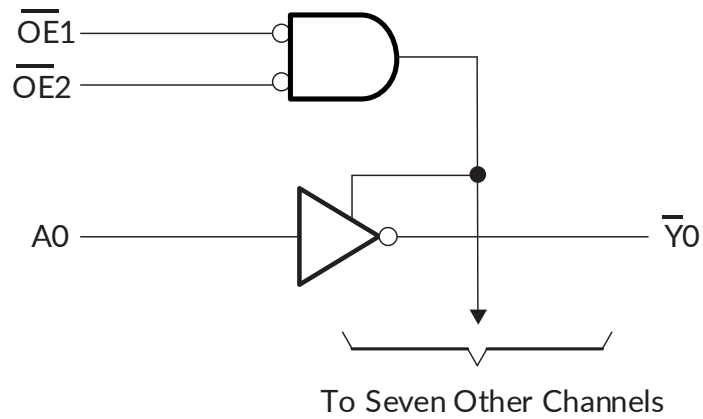
To ensure the high-impedance state during power up or power down, \overline{OE} should be tied to V_{CC} through a pullup resistor, the minimum value of the resistor is determined by the current-sinking capability of the driver.

Device Information ⁽¹⁾

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RS540T	TSSOP-20(20)	6.50mm×4.40mm
	SOIC-20 (SOP20)(20)	12.80mm×7.50mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

4 Functional Block Diagram



Function Table

INPUTS			OUTPUT
$\overline{OE1}$	$\overline{OE2}$	A PORT	\overline{Y} PORT
L	L	L	H
L	L	H	L
X	H	X	Z
H	X	X	Z

NOTE:
H=HIGH voltage level
L=LOW voltage level
X=Don't care
Z=High impedance OFF-state

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5 Revision History

Note: Page numbers for previous revisions may differ from page numbers in the current version.

VERSION	Change Date	Change Item
A.1	2023/09/25	Initial version completed
A.2	2024/01/05	Update PACKAGE/ORDERING INFORMATION

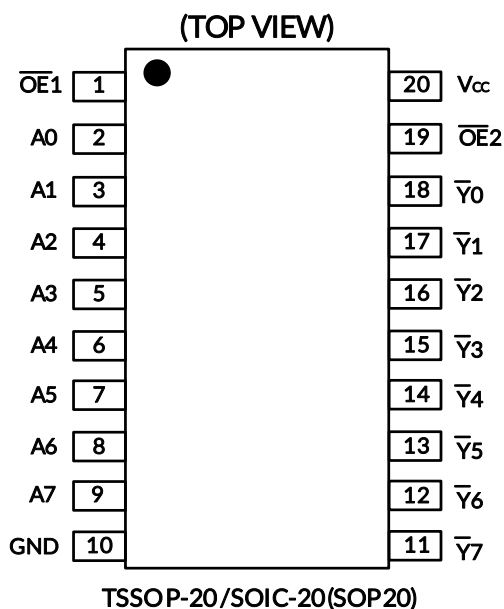
6 PACKAGE/ORDERING INFORMATION (1)

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING (2)	MSL(3)	PACKAGE OPTION
RS540T	RS540XTSS20-G	-40°C ~+125°C	TSSOP20	RS540T	MSL1	Tape and Reel,4000
	RS540TXS20	-40°C ~+125°C	SOIC-20 (SOP20)	RS540T	MSL3	Tape and Reel,1500

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information(data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

7 PIN CONFIGURATIONS



PIN DESCRIPTION

PIN	NAME	TYPE ⁽¹⁾	FUNCTION
TSSOP-20/ SOIC-20(SOP20)			
1	$\overline{OE1}$	I	Output Enable (Active Low). Pull $\overline{OE1}$ high to place all outputs in 3-state mode.
2	A0	I	Data Input
3	A1	I	Data Input
4	A2	I	Data Input
5	A3	I	Data Input
6	A4	I	Data Input
7	A5	I	Data Input
8	A6	I	Data Input
9	A7	I	Data Input
10	GND	G	Ground.
11	$\overline{Y7}$	O	Data Output
12	$\overline{Y6}$	O	Data Output
13	$\overline{Y5}$	O	Data Output
14	$\overline{Y4}$	O	Data Output
15	$\overline{Y3}$	O	Data Output
16	$\overline{Y2}$	O	Data Output
17	$\overline{Y1}$	O	Data Output
18	$\overline{Y0}$	O	Data Output
19	$\overline{OE2}$	I	Output Enable (Active Low). Pull $\overline{OE2}$ high to place all outputs in 3-state mode.
20	V _{CC}	P	Supply voltage: $2.0V \leq V_{CC} \leq 5.5V$

(1) I=input, O=output, P=power, G= Ground.

8 SPECIFICATIONS

8.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) ⁽¹⁾

SYMBOL	PARAMETER		MIN	MAX	UNIT
V _{CC}	Supply Voltage Range		-0.5	6.5	V
V _I ⁽²⁾	Input Voltage Range	A port	-0.5	6.5	V
		Control inputs	-0.5	6.5	V
V _O ⁽²⁾	Voltage range applied to any output in the high-impedance or power-off state	Y port	-0.5	6.5	V
V _O ⁽²⁾⁽³⁾	Voltage range applied to any output in the high or low state	Y port	-0.5	V _{CC} +0.5	V
I _{IK}	Input clamp current	V _I <0		-50	mA
I _{OK}	Output clamp current	V _O <0		-50	mA
I _O	Continuous output current			±50	mA
	Continuous current through V _{CC} or GND			±100	mA
θ _{JA}	Package thermal impedance ⁽⁴⁾	TSSOP-20		40	°C/W
		SOIC-20(SOP20)		40	
T _J	Junction Temperature ⁽⁵⁾		-40	150	°C
T _{stg}	Storage temperature		-65	150	

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

(3) The value of V_{CC} are provided in the recommended operating conditions table.

(4) The package thermal impedance is calculated in accordance with JEDEC-51.

(5) The maximum power dissipation is a function of T_{J(MAX)}, R_{θJA}, and T_A. The maximum allowable power dissipation at any ambient temperature is P_D = (T_{J(MAX)} - T_A) / R_{θJA}. All numbers apply for packages soldered directly onto a PCB.

8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
V _(ESD)	Electrostatic discharge	Human-body model (HBM), MIL-STD-883K METHOD 3015.9	±2000	V
		Charged-device model (CDM), ANSI/ESDA/JEDEC JS-002-2018	±1000	V
		Machine Model (MM), JESD22-A115C (2010)	±200	V



ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

8.3 Recommended Operating Conditions

V_{CC} is the supply voltage associated with the input port and output port. ⁽¹⁾⁽²⁾

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V _{CC}		2.0		5.5	V
High-Level Input Voltage	V _{IH}	V _{CC} =2.0V	1.0			V
		V _{CC} =3.0V	1.5			
		V _{CC} =4.5V to 5.5V	2.0			
Low-Level Input Voltage	V _{IL}	V _{CC} =2.0V			0.3	V
		V _{CC} =3.0V			0.55	
		V _{CC} =4.5V to 5.5V			0.8	
Input Voltage	V _I		0		5.5	V
Output Voltage	V _O		0		V _{CC}	V
High-Level Output Current	I _{OH}	V _{CC} =3.0V to 3.6V			-8	mA
		V _{CC} =4.5V to 5.5V			-16	
Low-Level Output Current	I _{OL}	V _{CC} =3.0V to 3.6V			8	mA
		V _{CC} =4.5V to 5.5V			16	
Input transition rise or fall rate	Δt/Δv	V _{CC} =3.0V to 3.6V			50	ns/V
		V _{CC} =4.5V to 5.5V			10	
Operating free-air Temperature	T _A		-40		125	°C

(1) All unused or driven (floating) data inputs (I/Os) of the device must be held at logic HIGH or LOW (preferably V_{CC} or GND) to ensure proper device operation and minimize power.

(2) All unused control inputs must be held at V_{CC} or GND to ensure proper device operation and minimize power consumption.

8.4 Electrical Characteristics

over recommended operating free-air temperature range (TYP values are at $T_A = +25^\circ\text{C}$, Full = -40°C to 125°C , unless otherwise noted).

PARAMETER	CONDITIONS	V _{CC}	TEMP	MIN ⁽¹⁾	TYP ⁽²⁾	MAX ⁽¹⁾	UNIT
V _{OH}	I _{OH} = -100μA, V _I = V _{IH}	2.0V to 5.5V	Full	V _{CC} -0.1			V
	I _{OH} = -8mA, V _I = V _{IH}	3.3V		2.8			
	I _{OH} = -16mA, V _I = V _{IH}	5V		4.5			
V _{OL}	I _{OL} = 100μA, V _I = V _{IL}	2.0V to 5.5V				0.1	V
	I _{OL} = 8mA, V _I = V _{IL}	3.3V				0.55	
	I _{OL} = 16mA, V _I = V _{IL}	5V				0.55	
I _I	V _I = 5.5V or GND	5.5V	+25°C			±1	μA
			Full			±2	
I _{off}	V _I or V _O = 0 to 5.5V	0V	+25°C			±1	μA
			Full			±2	
I _{oz} ⁽³⁾	V _O = V _{CC} or GND, V _I = V _{IH} or V _{IL}	2.0V to 5.5V	+25°C			±1	μA
			Full			±2	
I _{CC}	V _I = V _{CC} or GND ⁽⁴⁾ I _O = 0	2.0V to 5.5V	+25°C			1	μA
			Full			5	
I _{CC(T)}	One input at 3.4 V, Other inputs at GND or V _{CC}	5.5V	Full			100	μA
C _I	V _I = V _{CC} or GND	3.3V	+25°C		3.3		pF
C _O	V _O = V _{CC} or GND	3.3V	+25°C		5		pF

(1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

(2) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

(3) For I/O ports, the parameter I_{oz} includes the input leakage current.

(4) Hold all unused data inputs of the device at V_{CC} or GND to assure proper device operation.

8.5 Switching Characteristics

over recommended operating free-air temperature range (unless otherwise noted).

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 2.0V ⁽¹⁾		V _{CC} = 3.3V ⁽¹⁾		V _{CC} = 5V ⁽¹⁾		UNIT
			MIN	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	A _n	Y _n	4.8	19.5	1.8	7.8	1.5	6.3	ns
t _{PHL}									
t _{PHZ}	$\overline{\text{OE}}$	Y _n	4.8	22.2	2.1	10	1.3	7.7	ns
t _{PLZ}									
t _{PZH}	$\overline{\text{OE}}$	Y _n	4.1	26.6	1.7	11.6	1.6	9.2	ns
t _{PZL}									

(1) This parameter is ensured by design and/or characterization and is not tested in production.

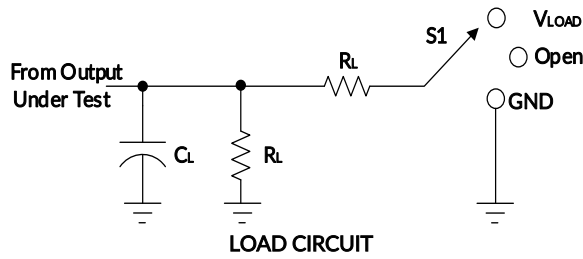
8.6 Operating Characteristics

$T_A=25^{\circ}\text{C}$

PARAMETER		TEST CONDITIONS	$V_{CC}=2.5\text{V}$	$V_{CC}=3.3\text{V}$	$V_{CC}=5\text{V}$	UNIT
			TYP	TYP	TYP	
$C_{pd}^{(1)}$	Outputs enabled	$C_L=0,$ $f=10\text{MHz},$ $t_r=t_f=1\text{ns}$	9	11	13	pF
	Outputs disabled		1	1.5	2	

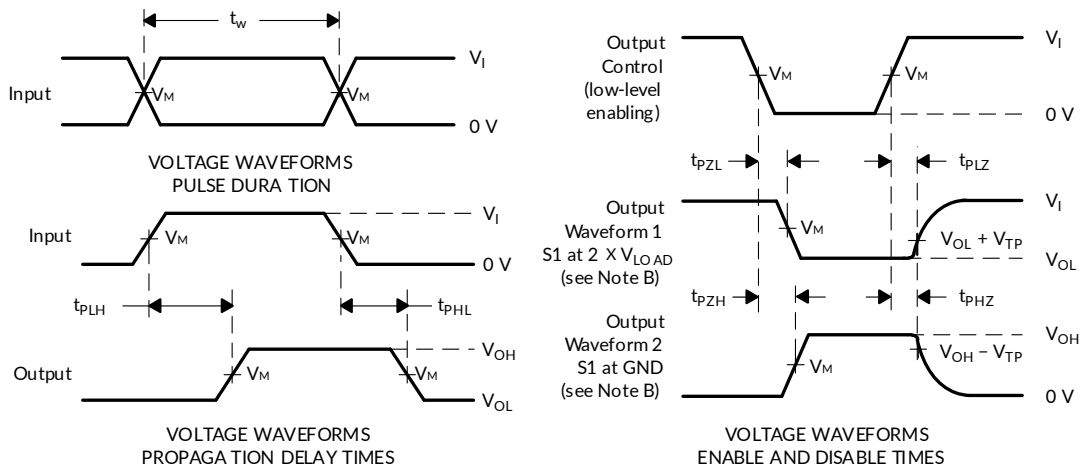
(1) Power dissipation capacitance per transceiver.

9 Parameter Measurement Information



TEST	S1
t_{pd}	Open
t_{PLZ}/t_{PZL}	V_{LOAD}
t_{PHZ}/t_{PZH}	GND

V_{CC}	V_I	V_M	C_L	R_L	V_{TP}
$2.0V \pm 0.2V$	V_{CC}	$V_{CC}/2$	15pF	2k Ω	0.15V
$3.3V \pm 0.3V$	2.7V	1.5V	15pF	2k Ω	0.3V
$5V \pm 0.5V$	2.7V	1.5V	15pF	2k Ω	0.3V



NOTES: A. C_L includes probe and jig capacitance.

B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control.

Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.

C. All input pulses are supplied by generators having the following characteristics: $PRR \leq 10$ MHz, $Z_o = 50\Omega$, $dv/dt \geq 1V/ns$.

D. The outputs are measured one at a time, with one transition per measurement.

E. t_{PLZ} and t_{PHZ} are the same as t_{dis} .

F. t_{PZL} and t_{PZH} are the same as t_{en} .

G. t_{PLH} and t_{PHL} are the same as t_{pd} .

H. All parameters and waveforms are not applicable to all devices.

Figure 1. Load Circuit and Voltage Waveforms

10 Detailed Description

10.1 Overview

The RS540T is an 8-bit inverting buffer/line driver with 3-state outputs. The device features two output enables ($\overline{OE1}$ and $\overline{OE2}$). A High on \overline{OE} causes the associated outputs to assume a high-impedance OFF-state.

Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

Schmitt-trigger action at all inputs makes the circuit tolerant of slower input rise and fall times. This device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

To ensure the high-impedance state during power up or power down, \overline{OE} should be tied to V_{CC} through a pullup resistor, the minimum value of the resistor is determined by the current-sinking capability of the driver.

11 Power Supply Recommendations

The power supply can be any voltage between the minimum and maximum supply voltage rating located in the Recommended Operating Conditions. Each V_{CC} terminal should have a good bypass capacitor to prevent power disturbance. A 0.1 μ F capacitor is recommended for this device. It is acceptable to parallel multiple bypass capacitors to reject different frequencies of noise. The 0.1 μ F and 1 μ F capacitors are commonly used in parallel. The bypass capacitor should be installed as close to the power terminal as possible for best results.

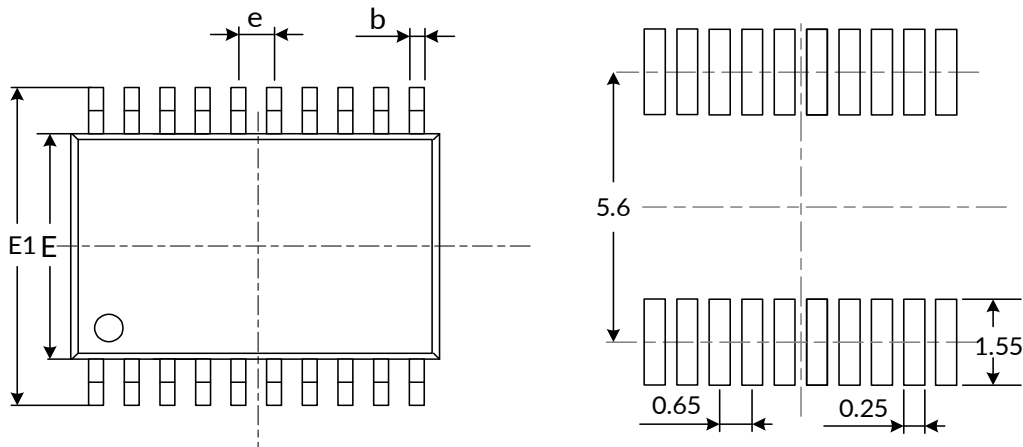
12 Layout

12.1 Layout Guidelines

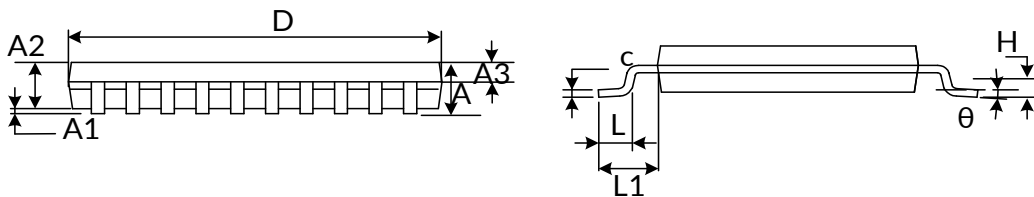
When using multiple bit logic devices inputs should not ever float. In many cases, functions or parts of functions of digital logic devices are unused; for example, when only two inputs of a triple-input AND gate are used or only 3 of the 4 buffer gates are used. Such input pins should not be left unconnected because the undefined voltages at the outside connections result in undefined operational states. Specified below are the rules that must be observed under all circumstances. All unused inputs of digital logic devices must be connected to a high or low bias to prevent them from floating. The logic level that should be applied to any particular unused input depends on the function of the device. Generally, they will be tied to GND or V_{CC} whichever make more sense or is more convenient.

13 PACKAGE OUTLINE DIMENSIONS

TSSOP-20⁽⁴⁾



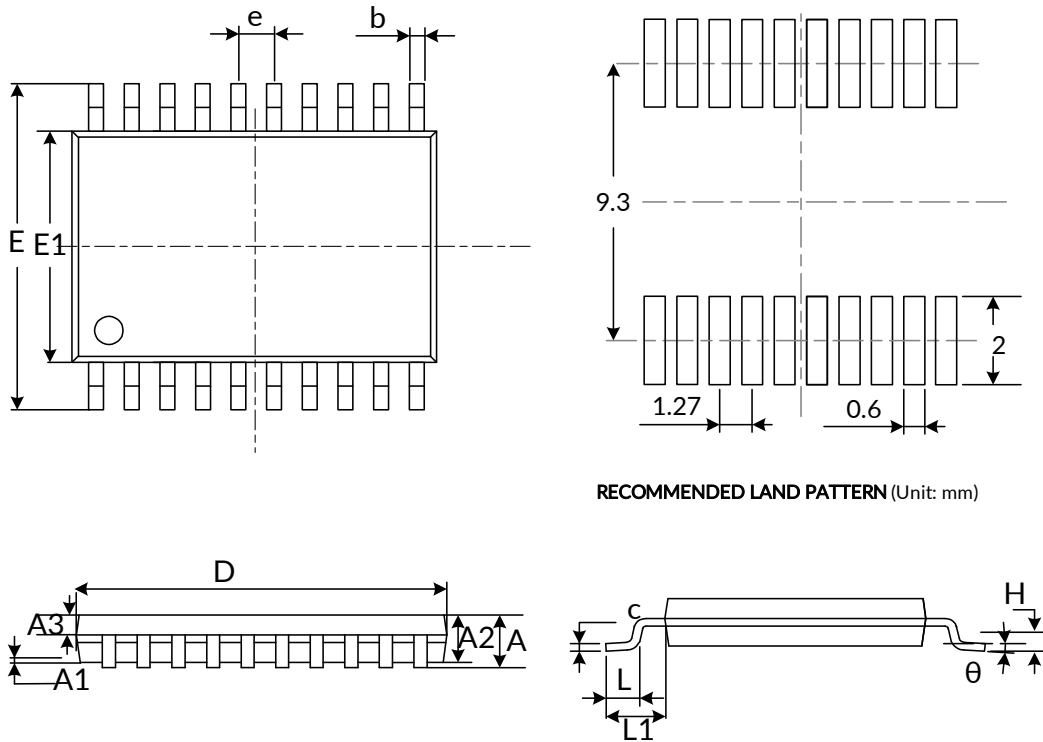
RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾		1.200		0.047
A1	0.050	0.150	0.002	0.006
A2	0.800	1.050	0.031	0.041
A3	0.390	0.490	0.015	0.020
b	0.200	0.290	0.008	0.011
c	0.130	0.170	0.005	0.007
D ⁽¹⁾	6.400	6.600	0.252	0.260
E ⁽¹⁾	4.300	4.500	0.169	0.177
E1	6.200	6.600	0.244	0.260
e	0.650(BSC) ⁽²⁾		0.026(BSC) ⁽²⁾	
L	0.450	0.750	0.018	0.030
H	0.250(TYP)		0.010(TYP)	
θ	0°	8°	0°	8°
L1	1.00(REF) ⁽³⁾		0.039(REF) ⁽³⁾	

NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. REF is the abbreviation for Reference.
4. This drawing is subject to change without notice.

SOIC-20(SOP20)⁽⁴⁾


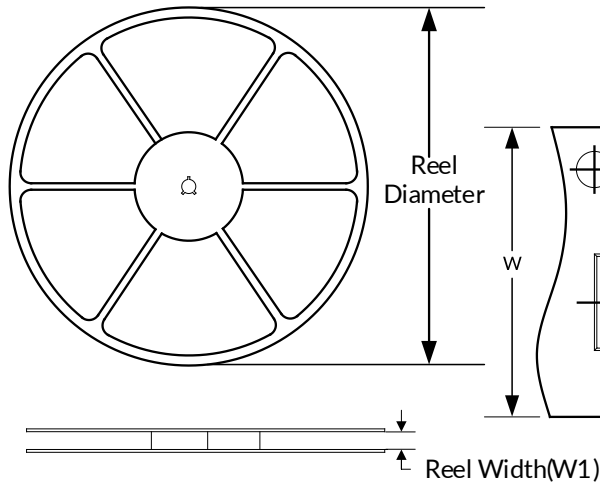
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾		2.650		0.104
A1	0.100	0.300	0.004	0.012
A2	2.250	2.350	0.089	0.093
A3	0.970	1.070	0.038	0.042
b	0.390	0.470	0.015	0.019
c	0.250	0.290	0.010	0.011
D ⁽¹⁾	12.700	12.900	0.500	0.508
E	10.100	10.500	0.398	0.413
E1 ⁽¹⁾	7.400	7.600	0.291	0.299
e	1.270(BSC) ⁽²⁾		0.050(BSC) ⁽²⁾	
L	0.700	1.000	0.028	0.039
H	0.250(TYP)		0.010(TYP)	
θ	0°	8°	0°	8°
L1	1.400(REF) ⁽³⁾		0.055(REF) ⁽³⁾	

NOTE:

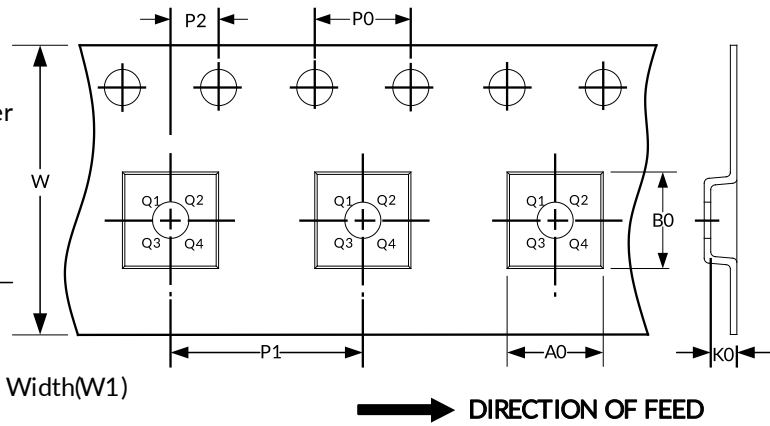
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14 TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
TSSOP-20	13"	12.4	6.75	6.95	1.20	4.0	8.0	2.0	12.0	Q1
SOIC-20(SOP20)	13"	24.4	10.75	13.55	2.65	4.0	12.0	2.0	24.0	Q1

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.

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