

# **Reference Specification**

Type RA Safety Standard Certified Lead Type Disc Ceramic Capacitors for General Purpose

Product specifications in this catalog are as of Jun. 2023, and are subject to change or obsolescence without notice. Please consult the approval sheet before ordering.Please read rating and Cautions first.

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## **1. OPERATING VOLTAGE**

1) Do not apply a voltage to a safety standard certified product that exceeds the rated voltage as called out in the specifications. Applied voltage between the terminals of a safety standard certified product shall be less than or equal to the rated voltage (+10 %). When a safety standard certified product is used as a DC voltage product, the AC rated voltage value becomes the DC rated voltage value.

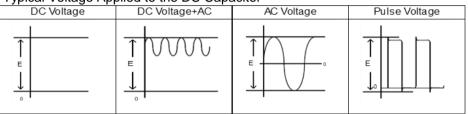
(Example:AC250 V (r.m.s.) rated product can be used as DC250 V (+10 %) rated product.)

If both AC rated voltage and DC rated voltage are specified, apply the voltage lower than the respective rated voltage.

1-1) When a safety standard certified product is used in a circuit connected to a commercial power supply, ensure that the applied commercial power supply voltage including fluctuation should be less than 10 % above its rated voltage.

1-2) When using a safety standard certified product as a DC rated product in circuits other than those connected to a commercial power supply.

When AC voltage is superimposed on DC voltage, the zero-to-peak voltage shall not exceed the rated DC voltage. When AC voltage or pulse voltage is applied, the peak-to-peak voltage shall not exceed the rated DC voltage.



Typical Voltage Applied to the DC Capacitor

(E: Maximum possible applied voltage.)

2) Abnormal voltages (surge voltage, static electricity, pulse voltage, etc.) shall not exceed the rated DC voltage.

## 2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

When the capacitor is used in a high-frequency current, pulse current or the like, it may have the selfgenerated heat due to dielectric-loss. Applied voltage should be the load such as self-generated heat is within 20 °C on the condition of atmosphere temperature 25 °C. When measuring, use a thermocouple of small thermal capacity-K of  $\Phi 0.1$  mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. (Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

## 3. TEST CONDITION FOR WITHSTANDING VOLTAGE

1) TEST EQUIPMENT

Test equipment for AC withstanding voltage should be used with the performance of the wave similar to 50/60 Hz sine wave.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

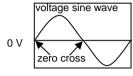
#### 2) VOLTAGE APPLIED METHOD

When the withstanding voltage is applied, capacitor's lead or terminal should be firmly connected to the out-put of the withstanding voltage test equipment, and then the voltage should be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the \*zero cross. At the end of the test time, the test voltage should be reduced to near zero, and then capacitor's lead or terminal should be taken off the out-put of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

\*ZERO CROSS is the point where voltage sine wave pass 0 V. - See the right figure -



#### 4. FAIL-SAFE

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

#### 5. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

#### 6. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip	: 400 °C max
Soldering iron wattage	: 50 W max.
Soldering time	: 3.5 s max.

#### 7. BONDING, RESIN MOLDING AND COATING

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.

In case of the amount of applications, dryness / hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive, molding resin or coating may cause a outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

#### 8. TREATMENT AFTER BONDING, RESIN MOLDING AND COATING

When the outer coating is hot (over 100  $^{\circ}$ C) after soldering, it becomes soft and fragile. So please be careful not to give it mechanical stress.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

#### 9. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40  $^{\circ}$ C and 15 to 85 %.

Use capacitors within 6 months after delivered. Check the solderability after 6 months or more.

#### **10. LIMITATION OF APPLICATIONS**

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. Power plant control equipment
- 5. Medical equipment
- 6. Transportation equipment (vehicles, trains, ships, etc.)
- 7. Traffic signal equipment
- 8. Disaster prevention / crime prevention equipment
- 9. Data-processing equipment exerting influence on public
- 10. Application of similar complexity and/or reliability requirements to the applications listed in the above.

#### NOTICE

#### 1. CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions. Rinse bath capacity : Output of 20 watts per liter or less.

Rinsing time : 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

#### 2. CAPACITANCE CHANGE OF CAPACITORS

#### Class 1 capacitors

Capacitance might change a little depending on a surrounding temperature or an applied voltage. Please contact us if you use for the strict time constant circuit.

Class 2 and 3 capacitors

Class 2 and 3 capacitors like temperature characteristic B, E and F have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit. Please contact us if you need a detail information.

#### 3. PERFORMANCE CHECK BY EQUIPMENT

Before using a capacitor, check that there is no problem in the equipment's performance and the specifications.

Generally speaking, CLASS 2 ceramic capacitors have voltage dependence characteristics and temperature dependence characteristics in capacitance. So, the capacitance value may change depending on the operating condition in a equipment. Therefore, be sure to confirm the apparatus performance of receiving influence in a capacitance value change of a capacitor, such as leakage current and noise suppression characteristic.

Moreover, check the surge-proof ability of a capacitor in the equipment, if needed, because the surge voltage may exceed specific value by the inductance of the circuit.

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- 1. Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- 2. You are requested not to use our product deviating from this specification.

#### 1.Application

This specification is applied to Safety Standard Certified Lead Type Disc Ceramic Capacitors Type RA used for General Electric equipment.

The safety standard certification is obtained by Class X1, Y1.

Do not use these products in any automotive power train or safety equipment including battery chargers for electric vehicles and plug-in hybrids.

Approval standard and certified number

	Standard number	*Certified number	Rated voltage
UL/cUL	UL60384-14/CSA E60384-14	E37921	
ENEC (VDE)	EN60384-14	40043033	X1: AC440 V(r.m.s.) Y1: AC400 V(r.m.s.)
CQC	IEC60384-14	CQC16001138225	

\*Above Certified number may be changed on account of the revision of standards and the renewal of certification.

#### 2.Rating

2-1.Operating temperature range

2-2.Rated Voltage

X1: AC440 V(r.m.s.) Y1: AC400 V(r.m.s.) DC1,000 V

2-3.Part number configuration

ex.)

DE1	E3	RA	472	М	J4	В	H01F
Series	Temperature	Certified	Capacitance	Capacitance	Lead	Package	Individual
	Characteristics	Туре		Tolerance	Style		Specification

Series

DE1 denotes class X1,Y1.

Temperature Characteristics

Please confirm detailed specification on [Specification and test methods].

Code	Temperature Characteristics
1X	SL
B3	В
E3	E

Certified Type

This denotes safety certified type name Type RA.

Capacitance

The first two digits denote significant figures ; the last digit denotes the multiplier of 10 in pF. ex.) In case of  $472_{\circ}$ .

 $47 \times 10^2 = 4700 \text{ pF}$ 

 Capacitance Tolerance Please refer to [ Part number list ].

#### Lead Style

\* Please refer to [Part number list].

Code	Lead Style
A*	Vertical crimp long type
J*	Vertical crimp short type
N*	Vertical crimp taping type

Package

aenage	
Code	Package
A	Ammo pack taping type
В	Bulk type

#### Individual Specification

For part number that cannot be identified without "Individual Specification", it is added at the end of part number.

Code	Individual Specification				
H01F	Rated voltage : X1: AC440 V(r.m.s.) Y1: AC400 V(r.m.s.) DC1,000 V     Halogen free Br≦900ppm, Cl≦900ppm $)$				
	Br+Cl≦1500ppm J →CP wire				

Note) Murata part numbers might be changed depending on Lead Style or any other changes. Therefore, please specify only the Certified Type (RA) and capacitance of products in the parts list when it is required for applying safety standard of electric equipment.

#### 3.Marking

Contifications		
Certified type	: RA	
Capacitance	: Actual value(under 100 pF)	
	3 digit system(100 pF and over)	
Capacitance tolerance	: Code	
Class code and Rated voltage mark		
	: Y1 400~	
Manufacturing year	: Letter code(The last digit of A.D. year.)	
Manufacturing month	: Code	
	$ \left( \begin{array}{ccc} \text{Feb./Mar.} \rightarrow 2 & \text{Aug./Sep.} \rightarrow 8 \\ \textbf{Apr./May} \rightarrow 4 & \text{Oct./Nov.} \rightarrow 0 \\ \text{Jun./Jul.} \rightarrow 6 & \text{Dec./Jan.} \rightarrow D \end{array} \right) $	٦
	Apr./May → 4 Oct./Nov. → O	
	(Jun./Jul. → 6 Dec./Jan. → D	J
	<b>C</b>	
Company name code	: CM15 (Made in Thailand)	
	(Evenne)	
	(Example)	
	RA 472M	
	( X1 440∼ )	
	∖ Y1 400~ /	
	<b>∠ 2D </b> € 15 /	

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4. Part number list •Vertical crimp long type (Lead Style: A*)										
Up to the end of or imp $F \pm 1.0$ Tmax. 3.0 max. 25.0 min. $\phi d_{\pm 0.05}$										
	rk ' * ' of Lead Style differ fro see the following list about o			ng (F) and	l lead c	diame	ter (d).		Unit :	mm
Customer	Murata	T.C.	Cap.	Cap.	Dir	mensi	on (mr	n)	Lead	
Part Number	Part Number		(pF)	tol.	D	Т	F	d	Style	(pcs)
	DE11XRA100KA4BH01F	SL	10	±10%	7.0	4.0	10.0	0.6	A4	250
	DE11XRA150KA4BH01F	SL	15	±10%	6.0	5.0	10.0	0.6	A4	500
	DE11XRA220KA4BH01F	SL	22	±10%	6.0	4.0	10.0	0.6	A4	500
	DE11XRA330KA4BH01F	SL	33	±10%	7.0	4.0	10.0	0.6	A4	250
	DE11XRA470KA4BH01F	SL	47	±10%	7.0	4.0	10.0	0.6	A4	250
	DE11XRA680KA4BH01F	SL	68	±10%	8.0	4.0	10.0	0.6	A4	250
	DE1B3RA101KA4BH01F	В	100	±10%	6.0	4.0	10.0	0.6		500
	DE1B3RA151KA4BH01F	В	150	±10%	7.0	4.0	10.0	0.6	A4	250
	DE1B3RA221KA4BH01F	В	220	±10%	6.0	5.0	10.0	0.6	A4	500
	DE1B3RA331KA4BH01F	В	330	±10%	6.0	5.0	10.0	0.6	A4	500
	DE1B3RA471KA4BH01F	В	470	±10%	7.0	5.0	10.0	0.6	A4	250
	DE1B3RA681KA4BH01F	В	680	±10%	8.0	5.0	10.0	0.6	A4	250
	DE1E3RA102MA4BH01F	Е	1000	±20%	7.0	4.0	10.0	0.6	A4	250
	DE1E3RA152MA4BH01F	Е	1500	±20%	8.0	4.0	10.0	0.6	A4	250
	DE1E3RA222MA4BH01F	Е	2200	±20%	9.0	4.0	10.0	0.6	A4	250
	DE1E3RA332MA4BH01F	E	3300	±20%	10.0	5.0	10.0	0.6	A4	250
	DE1E3RA472MA4BH01F	Е	4700	±20%	12.0	5.0	10.0	0.6	A4	200

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	·Vertical crimp sh (Lead Style:J*)	ort ty	vpe							
	Up to the end of crimp F ± 0.8	3.	Tm: 5±1.0 5±0.5	ax.						
	rk ' * ' of Lead Style differ fro see the following list about			ng (F) and	llead	diame	ter (d).		Unit :	mm
Customer	Murata		Cap.	Cap.	Di	mensi	on (mr	n)	Lead	Pack
Part Number	Part Number	T.C.	(pF)	tol.	D	Т	F	d	Style	qty. (pcs)
	DE11XRA100KJ4BH01F	SL	10	±10%	7.0	4.0	10.0	0.6	J4	500
	DE11XRA150KJ4BH01F	SL	15	±10%	6.0	5.0	10.0	0.6	J4	500
	DE11XRA220KJ4BH01F	SL	22	±10%	6.0	4.0	10.0	0.6	J4	500
	DE11XRA330KJ4BH01F	SL	33	±10%	7.0	4.0	10.0	0.6	J4	500
	DE11XRA470KJ4BH01F	SL	47	±10%	7.0	4.0	10.0	0.6	J4	500
	DE11XRA680KJ4BH01F	SL	68	±10%	8.0	4.0	10.0	0.6	J4	500
	DE1B3RA101KJ4BH01F	В	100	±10%	6.0	4.0	10.0	0.6	J4	500
	DE1B3RA151KJ4BH01F	В	150	±10%	7.0	4.0	10.0	0.6	J4	500
	DE1B3RA221KJ4BH01F	В	220	±10%	6.0	5.0	10.0	0.6	J4	500
	DE1B3RA331KJ4BH01F	В	330	±10%	6.0	5.0	10.0	0.6	J4	500
	DE1B3RA471KJ4BH01F	В	470	±10%	7.0	5.0	10.0	0.6	J4	500
	DE1B3RA681KJ4BH01F	В	680	±10%	8.0	5.0	10.0	0.6	J4	500
	DE1E3RA102MJ4BH01F	E	1000	±20%	7.0	4.0	10.0	0.6	J4	500
	DE1E3RA152MJ4BH01F	E	1500	±20%	8.0	4.0	10.0	0.6		500
	DE1E3RA222MJ4BH01F	E	2200	±20%	9.0	4.0	10.0	0.6		500
	DE1E3RA332MJ4BH01F	E	3300	±20%	10.0	5.0	10.0	0.6		500
	DE1E3RA472MJ4BH01F	E	4700	±20%	12.0	5.0	10.0	0.6	J4	250

<ul> <li>Vartical crimp taping type (Lead Style: N*)</li> </ul>														
P Dmax. Tmax. ¢d														
Note) The mark ' * ' of Lead Style differ from lead spacing (F) , lead diameter (d) and pitch of compoment (P). Please see the following list or taping specification about details.														
Customer	Murata	ТО	Cap.	Cap.		Dime	nsion	(mm)		Unit : Lead	Pac			
Part Number	Part Number	T.C.	(pF)		(pF)		tol.	D	Т	F	d	Ρ	Style	qty (pc
	DE11XRA100KN4AH01F	SL	10	±10%	7.0	4.0	10.0	0.6	25.4	N4	60			
	DE11XRA150KN4AH01F	SL	15	±10%	6.0	5.0	10.0	0.6	25.4	N4	60			
	DE11XRA220KN4AH01F	SL	22	±10%	6.0	4.0	10.0	0.6	25.4	N4	60			
	DE11XRA330KN4AH01F	SL	33	±10%	7.0	4.0	10.0	0.6	25.4	N4	60			
	DE11XRA470KN4AH01F	SL	47	±10%	7.0	4.0	10.0	0.6	25.4	N4	60			
	DE11XRA680KN4AH01F	SL	68	±10%	8.0	4.0	10.0	0.6	25.4	N4	60			
	DE1B3RA101KN4AH01F	В	100	±10%	6.0	4.0	10.0	0.6	25.4	N4	60			
	DE1B3RA151KN4AH01F	В	150	±10%	7.0	4.0	10.0		25.4	N4	60			
	DE1B3RA221KN4AH01F	В	220	±10%	6.0	5.0	10.0		25.4	N4	60			
	DE1B3RA331KN4AH01F	В	330	±10%	6.0	5.0	10.0	0.6	25.4	N4	60			
	DE1B3RA471KN4AH01F	В	470	±10%	7.0	5.0	10.0		25.4		60			
	DE1B3RA681KN4AH01F	В	680	±10%	8.0	5.0	10.0		25.4	N4	60			
	DE1E3RA102MN4AH01F	E	1000	±20%	7.0	4.0	10.0		25.4	N4	60			
		E	1500	±20%	0 0	4.0	10.0	0.6	25.4	N4	60			
	DE1E3RA152MN4AH01F				8.0									
	DE1E3RA152MN4AH01F DE1E3RA222MN4AH01F DE1E3RA332MN4AH01F	E	2200 3300	±20%	9.0 9.0	4.0 5.0	10.0 10.0		25.4 25.4	N4 N4	60 60			

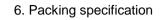
5. Sp	ecification and te	est methods		
No.	lte	em	Specification	Test method
1	Appearance and	d dimensions	No marked defect on appearance forr and dimensions. Please refer to [Part number list].	n The capacitor should be inspected by naked eyes for visible evidence of defect. Dimensions should be measured with slide calipers.
2	Marking		To be easily legible.	The capacitor should be inspected by naked eyes.
3	Dielectric strength	Between lead wires	No failure.	The capacitor should not be damaged when AC4,000 V(r.m.s.) <50/60 Hz> is applied between the lead wires for 60 s.
		Body insulation	No failure.	First, the terminals of the capacitor should be connected together. Then, a metal foil should be closely wrapped around the body of the capacitor to the distance of about 3 to 6 mm from each terminal. Then, the capacitor should be inserted into a container filled with metal balls of about 1 mm diameter. Finally, AC4,000 V(r.m.s.) <50/60 Hz> is applied for 60 s between the capacitor lead wires and metal balls.
4	Insulation Resis	stance (I.R.)	10,000 MΩ min.	The insulation resistance should be measured with DC500 $\pm$ 50 V within 60 $\pm$ 5 s of charging. The voltage should be applied to the capacitor through a resistor of 1 M $\Omega$ .
5	Capacitance		Within specified tolerance.	The capacitance should be measured at 20 °C with 1±0.1 kHz and AC1±0.2 V(r.m.s.) max
6	Dissipation Fac	tor (D.F.)	DF≦0.025	The dissipation factor should be measured at 20 $^\circ C$ with 1±0.1 kHz and AC1±0.2 V(r.m.s.) max
7	7 Temperature characteristic		Char. SL : +350 to -1,000 ppm/ °C (Temp. range : 20 to 85 °C) Char. B : Within ±10 % Char. E : Within +20/-55 % (Temp. range : -25 to 85 °C)	The capacitance measurement should be made at each step specified in Table.         Step       1       2       3       4       5
				Temp.(°C)         20±2         -25±2         20±2         85±2         20±2
8	8 Active flammability		The cheese-cloth should not be on fire.	The capacitors should be individually wrapped in at least one but more than two complete layers of cheese-cloth. The capacitor should be subjected to 20 discharges. The interval between successive discharges should be 5 s. The UAc should be maintained for 2 min after the last discharge. $\underbrace{s_1 \\ f_r \\ s_2 \\ uAc} \\ s_1 \\ u_1 \\ u_2 \\ u_3 \\ u_4 \\ u_4 \\ u_5 \\ u_6 \\ u_1 \\ u_1 \\ u_1 \\ u_2 \\ u_4 \\ u_1 \\ u_1 \\ u_1 \\ u_2 \\ u_1 \\ u_2 \\ u_2 \\ u_3 \\ u_4 \\ u_1 \\ u_2 \\ u_1 \\ u_2 \\ u_2 \\ u_2 \\ u_3 \\ u_4 \\ u_1 \\ u_2 \\ u_2 \\ u_2 \\ u_2 \\ u_3 \\ u_4 \\ u_1 \\ u_2 \\ u_1 \\$
				Cx : Capacitor under test F : Fuse, Rated 10 A Ut : Voltage applied to Ct ux skv

			Reference					
No.		em	Specification	Test method				
9	Robustness of terminations	Tensile	Lead wire should not cut off. Capacitor should not be broken.	Fix the body of capacitor, a tensile weight gradually to each lead wire in the radial direction of capacitor up to 10 N and keep it for 10±1 s. With the termination in its normal position, the capacitor is held b its body in such a manner that the axis of the termination is vertical; a mass applying a force of 5 N is then suspended from the end of the termination. The body of the capacitor is then inclined, within a period of 2 to 3 s, through an angle of approximately 90 ° in the vertical plane and then returned to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.				
		Bending						
10	Vibration	Appearance	No marked defect.	The capacitor should be firmly soldered to the supporting lead wire				
	resistance	Capacitance	Within the specified tolerance.	and vibration which is 10 to 55 Hz in the vibration frequency range,				
		Dissipation Factor (D.F.)	DF≦0.025	1.5 mm in total amplitude, and about 1 min in the rate of vibration change from 10 Hz to 55 Hz and back to 10 Hz is applied for a total of 6 h; 2 h each in 3 mutually perpendicular directions.				
11	Solderability of	leads	Lead wire should be soldered with	The lead wire of a capacitor should be dipped into a ethanol				
			uniformly coated on the axial direction over 3/4 of the circumferential direction.	solution of 25 wt% rosin and then into molten solder for $2\pm0.5$ s. In both cases the depth of dipping is up to about 1.5 to 2.0 mm from the root of lead wires. Temp. of solder : $245\pm5$ °C Lead Free Solder (Sn-3Ag-0.5Cu)				
12	Soldering	Appearance	No marked defect.	Solder temperature : 350±10 °C or 260±5 °C				
	effect (Non-preheat)	Capacitance change	Within ±10 %	Immersion time $: 3.5\pm0.5 \text{ s}$ (In case of $260\pm5 \text{ °C} : 10\pm1 \text{ s}$ ) The depth of immersion is up to about 1.5 to 2.0 mm from the root				
		I.R.	1,000 MΩ min.	of lead wires.				
		Dielectric strength	Per item 3	insulating 1.5 to 2.0mm  Molten solder				
				Pre-treatment : Capacitor should be stored at 125±2 °C for 1 h, and apply the AC4,000 V(r.m.s.) 60 s then placed at *room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 1 to 2 h at *room condition.				
13	Soldering	Appearance	No marked defect.	First the capacitor should be stored at 120+0/-5 °C for 60+0/-5 s.				
	effect (On-preheat)	Capacitance change	Within ±10 %	Then, as in figure, the lead wires should be immersed solder of $260+0/-5$ °C up to 1.5 to 2.0 mm from the root of terminal for $7.5+0/1.5$				
		I.R.	1,000 MΩ min.	7.5+0/-1 S. Thermal Capacitor				
		Dielectric strength	Per item 3	1.5 To 2.0mm To 2.0mm To 2.0mm To 2.0mm Solder Solder				
				Pre-treatment : Capacitor should be stored at 125±2 °C for 1 h, and apply the AC4,000 V(r.m.s.) 60 s then placed at *room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 1 to 2 h at *room condition				
* "roo	m condition" Te	mperature : 15 to	o 35 °C, Relative humidity : 45 to 75 %, /	Atmospheric pressure : 86 to 106 kPa				

			Reference	Uniy				
No.	Ite	em	Specification	Test method				
14	Flame test		The capacitor flame discontinue as follows.	The capacitor should be subjected to applied flame for 15 s. and then removed for 15 s until 5 cycles.				
			CycleTime1 to 430 s max.560 s max.	Capacitor Flame Gas Burner				
				(in mm)				
15	Passive flammability		The burning time should not be exceeded the time 30 s.	The capacitor under test should be held in the flame in the position which best promotes burning.				
				Time of exposure to flame is for 30 s.				
			The tissue paper should not ignite.	Length of flame : 12±1 mm Gas burner : Length 35 mm min. Inside Dia. 0.5±0.1 mm				
				Outside Dia. 0.9 mm max. Gas : Butane gas Purity 95 % min.				
				About 8mm Gas burner About 10mm thick board				
16	Humidity	Appearance	No marked defect.	Set the capacitor for 500±12 h at 40±2 °C in 90 to 95 % relative				
	(Under steady state)	Capacitance change	Char. SL : Within ±5 % Char. B : Within ±10 % Char. E : Within ±15 %	humidity. Pre-treatment : Capacitor should be stored at 125±2 °C for 1 h, and apply the AC4,000 V(r.m.s.) 60 s then placed at *room condition for 24±2 h before initial measurements.				
		Dissipation Factor (D.F.)	Char. SL :DF≦0.025 Char. B, E:DF≦0.05					
		I.R.	3,000 MΩ min.	(Do not apply to Char. SL)				
		Dielectric strength	Per item 3	Post-treatment : Capacitor should be stored for 1 to 2 h at *room condition.				
	Humidity loading	Appearance	No marked defect.	Apply AC440 V(r.m.s.) for 500±12 h at 40±2 °C in 90 to 95 % relative humidity.				
		Capacitance change	Char. SL : Within ±5 % Char. B : Within ±10 % Char. E : Within ±15 %	Pre-treatment : Capacitor should be stored at 125±2 °C for 1 h, and apply the AC4,000 V(r.m.s.) 60 s then placed				
		Dissipation Factor (D.F.)	Char. SL :DF≦0.025 Char. B, E:DF≦0.05	at *room condition for 24±2 h before initial measurements.				
		I.R.	3,000 MΩ min.	(Do not apply to Char. SL)				
		Dielectric strength	Per item 3	Post-treatment : Capacitor should be stored for 1 to 2 h at *room condition.				
* "*~~~	m condition" T-		o 35 °C, Relative humidity : 45 to 75 %,	Atmospheric prossure : 86 to 106 k/Do				
100		mperature : 15 to	5.55 C, relative numbuly : 45 to 75 %, .					
ĺ								

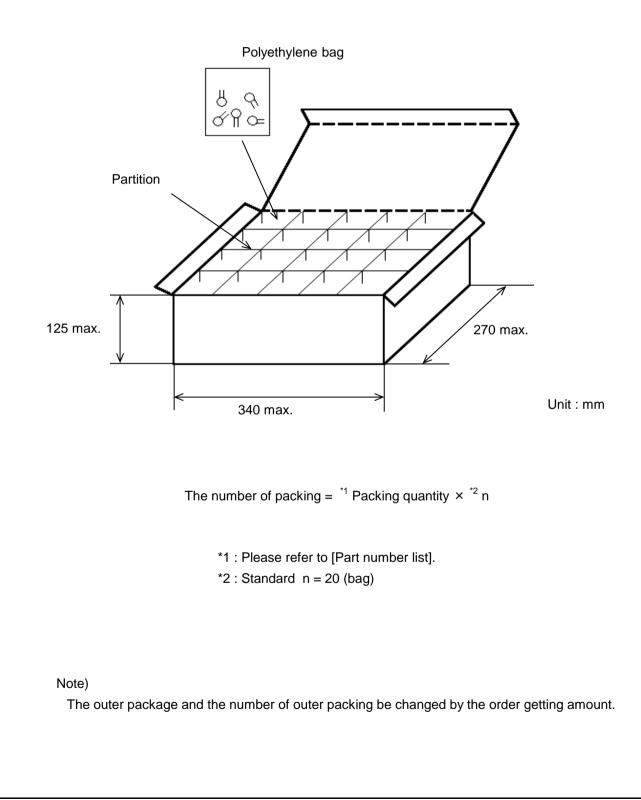
Reference only
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18       Life       Appearance       No marked defect.       Impulse voltage         18       Life       Capacitance change       Within ±20 %.       For times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitors are applied for three times or more. Then the capacitor are applied for three times or more. Then the capacitor are applied for the capacitors are applied for the capacitors are applied for the capacitor should be stored to 24±2 for the voltage is increased to AC1,000 V(r.m. s) for 0.1 in measurements. (Do not apply to Char. SL)         19       Temperature and and immersion cycle       Appearance       No marked defect.       The capacitor should be stored to 24±2 capacitors should be stored to 24±2 heters is measurements. (Do not apply the AC4,000 V(r.m. s) for 3.         19       Temperature capplied for the stis orease.       Te			om	Constitution			Τ	t moth				
Image       Capacitance change       Within ±20 %         I.R.       3,000 MΩ min.         Dielectric       Per item 3         I.R.       0,000 MΩ min.         I.R.       3,000 MΩ min.			1	Specification	Impul	se volt		t method				
19       Temperature and min.         19       Temperature and min.         10       Temperature strength         19       Temperature and min.         10       Temperature and min.         10       Temperature and min.         11       Temperature and min.         12       Temperature and min.         13       Temperature and min.         14       Appearance         15       Temperature and min.         16       Temperature and min.         17       Temperature and min.         18       Appearance         19       Temperature and and paperature of the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1,000 V(r.m.s.) for 0.1 set the voltage is increased to AC1	10	LIIE				Each individual capacitor should be subjected to a 8 kV impulses						
Delectric strength     Per item 3       Delectric strength     Per item 3       Delectric strength     Per item 3       The capacitors are placed in a circulating air oven for 1,000 h. The air in the oven is maintained at a temperature of 125:24°0 °C, and relative humility of 50 % max. The test, the capacitors are subjected to a AC680 V(rm.s.) for 0.1 The voltage is increased to AC1.000 V(r.m.s.) for 0.1 Pre-treatment: Capacitor should be stored at 125±2 and apply the AC4,000 V(r.m.s.) for 0.1 Pre-treatment: Capacitor should be stored for 24±2 hefore i measurements. (Do not apply to Char. SL)       19     Temperature and immersion cycle     Appearance     No marked defect. Char. SL : Within ±5 % Char. E : Within ±10 % Char. E : Within ±10 % Char. B : OF ≦0.05        18.     Appearance Char. SL : DF ≤0.025     The capacitor should be subjected to 5 temperature of the consecutively to 2 immersion cycles.       Vietner     Dissipation Factor (D.F.)     Char. B : DF ≦0.05     Time 3 12543/0 30 min 3 12 15543/0 30 min 3 12 15543/0 30				Within ±20 %		for three times or more. Then the capacitors are applied to life test. Front time (T1) = 1.7 $\mu$ s=1.67T Time to half-value (T2) = 50 $\mu$ s						
Image: Delectric strength     Per item 3       Delectric strength     Per item 3       Image: Delectric strength     Pre-treatment: Capacitor should be stored to 125±2'       Image: Delectric strength     Pre-treatment: Capacitor should be stored to 24±2 before in measurements. (Do not apply to Char. SL)       Image: Delectric strength     Per item 3       Image: Delectric strength			I.R.	3,000 MΩ min.								
9     1       10     1       11     1       12     1       13     1       14     1       15     1       15     1       16     1       17     1       18     1       19     Temperature and and memorion cycle     Appearance Char. SL : Within ±5 % Char. SL : Within ±2 % Char. SL : DF≤0.05       19     Temperature and pissipation cycle     Appearance Char. SL : Within ±2 % Char. SL : DF≤0.05       19     Temperature and pissipation cycle     Appearance Char. SL : DF≤0.05       10     Temperature change     Char. SL : DF≤0.05       17     3.000 MΩ min.       18     I.R. Diseipation Factor (D.F.) Char. B, E : DF≤0.05       19     Temperature cycle       10     The capacitor should be subjected to 5 temperature condition.       11     1       12     1       13     12       14     Room temp.       15     1       16     1       17     1       18     1       19     1       10     1       10     1       11     1       12     1       13     1       14     1<			Dielectric	Per item 3								
19       Temperature and immersion cycle       Appearance Char, BL: DF≦0.025 Factor (D.F.)       No marked defect. Char, SL: DF≦0.025 Factor (D.F.)       The air in the own is maintained at a temperature of 125+2/-0 °C, and relative humidity of 50 % max. Thr test, the capacitors are subjected to a AC680 V(r.m.s.) for 0.1 s attemating voltage of mains frequency, except that or the voltage is increased to AC1,000 V(r.m.s.) for 0.1 s attemating voltage of mains frequency, except that or the voltage is increased to AC1,000 V(r.m.s.) for 0.1 s attemating voltage of mains frequency, except that or the voltage is increased to AC1,000 V(r.m.s.) for 0.1 s attemating voltage of mains frequency, except that or the voltage is increased to AC1,000 V(r.m.s.) for 0.1 s attemating voltage of mains frequency, except that or the voltage is increased to AC1,000 V(r.m.s.) for 0.1 s attemating voltage of mains frequency, except that or the voltage is increased to AC1,000 V(r.m.s.) for 0.1 s provide to the subjected to 5 temperature of the consecutively to Char. SL: Vithin ±10 % Char. SL: DF≦0.025 I.R.         19       Temperature change       Char. SL: DF≦0.025 I.R.       The capacitor should be stored for 24±2 Condition.         10       Dissipation Factor (D.F.)       Char. SL: DF≦0.025 I.R.       Step Temperature(°C) Time 3 min 3 125±3/-0 30 min 3 min 2 125±3/-0 30 min 3 min         11       Dielectric strength       Per item 3       Cycle time : 5 cycles         12       No main Air Max Cycle time : 5 cycles       Step Temperature(°C) Time 3 at yoon Cycles         12       Dissipation Factor (D.F.)       Per item 3 step W Cycle time : 5 cycles			strength									
1,000 h.       The air in the oven is maintained at a temperature of 125+2/0 °C, and relative humidity of 50 % max. The test, the capacitors are subjected to a AC880 V(r.m.s.) alternating voltage of mains frequency, except that or the voltage is increased to AC1,000 V(r.m.s.) for 0.1 state and apply the AC4,000 V(r.m.s.) for 0.1 state and and it room condition for 24±2 hefore i measurements. (Do not apply to Char. SL)         19       Temperature and immersion cycle       Appearance       No marked defect.         19       Temperature and immersion cycle       Char. SL : Within ±5 % Char. B : Within ±10 % Char. B : Within ±10 % Char. B : Within ±20 %       The capacitor should be subjected to 5 temperature of the consecutively to 2 immersion cycles.         10       Char. SL : DF ≤ 0.025       Temperature(*C) Time 1/2 Room temp. 3 min 3/2 Room temp. 3 min 3/2 (2 + 3/-0) 30 min 3/2 (2 + 3/-0) 3/2 (2 + 3/-0) 3/2 min 3/2 (2 + 3/-0) 3/2 (2 + 3/-0) 3/2 min 3/2 (2 + 3/-0) 3/2 (2 + 3/-0) 3												
19       Temperature and immersion cycle       Appearance       No marked defect.       The capacitor should be stored for 24±2 no domination for 24±2 holdore immeasurements. (Do not apply to Char. SL)         19       Temperature and immersion cycle       Char. SL : Within ±5 % Char. B : Within ±10 % Char. E : Within ±20 %       The capacitor should be subjected to 5 temperature of then consecutively to 2 immersion cycles.         19       Temperature factor (D.F.)       Char. SL : DF≤0.025       Temperature cycle>         10       Tr.       3,000 MΩ min.       2       Room temp.       3 min         10       Diseipation       Char. SL : DF≤0.05       1       -40+0/-3       30 min         10       Electric strength       Per item 3       Organitance       Cycle time : 5 cycles         2       Room temp.       3 min       2       2 (Construct)       3 min         11       -40+0/-3       30 min       4       Room temp.       3 min         12       Room temp.       3 min       2       (Construct)       Cycle time : 5 cycles          Immersion cycle>       Pre-itreatment: Capacitor should be stored at 125±2 's and apply the AC4,000 V(r.m.s.) 60 s at "room condition for 24±2 holdore immeasurements. (Do not apply to Char. SL)       Pre-treatment: Capacitor should be stored for 24±2 holdore immeasurements. (Do not apply to Char. SL)         Post-					1,000 The a 125+2 test, t altern							
19       Temperature and immersion cycle       Appearance       No marked defect.       The capacitor should be subjected to 5 temperature of then consecutively to 2 immersion cycles.         19       Temperature and immersion cycle       Char. SL : Within ±5 % Char. B : Within ±20 %       The capacitor should be subjected to 5 temperature of then consecutively to 2 immersion cycles.         10       Dissipation Char. SL : DF ≤ 0.025       Time 140 % Char. B, E : DF ≤ 0.055       Step Temperature(°C) Time 14 + 40 + 0/-3 30 min 22 Room temp. 3 min 3 125 + 3/-0 30 min 4 Room temp. 3 min 3 125 + 3/-0 30 min 4 Room temp. 3 min 2 Cycle time : 5 cycles         10       Dielectric strength       Per item 3       Cycle time : 5 cycles         2       Nom and temp. 3 min 2 Room temp. 3 min 2 Ro					Pre-tr	eatme	and apply the A at *room condit	C4,000 V( ion for 24±	(r.m.s.) 60 s then p			
19       Temperature and immersion cycle       Appearance       No marked defect.       The capacitor should be subjected to 5 temperature of then consecutively to 2 immersion cycles.         19       Temperature and immersion cycle       Char. SL : Within ±5 % Char. E : Within ±20 %       The capacitor should be subjected to 5 temperature of then consecutively to 2 immersion cycles.         10       Dissipation cycle       Char. B : Within ±20 %       Step Temperature (°C) Time 1 40+0/-3 30 min 2 Room temp. 3 min 3 125+3/-0 30 min 2 Room temp. 3 min 3 125+3/-0 30 min 4 Room temp. 3 min 3 125+3/-0 30 min 4 Room temp. 3 min 2 Cycle time : 5 cycles         10       Per item 3       Per item 3 Cycle time : 5 cycles         11       65+5/-0 15 min Clean 2 0 ±3 15 min Satt w Cycle time : Capacitor should be stored at 125±2 ° and apply the AC4,000 V(r.m.s.) 60 s at *room condition for 24±2 h before i measurements. (Do not apply to Char. SL)         Pre-treatment : Capacitor should be stored for 24±2       Post-treatment : Capacitor should be stored for 24±2												
19       Temperature and immersion cycle       Appearance       No marked defect.       The capacitor should be subjected to 5 temperature of then consecutively to 2 immersion cycles.         2       Char. SL : Within ±20 %       Dissipation       Char. SL : DF ≤ 0.025       Time         1.R.       3,000 MΩ min.       3       125+3/-0       30 min         2       Room temp.       3 min         3       125+3/-0       30 min         4       Room temp.       3 min         2       Out the consecutively to 2 immersion cycles         3       125+3/-0       30 min         4       Room temp.       3 min         2       Out the consecutively to 2 immersion cycles         char. B, E : DF ≤ 0.05       Time         1.R.       3,000 MΩ min.         Dielectric       Per item 3         Strength       Per item 3         Cycle time : 5 cycles <immersion cycle="">         Step       Temperature(°C)         1       65+5/-0         1       65+5/-0         2       0±3         1       65+5/-0         2       0±3         1       65+5/-0         2       0±3         1<!--</td--><td></td><td></td><td></td><td></td><td>Doot 4</td><td>traatm</td><td>· · · ·</td><td></td><td>,</td></immersion>					Doot 4	traatm	· · · ·		,			
19       Temperature and immersion cycle       Appearance       No marked defect.       The capacitor should be subjected to 5 temperature of then consecutively to 2 immersion cycles.         19       The capacitor should be subjected to 5 temperature of change       Char. SL : Within ±5 % Char. B : Within ±0 % Char. SL : DF ≤ 0.025       The capacitor should be subjected to 5 temperature of then consecutively to 2 immersion cycles.         10       Dissipation Factor (D.F.)       Char. SL : DF ≤ 0.025       Time Char. B, E : DF ≤ 0.05         1.R.       3,000 MΩ min.       3 125+3/-0 30 min 4 Room temp. 3 min 3 125+3/-0 30 min 4 Room temp. 3 min Cycle time : 5 cycles         Immersion cycle>       Step Temperature(°C) Time Immersion 1 65+5/-0 15 min Clear of 2 0±3 15 min Salt w Cycle time :         V// Cycle time :       Clear of 2 0±3 15 min Salt w Cycle time :         0       Display the AC4,000 V(r.m.s.) 60 s at *room condition for 24±2 h before i measurements. (Do not apply to Char. SL)         Pre-treatment : Capacitor should be stored for 24±2					FUSI-I	ueaun						
immersion cycleCapacitatice changeChar. B : Within $\pm 10 %$ Char. B : Within $\pm 20 \%$ <	19		Appearance	No marked defect.			or should be subjec					
cycle Char, B : Within $\pm 10\%$ Char, E : Within $\pm 20\%$ Dissipation Char, SL : DF $\leq 0.025$ Factor (D.F.) Char, B, E : DF $\leq 0.05$ I.R. 3,000 M $\Omega$ min. Dielectric strength Per item 3 Cycle time : 5 cycles cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ I.R. 3,000 M $\Omega$ min. Dielectric strength Per item 3 Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Per item 3 Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Per item 3 Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Cycle time : 5 cycles class the strength Char, B, E : DF $\leq 0.05$ Cycle time : Cycle time : Cyc			Capacitance	Char. SL : Within ±5 %	then o	consec	cutively to 2 immers	ion cycles.				
StepTemperature(°C)TimeDissipation Factor (D.F.)Char. SL : DF $\leq 0.025$ Char. B, E : DF $\leq 0.05$ 1-40+0/-330 minI.R.3,000 MΩ min.2Room temp.3 minDielectric strengthPer item 3Cycle time : 5 cyclesClaim $\leq 0.025$ 2Claim $\leq 0.05$ I.R.3,000 MΩ min.Cycle time : 5 cyclesDielectric strengthPer item 3Cycle time : 5 cyclesVertexStepTemperature(°C)TimeImmersion $\geq 0.43$ 15 minClean to $\geq 0.43$ VertexStepTemperature(°C)TimeImmersion $\geq 0.43$ 15 minSalt w Cycle time :Cycle time : </td <td></td> <td></td> <td>change</td> <td></td> <td><t< td=""><td>Temper</td><td>rature cycle&gt;</td><td></td><td></td></t<></td>			change		<t< td=""><td>Temper</td><td>rature cycle&gt;</td><td></td><td></td></t<>	Temper	rature cycle>					
Factor (D.F.)       Char. B, E : DF ≤ 0.05         I.R.       3,000 MΩ min.         Dielectric strength       Per item 3         Per item 3       Cycle time : 5 cycles         Step       Temperature(°C)         1       65+5/-0         1       65+5/-0         2       0±3         1       65+5/-0         1       65+5/-0         1       65+5/-0         1       65+5/-0         1       65+5/-0         1       65+5/-0         2       0±3         15       min         Cycle time :       cycle time :         cycle time		Cycle			ę	Step	Temperature(°C)	Time	!			
I.R.       3,000 MΩ min.         Dielectric strength       Per item 3         Cycle time : 5 cycles          Immersion cycle>         Step       Temperature(°C)         1       65+5/-0         2       0±3         15 min       Salt w Cycle time :         Cycle time :       Cycle time :         2       0±3         15 min       Salt w Cycle time :         Cycle time :       Cycle time :         0       Cycle time :         0       0         0       0         1       65+5/-0         15 min       Salt w Cycle time :         Cycle time :       Cycle time :         0       0         0       15 min         0       10         0       10         0       10         0       10         0       10         0       15         0       15         0       10         0       10         0       10         0       10         0       10         0       10         0							-40+0/-3					
1.1X.       5,000 M2 mm.         Dielectric strength       Per item 3         4       Room temp.       3 min Cycle time : 5 cycles          Immersion cycle>         Step       Temperature(°C)       Time         1       65+5/-0       15 min         2       0±3       15 min         Step       Temperature(°C)       Time         Pre-treatment : Capacitor should be stored at 125±2 ° and apply the AC4,000 V(r.m.s.) 60 s at *room condition for 24±2 h before i measurements. (Do not apply to Char. SL)         Post-treatment : Capacitor should be stored for 24±2			Factor (D.F.)									
Dielectric strength       Per item 3         Cycle time : 5 cycles             Step       Temperature(°C)         1       65+5/-0         2       0±3         15       min         Cycle time :       Salt w         Cycle time :       Cycle time :         2       0±3         15       min         Salt w       Cycle time :         Cycle time :       Cycle time :			I.R.	3,000 MΩ min.								
Strength           Immersion cycle>         Step       Temperature(°C)       Time         1       65+5/-0       15 min         2       0±3       15 min         Cycle time :         and apply the AC4,000 V(r.m.s.) 60 s         at *room condition for 24±2 h before i         measurements.         (Do not apply to Char. SL)         Post-treatment : Capacitor should be stored for 24±2			Dielectric	Per item 3		4	-					
Step       Temperature(°C)       Time       Immersion         1       65+5/-0       15 min       Clean         2       0±3       15 min       Salt w         Cycle time :         Pre-treatment : Capacitor should be stored at 125±2 °         and apply the AC4,000 V(r.m.s.) 60 s         at *room condition for 24±2 h before i         (Do not apply to Char. SL)         Post-treatment : Capacitor should be stored for 24±2			strength		<lr></lr>							
2       0±3       15 min       Salt w         Cycle time :         Pre-treatment : Capacitor should be stored at 125±2 °         and apply the AC4,000 V(r.m.s.) 60 s         at *room condition for 24±2 h before i         measurements.         (Do not apply to Char. SL)         Post-treatment : Capacitor should be stored for 24±2						1	-	Time	Immersion water			
Cycle time : Pre-treatment : Capacitor should be stored at 125±2 ° and apply the AC4,000 V(r.m.s.) 60 s at *room condition for 24±2 h before i measurements. (Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 24±2							65+5/-0	15 min	Clean water			
Pre-treatment : Capacitor should be stored at 125±2 ° and apply the AC4,000 V(r.m.s.) 60 s at *room condition for 24±2 h before i measurements. (Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 24±2						2	0±3	15 min	Salt water			
and apply the AC4,000 V(r.m.s.) 60 s at *room condition for 24±2 h before i measurements. (Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 24±2									Cycle time : 2 cycl			
					Post-1	(Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 24±2 h at *room condition.						
"room condition" Temperature : 15 to 35 °C, Relative humidity : 45 to 75 %, Atmospheric pressure : 86 to 106 kPa	"roo	m condition" Te	emperature : 15	to 35 °C, Relative humidity : 45 to 7	′5 %, Atmos	pheric	pressure : 86 to 106	6 kPa				



•Bulk type (Package : B)

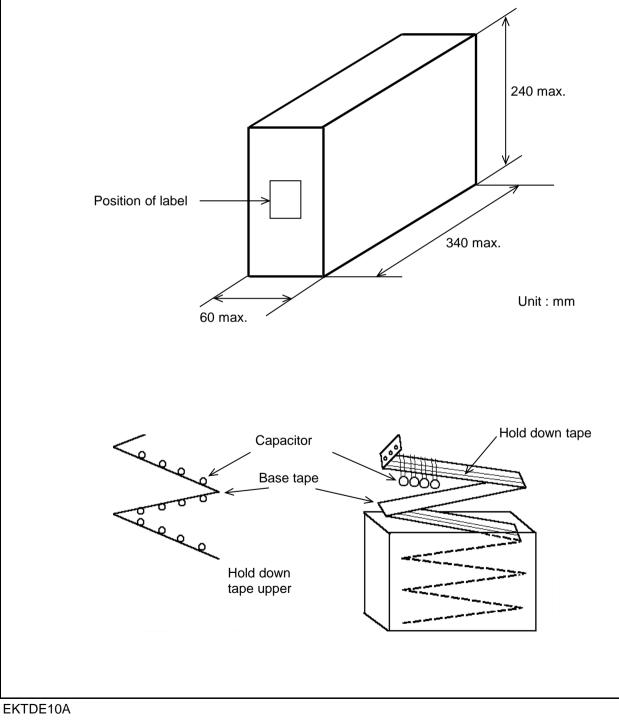
The size of packing case and packing way



Ammo pack taping type (Package : A)

- •The tape with capacitors is packed zigzag into a case.
- •When body of the capacitor is piled on other body under it.
- •There should be 3 pitches and over without capacitors in leader and trailer.

The size of packing case and packing way

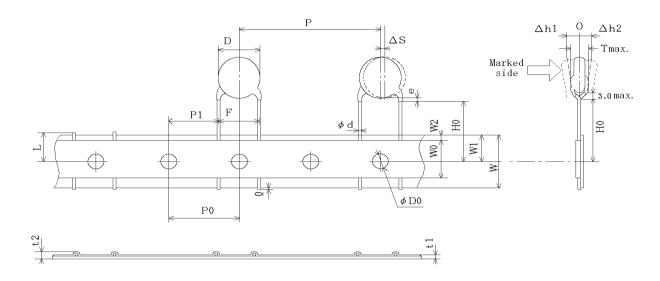


# 7. Taping specification

7-1. Dimension of capacitors on tape

Vertical crimp taping type < Lead Style : N4 >

Pitch of component 25.4 mm / Lead spacing 10.0 mm



Unit : mm

Item	Code	Dimensions	Remarks		
Pitch of component	Р	25.4+/-2.0			
Pitch of sprocket hole	P0	12.7+/-0.3			
Lead spacing	F	10.0+/-1.0			
Length from hole center to lead		7.7+/-1.5			
Body diameter		Please refer to	[Part number list ].		
Deviation along tape, left or right		0+/-2.0	They include deviation by lead bend.		
Carrier tape width	W	18.0+/-0.5			
Position of sprocket hole	W1	9.0+/-0.5	Deviation of tape width direction		
Lead distance between reference and bottom planes	HO	18.0+2.0/-0			
Protrusion length	l	+0.5~-1.0			
Diameter of sprocket hole	ΦD0	4.0+/-0.1			
Lead diameter		0.60+/-0.05			
Total tape thickness		0.6+/-0.3	They include hold down tape		
Total thickness of tape and lead wire	t2	1.5 max.	thickness.		
Deviation across tape, front Deviation across tape, rear		2.0 max.			
		2.0 max.			
Portion to cut in case of defect	L	11.0+0/-1.0			
Hold down tape width	W0	11.5 min.			
Hold down tape position	W2	1.5+/-1.5			
Coating extension on lead	е	Up to the end o	f crimp		
Body thickness	Т	Please refer to [Part number list ].			

