

### Description

The BSZ130N03LSG uses advanced trench technology

to provide excellent RDS(ON), low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = 30V I<sub>D</sub> = 30 A

 $R_{DS(ON)} < 13m\Omega @ V_{GS}=10V$ 

### Application

Battery protection

Load switch

Uninterruptible power supply

EAS

IAS

PD@Tc=25°C

TSTG

ТJ

R₀JA

 $R_{\theta}JC$ 

#### Package Marking and Ordering Information

0	0				
Product ID	Pack	Marking	Qty(PCS)		
BSZ130N03LSG	DFN3X3-8L	130N03LS XXXX	5000		
Absolute Maximum	 Ratings (Tc=25℃unless	otherwise noted)			
Symbol	Paramete	er	Rating	ng Units	
VDS	Drain-Source \	/oltage	30	V	
VGS	Gate-Source V	/oltage	±20	V	
I⊳@Tc=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>		30	А	
I₀@Tc=100°C	Continuous Drain Curre	ent, V <sub>GS</sub> @ 10V <sup>1</sup>	18	А	
IDM	Pulsed Drain Current <sup>2</sup>		55	А	

Single Pulse Avalanche Energy<sup>3</sup>

Avalanche Current

Total Power Dissipation<sup>4</sup>

Storage Temperature Range

**Operating Junction Temperature Range** 

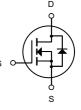
Thermal Resistance Junction-ambient <sup>1</sup>

Thermal Resistance Junction-Case<sup>1</sup>





DFN3X3-8L



N-Channel MOSFET

22.1

21

20

-55 to 150

-55 to 150

75

6

mJ

А

W

°C

°C

°C/W

°C/W



N-Channel Enhancement Mode MOSFET

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
$\triangle BV_{DSS} / \triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.022		V/°C
	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =10A		9	13	
RDS(ON)		V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A		12	20	mΩ
$V_{GS(th)}$	Gate Threshold Voltage		1.0		2.5	V
$\bigtriangleup V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	−V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA		-5.1		mV/°C
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	
		V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5	uA
lgss	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =1A		4.5		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		2.5		Ω
Qg	Total Gate Charge (4.5V)			7.2		
Qgs	Gate-Source Charge	V <sub>DS</sub> =20V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A		1.4		nC
Qgd	Gate-Drain Charge			2.2		
Td(on)	Turn-On Delay Time	V <sub>DD</sub> =12V , V <sub>GS</sub> =10V ,		4.1		
Tr	Rise Time	- VDD-12V, VGS-10V, RG=3.3 ID=5A		9.8		- ns
Td(off)	Turn-Off Delay Time			15.5		
T <sub>f</sub>	Fall Time			6.0		
Ciss	Input Capacitance			572		
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		81		pF
Crss	Reverse Transfer Capacitance			65		
ls	Continuous Source Current <sup>1,5</sup>				28	А
lsм	Pulsed Source Current <sup>2,5</sup>	−V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			55	А
Vsd	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1.2	V

## Electrical Characteristics (TJ=25 °C, unless otherwise noted)

Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width  $\leq 300 us$  , duty cycle  $\leq 2\%$ 

3. The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V,V<sub>GS</sub>=10V,L=0.1mH,I<sub>AS</sub>=21A

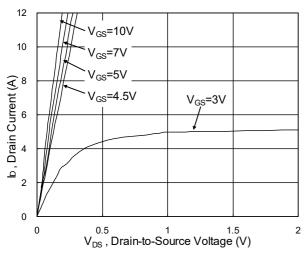
4 .The power dissipation is limited by 150  $^\circ\text{C}$  junction temperature 5.The data is theoretically the same as I\_D and

 $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.



## BSZ130N03LSG N-Channel Enhancement Mode MOSFET

### **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

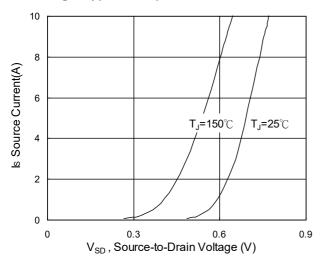


Fig.3 Forward Characteristics Of Reverse

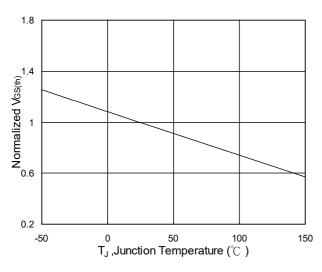


Fig.5 Normalized  $V_{\text{GS(th)}}\,vs.\,T_J$ 

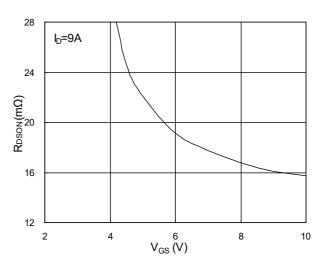


Fig.2 On-Resistance vs. Gate-Source

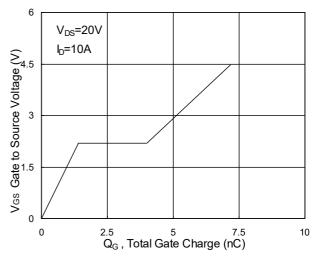


Fig.4 Gate-Charge Characteristics

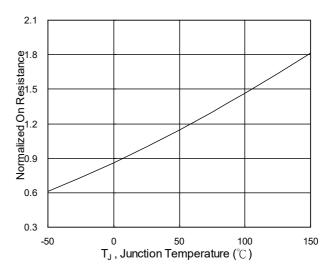


Fig.6 Normalized R<sub>DSON</sub> vs T<sub>J</sub>



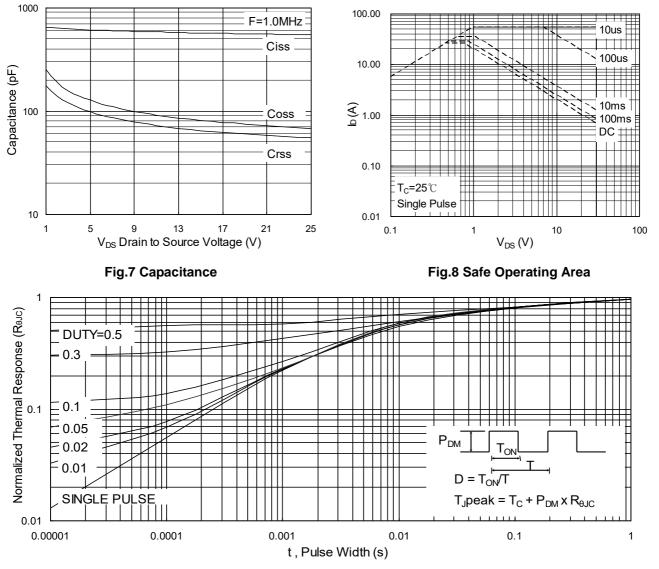


Fig.9 Normalized Maximum Transient Thermal Impedance

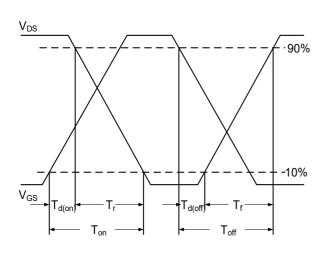
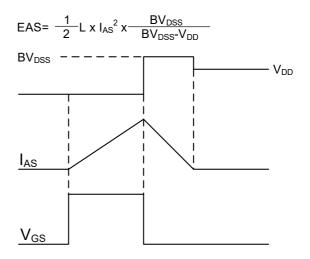


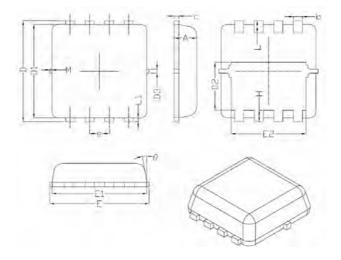
Fig.10 Switching Time Waveform







# DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
с	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
М	*	*	0.15	
θ		10 <sup>°</sup>	12 <sup>°</sup>	



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