

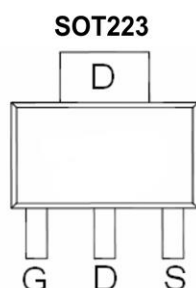
### GENERAL FEATURES

$V_{DS}$	$R_{DS(ON)} MAX$	$I_D MAX$
-250V	4.2Ω@-10V	-0.45A
	4.5mΩ@-4.5V	

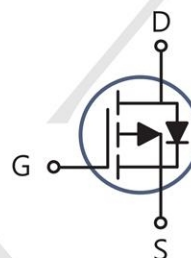
### Application

- Load/Power Switching
- Interfacing Switching
- Logic Level Shift

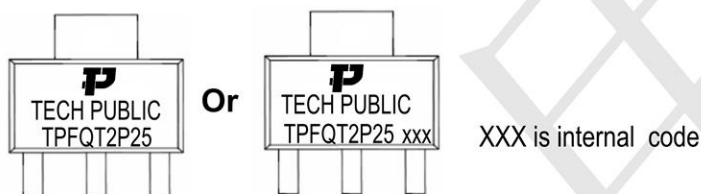
### Package and Pin Configuration



### Circuit diagram



### Marking:



### ABSOLUTE MAXIMUM RATINGS ( $T_C=25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Limit	Units	
Drain-Source Voltage	$V_{DS}$	-250	V	
Gate-Source Voltage	$V_{GS}$	±20	V	
Drain Current-Continuous (Note 2)	$I_D$	$T_A=25^{\circ}C$	-0.45	A
		$T_A=70^{\circ}C$	-0.35	A
- Pulsed (Note 1、Note 2)	$I_{DM}$	-1.72	A	
Single Pulse Avalanche Energy (Note 3)	EAS	12	mJ	
Maximum Power Dissipation	$T_A=25^{\circ}C$	1.8	W	
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^{\circ}C$	

### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	160	$^{\circ}C/W$
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### ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
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#### OFF CHARACTERISTICS

Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-250			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-200V, V <sub>GS</sub> =0V			-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> =0V			±100	nA

#### ON CHARACTERISTICS

Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1	-1.5	-3	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-0.3A		3.3	4.2	ohm
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.2A		3.4	4.5	ohm
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =-0.3A		1.5		S

#### DYNAMIC CHARACTERISTICS (Note 4)

Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =-100V, V <sub>GS</sub> =0 V f=1.0MHz		500		pF
Output Capacitance	C <sub>OSS</sub>			36.5		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			19.2		pF
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-100V, I <sub>D</sub> =-0.3A, V <sub>GS</sub> =-10V		8.9		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-100V, I <sub>D</sub> =-0.3A, V <sub>GS</sub> =-10V		1.5		nC
Gate-Drain Charge	Q <sub>gd</sub>			1.8		nC

#### SWITCHING CHARACTERISTICS (Note 4)

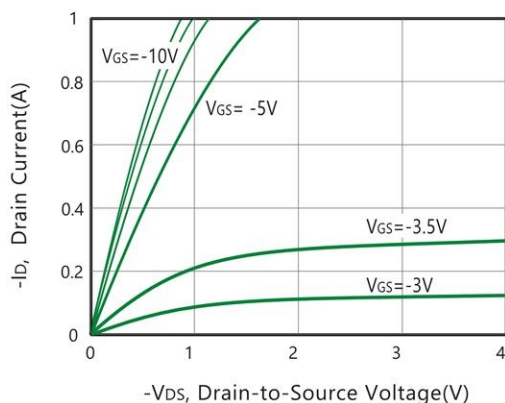
Turn-On Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> =-100V I <sub>D</sub> =-0.3A V <sub>GS</sub> =-10V R <sub>GEN</sub> = 6 ohm		1.9		ns
Rise Time	t <sub>r</sub>			1.6		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			21.6		ns
Fall Time	t <sub>f</sub>			10.2		ns

#### DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

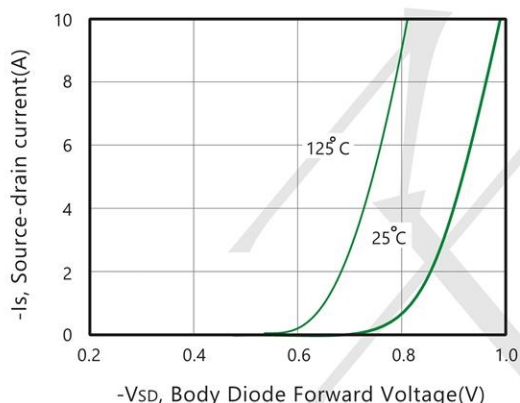
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A		-0.83	-1.3	V
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**Typical Electrical and Thermal Characteristics**

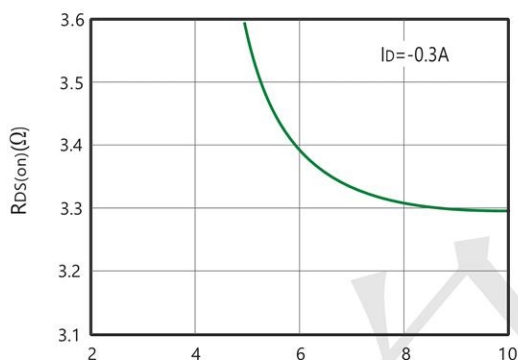
**Figure 1. Output Characteristics**



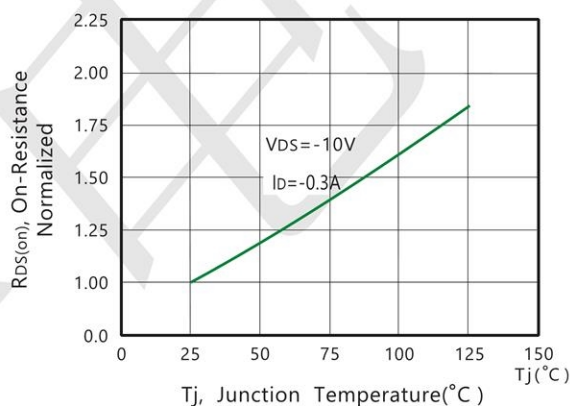
**Figure 2. Body Diode Forward Voltage Variation with Source Current**



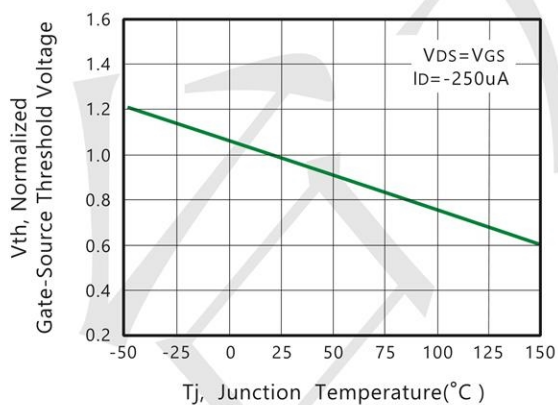
**Figure 3. On-Resistance vs. Gate-Source Voltage**



**Figure 4. On-Resistance Variation with Drain Current and Temperature**



**Figure 5. Gate Threshold Variation with Temperature**



**Figure 6. Gate Charge**

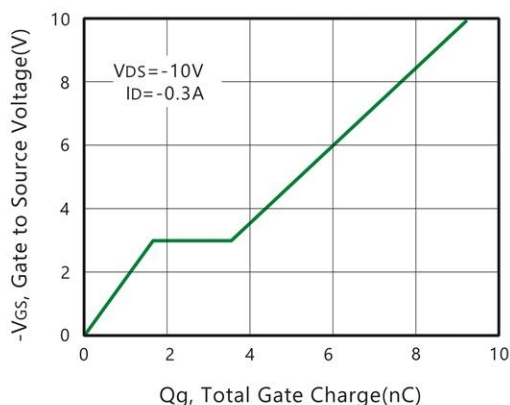


Figure 7. Capacitance

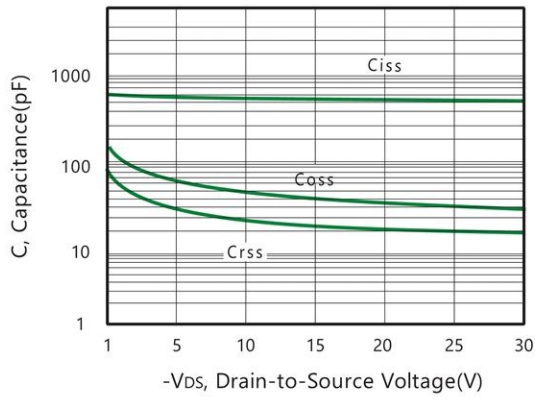


Figure 8. Maximum Safe Operating Area

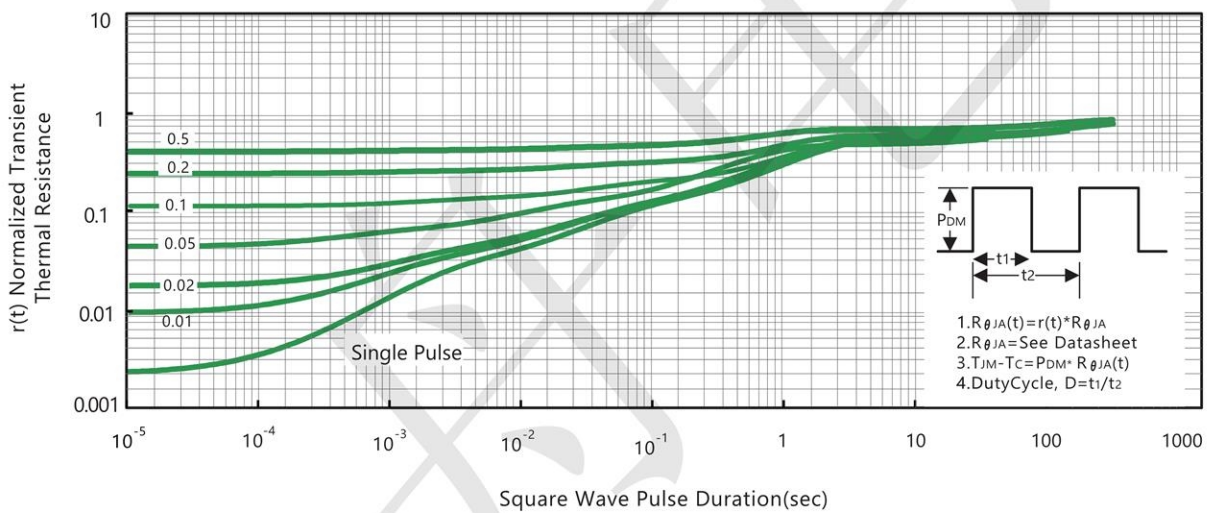
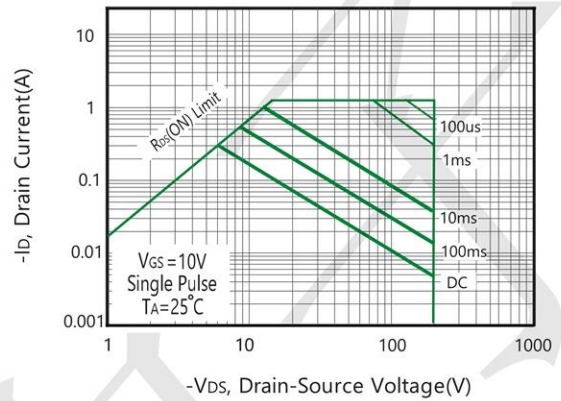


Figure 9. Normalized Thermal Transient Impedance Curve

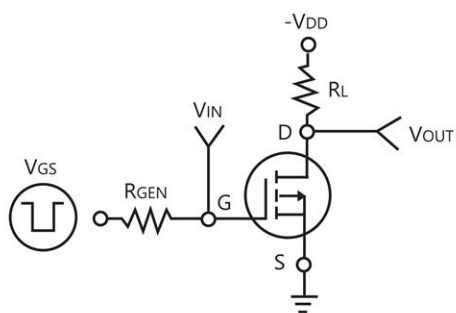


Figure 10a. Switching Test Circuit

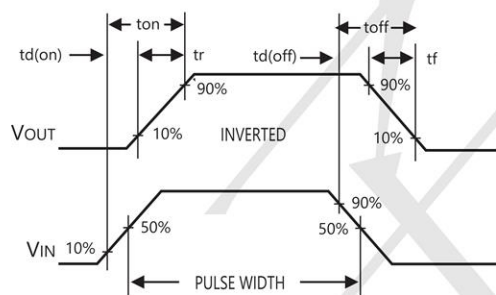
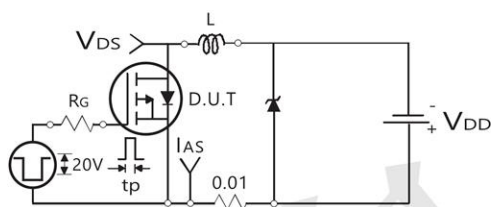
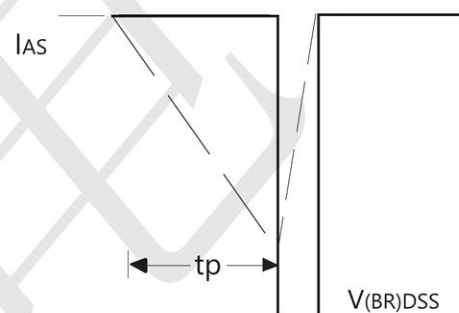


Figure 10b. Switching Waveforms



Unclamped Inductive Test Circuit

Figure 11a.

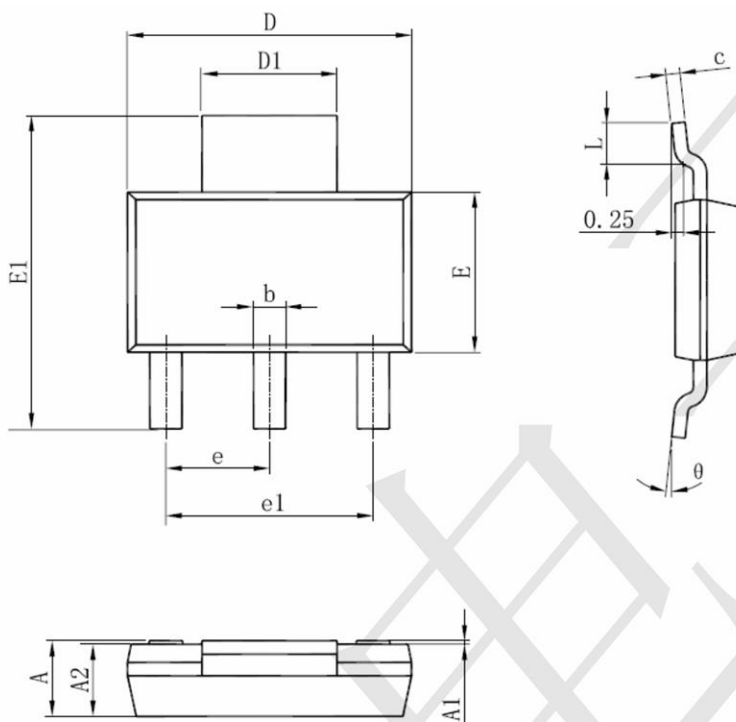


Unclamped Inductive Waveforms

Figure 11b.



SOT-223 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.520	1.800	0.060	0.071
A1	0.000	0.100	0.000	0.004
A2	1.500	1.700	0.059	0.067
b	0.660	0.820	0.026	0.032
c	0.250	0.350	0.010	0.014
D	6.200	6.400	0.244	0.252
D1	2.900	3.100	0.114	0.122
E	3.300	3.700	0.130	0.146
E1	6.830	7.070	0.269	0.278
e	2.300(BSC)		0.091(BSC)	
e1	4.500	4.700	0.177	0.185
L	0.900	1.150	0.035	0.045
θ	0°	10°	0°	10°