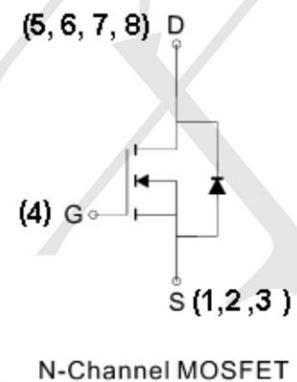
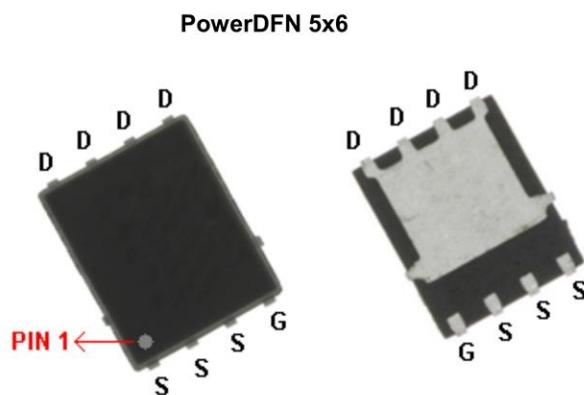


Product Summary

- ◆ $V_{DS} = 30V$ $I_D = 150A$
- $R_{DS(ON)} \leq 2.5m\Omega$ @ $V_{GS} = 10V$
- $R_{DS(ON)} \leq 3.3m\Omega$ @ $V_{GS} = 4.5V$

Application

- ◆ Load/Power switch
- ◆ Interfacing, logic switching
- ◆ Battery management for ultra portable electronics



Marking:150N03

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise specified)

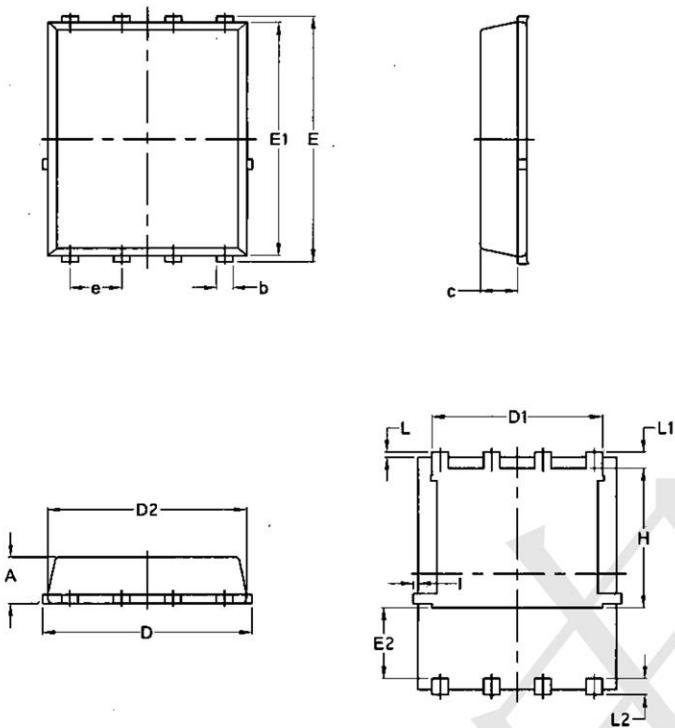
Parameter	Symbol	Maximum Ratings		Unit
Drain-Source Voltage	V_{DS}	30		V
Gate-Source Voltage	V_{GS}	± 20		V
Continuous Drain	$T_C=25^\circ C$	150		A
	$T_C=100^\circ C$	100		
Pulsed Drain Current	I_{DM}	350		A
Maximum Power Dissipation*	$T_A=25^\circ C$	75		W
	$T_A=100^\circ C$	30		
Operating Junction Temperature	T_J	-55 to 150		°C
Thermal Resistance-Junction to Ambient*	$R_{\theta JA}$	Steady State	45	°C/W
Thermal Resistance-Junction to Case*	$R_{\theta JC}$	3.3		°C/W

Electrical Characteristics ($T = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Limit	Min	Typ	Max	Unit
STATIC						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	30			V
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250 \mu\text{A}$	1.3		3.0	V
I_{GSS}	Gate Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$			± 100	nA
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$			1	μA
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^a	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=27\text{A}$		1.9	2.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=20\text{A}$		2.5	3.3	
V_{SD}	Diode Forward Voltage	$I_{\text{S}}=2.8\text{A}, V_{\text{GS}}=0\text{V}$		0.75	1.1	V
DYNAMIC						
Q_g	Total Gate Charge	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=27\text{A}$		58		nC
Q_{gs}	Gate-Source Charge			23		
Q_{gd}	Gate-Drain Charge			30		
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$		5930		pF
C_{oss}	Output Capacitance			660		
C_{rss}	Reverse Transfer Capacitance			220		
R_g	Gate-Resistance	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$		0.85		Ω
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=15\text{V}, R_{\text{L}}=15\Omega$ $I_{\text{D}}=1\text{A}, V_{\text{GEN}}=10\text{V}$ $R_{\text{G}}=6\Omega$		36		Ns
t_r	Turn-On Rise Time			23		
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time			170		
t_f	Turn-Off Fall Time			44		



Package Outline Dimensions PDFN5*6-8L



Symbol	Common			
	mm		Inch	
	Mim	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070