# Datasheet

# **ROHM**

# SiC Schottky Barrier Diode

$V_R$	650V
I <sub>F</sub>	12A
$Q_{C}$	18nC

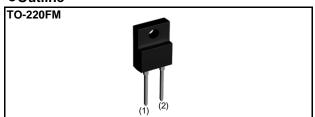
## Features

- 1) Shorter recovery time
- 2) Reduced temperature dependence
- 3) High-speed switching possible

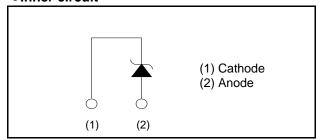
# Applications

- · PFC Boost Topology
- · Secondary Side Rectification
- Data Center
- · PV Power Conditioners

## Outline



# ●Inner circuit



# Packaging specifications

	jg - p	
	Packaging	Tube
	Reel size (mm)	-
Type	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	50
	Packing code	С
	Marking	SCS212AM

# ● Absolute maximum ratings (T<sub>vj</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Reverse voltage (re	epetitive peak)	$V_{RM}$	650	V
Reverse voltage (D	C)	$V_R$	650	V
Continuous forward	I current (T <sub>c</sub> = 75°C)	I <sub>F</sub>	12 *1	А
Surge non-	PW=10ms sinusoidal, T <sub>vj</sub> =25°C		43	А
repetitive forward	PW=10ms sinusoidal, T <sub>vj</sub> =150°C	I <sub>FSM</sub>	34	А
current	PW=10μs square, T <sub>vj</sub> =25°C		170	А
Repetitive peak forward current		I <sub>FRM</sub>	32 *2	А
PW=10ms, T <sub>vj</sub> =25°C		∫ i²dt	9.2	A <sup>2</sup> s
i <sup>2</sup> t value	PW=10ms, T <sub>vj</sub> =150°C	J i*dt	5.7	A <sup>2</sup> s
Total power disspation		$P_{D}$	37 <sup>*3</sup>	W
Virtual Junction temperature		$T_{vj}$	175	°C
Range of storage temperature		T <sub>stg</sub>	−55 to +175	°C

<sup>\*1</sup> Limited by maximum  $T_{vj}$  and for Max.  $R_{thJC}$ .

<sup>\*2</sup>  $T_c$ =100°C,  $T_{vj}$ =150°C, Duty cycle=10% \*3  $T_c$ =25°C

# ullet Electrical characteristics (T<sub>vj</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Linit
			Min.	Тур.	Max.	Unit
DC blocking voltage	$V_{DC}$	I <sub>R</sub> =2.4mA	650	-	-	V
	V <sub>F</sub>	I <sub>F</sub> =12A,T <sub>vj</sub> =25°C	-	1.35	1.55	V
Forward voltage		I <sub>F</sub> =12A,T <sub>vj</sub> =150°C	-	1.55	-	V
		I <sub>F</sub> =12A,T <sub>vj</sub> =175°C	-	1.63	-	V
Reverse current	I <sub>R</sub>	V <sub>R</sub> =650V,T <sub>vj</sub> =25°C	-	2.4	240	μΑ
		V <sub>R</sub> =650V,T <sub>vj</sub> =150°C	-	36	-	μΑ
		V <sub>R</sub> =650V,T <sub>vj</sub> =175°C	-	84	-	μΑ
Total capacitance	С	V <sub>R</sub> =1V,f=1MHz	-	440	-	pF
		V <sub>R</sub> =600V,f=1MHz	-	44	-	pF
Total capacitive charge	Q <sub>C</sub>	V <sub>R</sub> =400V,di/dt=350A/μs	-	18	-	nC
Switching time	t <sub>C</sub>	V <sub>R</sub> =400V,di/dt=350A/μs	-	16	-	ns

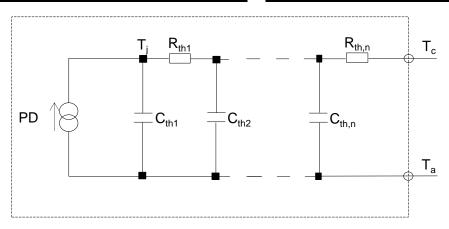
# ●Thermal characteristics

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Offic
Thermal resistance	$R_{thJC}$	-	-	3.4	4.0	K/W

# ●Typical Transient Thermal Characteristics

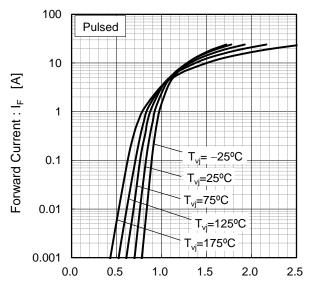
Symbol	Value	Unit
R <sub>th1</sub>	6.06E-01	
R <sub>th2</sub>	1.29E+00	K/W
R <sub>th3</sub>	1.51E+00	

Symbol	Value	Unit
C <sub>th1</sub>	2.09E-03	
C <sub>th2</sub>	7.52E-03	Ws/K
C <sub>th3</sub>	7.44E-01	



# •Electrical characteristic curves

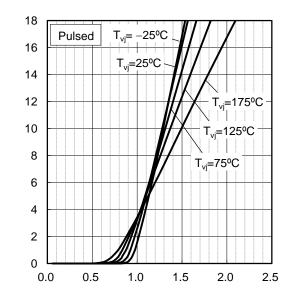
Fig.1 V<sub>F</sub> - I<sub>F</sub> Characteristics



Forward Voltage : V<sub>F</sub> [V]

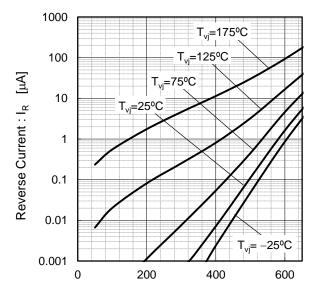
Fig.2 V<sub>F</sub> - I<sub>F</sub> Characteristics

Forward Current : I<sub>F</sub> [A]



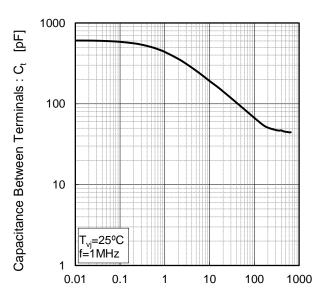
Forward Voltage : V<sub>F</sub> [V]

Fig.3  $V_R$  -  $I_R$  Characteristics



Reverse Voltage: V<sub>R</sub> [V]

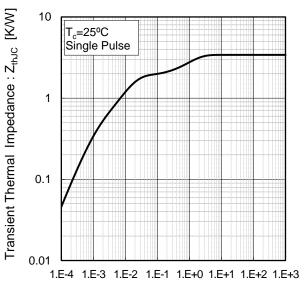
Fig.4 V<sub>R</sub> - C<sub>t</sub> Characteristics



Reverse Voltage : V<sub>R</sub> [V]

## •Electrical characteristic curves

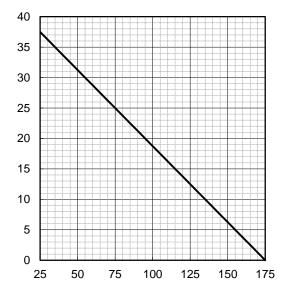
Fig.5 Typical Transient Thermal Impedance vs. Pulse Width



Pulse Width: PW [s]

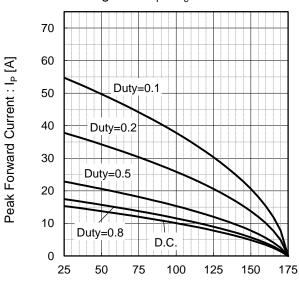
Fig.6 Power Dissipation

Power Dissipation [W]



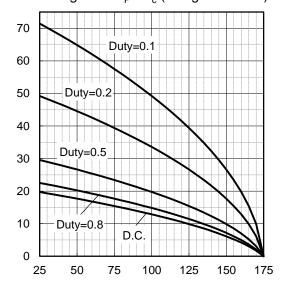
Case Temperature : T<sub>c</sub> [°C]

Fig.7\*4 Maximum peak forward current derating curve  $I_P - T_c$ 



Case Temperature :  $T_c$  [ $^{\circ}$ C] \*4 Based on max Vf, max  $Z_{thJC}$  Valid for switching of above 10kHz, excluding D.C. curve.

Fig.8\*5 Typical peak forward current derating curve I<sub>P</sub> - T<sub>c</sub> (Not guaranteed)

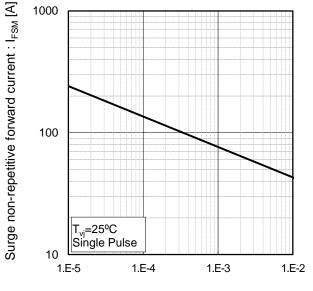


Case Temperature : T<sub>c</sub> [°C]
\*5 Based on typ Vf, typ Z<sub>thJC</sub>
Typical value, not guaranteed
Valid for switching of above 10kHz,
excluding D.C. curve

Peak Forward Current : I<sub>P</sub> [A]

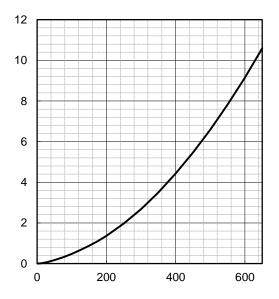
## •Electrical characteristic curves

Fig.9 Surge non-repetitive forward current vs. Pulse width (Sinusoidal waveform)



Pulse Width: PW [s]

Fig.10 Typical capacitance store energy

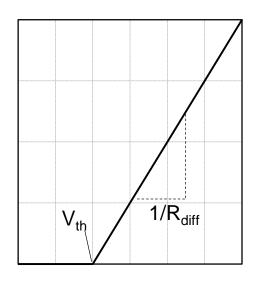


Capacitance stored energy :  $E_C[\mu J]$ 

Reverse Voltage : V<sub>R</sub> [V]

# Symplified forward characteristic model

Fig.11 Equivalent forward current curve



Forward Voltage: V<sub>F</sub>

$$V_F = V_{th} + R_{diff} I_F$$

$$\begin{aligned} & V_{th} \left( \ T_{vj} \ \right) = a_0 + a_1 \, T_{vj} \\ & R_{diff} \left( \ T_{vj} \ \right) = b_0 + b_1 \, T_{vj} + b_2 \, T_{vj}^{\ 2} \end{aligned}$$

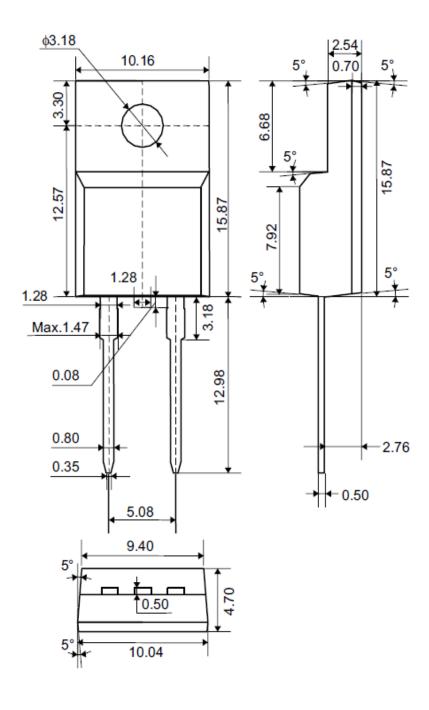
Symbol	Typical Value	Unit
a <sub>0</sub>	9.35E-01	V
a <sub>1</sub>	-1.12E-03	V/°C
b <sub>0</sub>	3.32E-02	Ω
b <sub>1</sub>	8.50E-05	Ω/°C
$b_2$	9.00E-07	$\Omega$ /°C <sup>2</sup>

 $T_{vj}$  in °C; -55°C <  $T_{vj}$  < 175°C ;  $I_F$  < 24 A

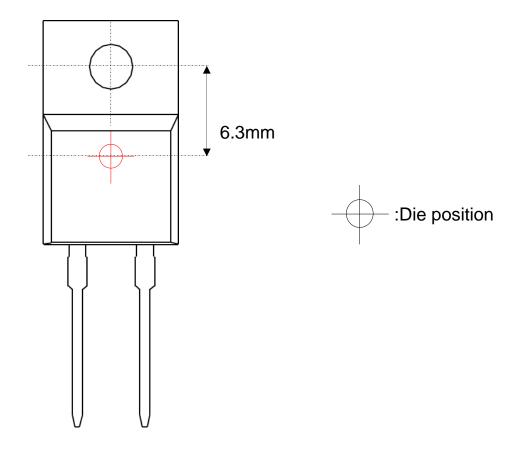
Forward Current: I<sub>F</sub>

# ●Dimensions (Unit: mm)

# TO-220FM (2pin)



# **●**Die Bonding Layout



- •Front view of the packaging.
- •Dimensions are design values.
- ·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

7/7

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