# Am26S12/Am26S12A

Quad Bus Transceivers

#### DISTINCTIVE CHARACTERISTICS

- · Quad high-speed bus transceivers
- Driver outputs can sink 100mA at 0.7V typically
- · Choice of receiver hysteresis characteristics

#### **GENERAL DESCRIPTION**

The Am26S12/Am26S12A are high-speed quad Bus Transceivers consisting of four high-speed bus drivers with open-collector outputs capable of sinking 100mA at 0.7 volts and four high-speed bus receivers. Each driver output is brought out and also connected internally to the high-speed bus receiver. The receiver has an input hysteresis characteristic and a TTL output capable of driving ten TTL leads.

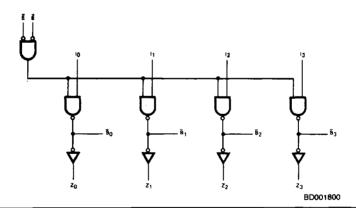
An active LOW, two-input AND gate controls the four drivers so that outputs of different device drivers can be connected together for party-line operation. The enable inputs can be conveniently driven by active LOW decoders such as the Am54S/74S139.

The high-drive capability in the LOW state allows party-line operation with a line impedance as low as  $100\Omega$ . The line

can be terminated at both ends, and still give considerable noise margin at the receiver. The hysteresis characteristic of the Am26S12 receiver is chosen so that the receiver output switches to a HIGH logic level when the receiver input is at a HIGH logic level and moves to 1.4 volts typically, and switches to a LOW logic level when the receiver input is at a LOW logic level and moves to 2.0 volts typically. This hysteresis characteristic makes the receiver very insensitive to noise on the bus.

The Am26S12A is functionally identical to the Am26S12 but has a different hysteresis characteristic so that the output switches with the input being typically at 1.2 volts or 2.25 volts. In both devices the threshold margin, the difference between the switching points, is greater than 0.4 volts.

#### **BLOCK DIAGRAM**

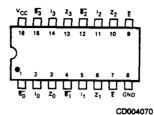


#### **RELATED PRODUCTS**

Part No.	Description
26S10 26S11 2915A 2916A 2917A	Quad Bus Transceiver Quad Bus Transceiver Quad Three-State Bus Transceiver with Interface Logic Quad Three-State Bus Transceiver with Interface Logic Quad Three-State Bus Transceiver with Interface Logic

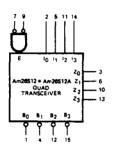
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## CONNECTION DIAGRAM Top View



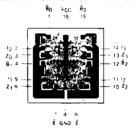
Note: Pin 1 is marked for orientation

#### LOGIC SYMBOL



LS000860 V<sub>cc</sub> = Pin 16 GND = Pin 8

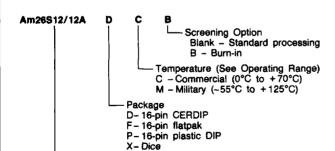
#### **METALLIZATION AND PAD LAYOUT**



DIE SIZE: 0.071" x 0.072"

#### ORDERING INFORMATION

AMD products are available in several packages and operating ranges. The order number is formed by a combination of the following: Device number, speed option (if applicable), package type, operating range and screening option (if desired).



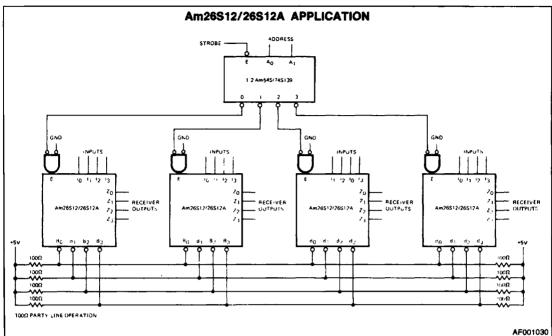
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Device	type	
		Transceivers

Valid Con	nbinations
Am26S12 Am26S12A	PC DC, DM FM XC, XM

#### **Valid Combinations**

Consult the AMD sales office in your area to determine if a device is currently available in the combination you wish.

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#### **ABSOLUTE MAXIMUM RATINGS**

Storage Temperature65°C to +150°C
Temperature (Ambient) Under Bias55°C to +125°C
Supply Voltage to Ground Potential
(Pin 16 to Pin 8) Continuous0.5V to +7.0V
DC Voltage Applied to Outputs For
High Output State0.5V to +V <sub>CC</sub> max
DC Input Voltage0.5V to +5.5V
DC Output Current, Into (Bus)200mA
DC Output Current, Into Outputs
(Receiver)30mA
DC Input Current30mA to +5.0mA

Stresses above those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent device failure. Functionality at or above these limits is not implied. Exposure to absolute maximum ratings for extended periods may affect device reliability.

#### **OPERATING RANGES**

0°C to +70°C
+ 4.75V to +5.25V
55°C to +125°C
+ 4.5V to + 5.5V
over which the function-

## DC CHARACTERISTICS over operating range unless otherwise specified

Parameters	Description	Test Conditions			Min	Typ (Note 1)	Max	Unite
lcc	Power Supply Current	V <sub>CC</sub> = MAX				46	70	mA
IBUS	Bus Leakage Current	V <sub>CC</sub> = MAX or 0V; V <sub>BUS</sub> = 4.0V; Driver in OFF State					100	μΑ
Driver Cha	racteristics			<del></del>				
VOL Output LOV			COM'L	IOL = 100mA		0.7	0.8	Volts
	Output LOW Voltage	V <sub>CC</sub> = MiN		IOL = 60mA		0.55	0.7	
		VIN = VIH or VIL	- MIL	IOL = 100mA		0.7	0.85	Volts
V <sub>IH</sub>	Input HIGH Voltage		•	•	2.0			Volta
VIL	Input LOW Voltage	1					0.8	Volte
VI	Input Clamp Voltage	V <sub>CC</sub> = MIN, I <sub>IN</sub> = −18mA					-1.2	Volte
l <sub>l</sub>	Input Current at Maximum Input Voltage	V <sub>CC</sub> = MAX, V <sub>I</sub> = 5.5V					1.0	mA
liH	Unit Load Input HIGH Current	V <sub>CC</sub> = MAX, V <sub>I</sub> = 2.4V				1.0	40	μΑ
tiL	Unit Load Input LOW Current	V <sub>CC</sub> = MAX, V <sub>I</sub> = 0.4V				-0.4	-1.6	mA
Receiver (	Characteristics							
Vон	Output HIGH Voltage	V <sub>CC</sub> = MIN, I <sub>OH</sub> = -800μA V <sub>IN</sub> = V <sub>IL</sub> (Receiver)			2.4			Volt
VOL	Output LOW Voltage	V <sub>CC</sub> = MIN, I <sub>OL</sub> = 20mA V <sub>IN</sub> = V <sub>IL</sub> (Receiver)				0.4	0.5	Volt
VIH	Input HIGH Level Threshold		6S12 6S12A		1.8	2.0	2.2	Volt
	·	E = H Am26S12 Am26S12A		1.2	1,4	1.6	Volts	
VIL	Input LOW Level Threshold			1.0	1.2	1.4		
VTM	Input Threshold Margin	Е-н			0.4			Volt
los	Output Short Circuit Current	V <sub>CC</sub> = MAX, V <sub>C</sub>	V0.0 = T1 K		-20	<del>                                     </del>	-55	mA

Notes: 1. Typical limits are at V<sub>CC</sub> = 5.0V, 25°C ambient and maximum loading.
2. For the Am26S12FM, Am26S12AFM the output current must be limited at 60mA or the maximum case temperature limited to 125°C for correct operation.

3. Hysteresis characteristics data tested at 25°C only.

### SWITCHING CHARACTERISTICS (TA = +25°C, VCC = 5.0V)

Parameters	Description	Conditions	Min	Тур	Max	Unite
<b>t</b> РĻН	Turn Off Delay Input to Bus	C <sub>LB</sub> = 15 pF, R <sub>LB</sub> = 100 Ω		7	11	ns
t <sub>PHL</sub>	Turn On Delay Input to Bus	C <sub>LB</sub> = 300 pF, R <sub>LB</sub> = 50 Ω		14	21	ns
t <sub>PLH</sub>	Turn Off Delay Enable to Bus	$C_{LB}$ = 15 pF, $R_{LB}$ = 50 $\Omega$		10	15	ns
tPHL	Turn On Delay Enable to Bus	$C_{LB}$ = 15 pF, $R_{LB}$ = 50 $\Omega$		10	15	ns
tpLH	Turn Off Delay Bus to Output	C <sub>L</sub> = 15 pF		18	26	ns
t <sub>PHL</sub>	Turn On Delay Bus to Output	C <sub>L</sub> = 15 pF		18	26	ns