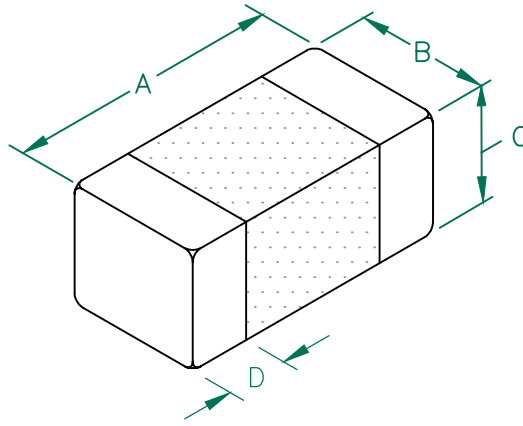


HI0603N300R-10

PHYSICAL DIMENSIONS:

A	1.60	[.063]	+	0.15	[.006]
B	0.80	[.031]	+	0.15	[.006]
C	0.80	[.031]	+	0.15	[.006]
D	0.36	[.014]	+	0.15	[.006]

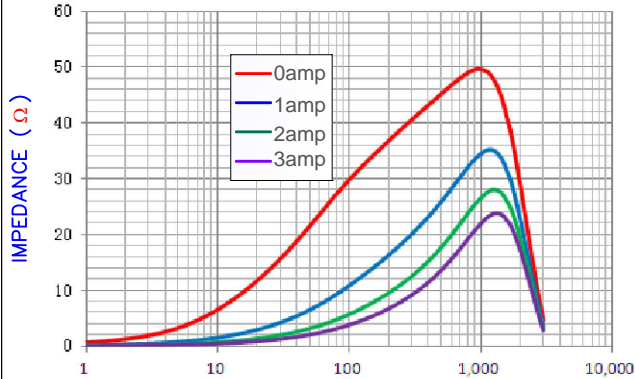


ELECTRICAL CHARACTERISTICS:			
Z @ 100MHz (Ω)	DCR (Ω)	Rated Current	
Nominal	30		
Minimum	22		
Maximum	38	0.04	3000 mA

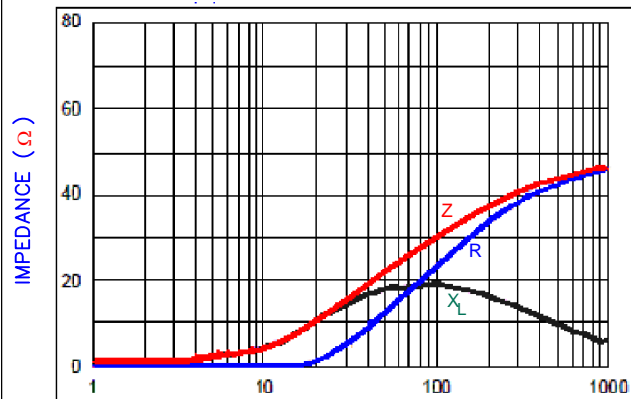
NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL.
2. TERMINATION FINISH IS 100% TIN.
3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
4. OPERATING TEMPERATURE TEMP: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$. (INCLUDING SELF-HEATING)

Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)
|Z|, R, AND X vs. FREQUENCY



FREQUENCY (MHz)

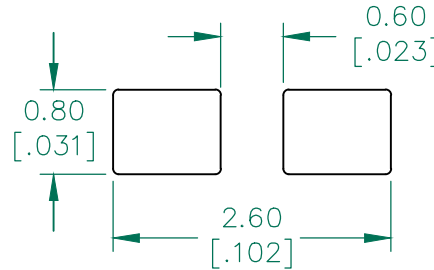
Z

R

X_L

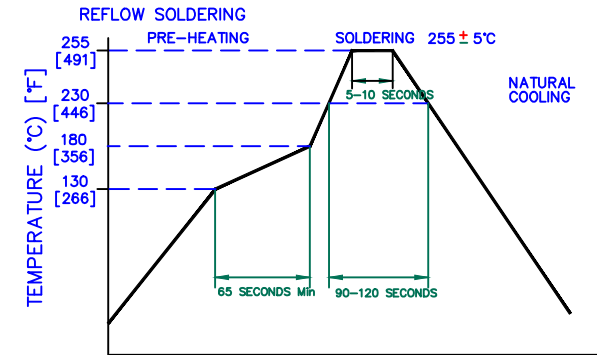
AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture.

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [.030] to this dimension)

RECOMMENDED SOLDERING CONDITIONS



ROHS

DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.		Laird	
PROJECT/PART NUMBER:				REV	PART TYPE:	DRAWN BY:	
HI0603N300R-10				C	CO-FIRE	QU	
C	UPDATE LAIRD LOGO AND NOTES 4	08/05/13	QU	DATE:	03/01/11	SCALE:	SHEET:
B	ADD DIRECT CURRENT BIAS CURVE	06/13/12	QU				
A	ORIGINAL DRAFT	03/01/11	QU	CAD #	HI0603N300R-10-C	TOOL #	
REV	DESCRIPTION	DATE	INT			1 of 1	