

#### **DESCRIPTION**

The SLESD5B5CL is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, portable devices, digital cameras, power supplies and many other portable applications where board space comes at a premium. Also because of its low capacitance, it is suited for use in high frequency designs such as USB 2.0 high speed, VGA, DVI, SDI and other high speed line applications.

This device has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD (electrostatic discharge), and EFT (electrical fast transients).

#### **ORDERING INFORMATION**

→ Device: SLESD5B5CL→ Package: SOD-523

♦ Marking: LB

→ Material: Halogen free→ Packing: Tape & Reel

## **FEATURES**

- ♦IEC61000-4-4 (EFT) 40A (5/50ηs)
- ♦Peak power dissipation: 40W (8/20µs)
- ♦Protects one I/O line
- ♦Low clamping voltage
- ♦ Working voltages : 5V
- ♦Low leakage current
- ♦Low capacitance

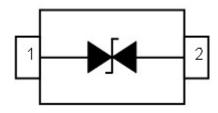
## **MACHANICAL DATA**

- ♦SOD-523 package
- ♦ Terminals: Tin plated, solderable per MIL-STD-750, method 2026
- ♦ Packaging: Tape and Reel
- ♦ Reel size: 7 inch
- ♦Weight: 0.0018 gram (approx.)

#### **APPLICATIONS**

- ♦ High Speed Line :USB1.0/2.0, VGA, DVI, SDI,
- ♦ Serial and Parallel Ports
- ♦ Notebooks, Desktops, Servers
- ♦ Projection TV
- ♦ Cellular handsets and accessories
- ♦Portable instrumentation
- ♦ Peripherals

# **PIN CONFIGURATION**



# **PACKAGE OUTLINE**





ABSOLUTE MAXIMUM RATING							
Symbol	Parameter	Value	Units				
V <sub>ESD</sub>	ESD per IEC 61000-4-2 (Contact) ESD per IEC 61000-4-2 (Air)	±8 ±15	kV				
P <sub>PP</sub>	Peak Pulse Power (8/20μs)	40	W				
T <sub>OPT</sub>	Operating Temperature	-55~150	°C				
T <sub>STG</sub>	Storage Temperature	-55~150	°C				

ELECTRICAL CHARACTERISTICS (Tamb=25°C)									
Symbol	Parameter	Test Condition	Min	Тур	Max	Units			
$V_{RWM}$	Reverse Working Voltage				5.0	V			
$V_{BR}$	Reverse Breakdown Voltage	I <sub>T</sub> = 1mA	5.2		8.0	V			
I <sub>R</sub>	Reverse Leakage Current	V <sub>RWM</sub> = 5V			2.0	μΑ			
V <sub>C</sub>	Clamping Voltage	$I_{PP} = 1A, t_p = 8/20 \mu s$			9	V			
CJ	Junction Capacitance	V <sub>R</sub> = 0V, f = 1MHz		4.5	6	pF			

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# **ELECTRICAL CHARACTERISTICS CURVE**

