

## N-Channel 100-V (D-S) MOSFET

PRODUCT	SUMMARY	
V <sub>(BR)DSS</sub> (V)	r <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)
100	0.017 at V <sub>GS</sub> = 10 V	70 <sup>a</sup>

#### FEATURES

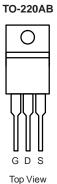
- TrenchFET<sup>®</sup> Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R<sub>g</sub> Tested

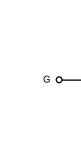
#### **APPLICATIONS**

D

• Isolated DC/DC Converters







N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_{C} = 25 \text{ °C}$ , unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	100	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>C</sub> = 25 °C	I	70 <sup>a</sup>		
Continuous Drain Current $(1) = 173^{\circ}$ C)	T <sub>C</sub> = 125 °C	D ID	35 <sup>a</sup>	A	
Pulsed Drain Current		I <sub>DM</sub>	145	A	
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	31		
Single Pulse Avalanche Energy <sup>b</sup>	L = 0.11111	E <sub>AS</sub>	60	mJ	
Maximum Drawn Diasia atia ab	T <sub>C</sub> = 25 °C	P <sub>D</sub>	355 <sup>c</sup>	W	
Maximum Power Dissipation <sup>b</sup>	T <sub>A</sub> = 25 °C <sup>d</sup>	r'D	3.35	V	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

TINGS			
	Symbol	Limit	Unit
PCB Mount	R <sub>thJA</sub>	40	°C/W
	R <sub>thJC</sub>	0.4	C/W
	PCB Mount	PCB Mount R <sub>thJA</sub>	Symbol Limit   PCB Mount R <sub>thJA</sub> 40

Notes:

- a. Package limited.
- b. Duty cycle  $\leq$  1 %.
- c. See SOA curve for voltage derating.

d. When Mounted on 1" square PCB (FR-4 material).

$\begin{array}{ c c c c c c } \hline Parameter & Symbol & Test Conditions & Min. & Typ. & Max. & Unit \\ \hline Static & & & & & & & & & & & & & & & & & & &$	SPECIFICATIONS T <sub>J</sub> = 25 $^{\circ}$	C, unless o	therwise noted				
$\begin{array}{ c c c c c c } \hline Drain-Source Breakdown Voltage & V_{(BR)DSS} & V_{DS} = 0 \ V, \ V_{DS} = V_{GS}, \ V_{DS} = V_{GS}, \ V_{DS} = V_{GS}, \ V_{DS} = 0 \ V, \ V_{QS} = 250 \ \mu\text{A} & 2 & 4 & V & \\ \hline Cate-Body Leakage & I_{QSS} & V_{DS} = 0 \ V, \ V_{QS} = 20 \ V & 4 & 100 & nA & \\ \hline V_{DS} = 100 \ V, \ V_{QS} = 0 \ V, \ V_{QS} = 0 \ V & 1 & V & \\ \hline V_{DS} = 100 \ V, \ V_{QS} = 0 \ V, \ V_{DS} = 0 \ V, \ $	Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Static						
$ \begin{array}{ c c c c } \hline \mbox{Gate-Threshold Voltage} & V_{GS(th)} & V_{DS} = 250 \ \mu A & 2 & 4 & -1 \\ \hline \mbox{Gate-Day Leakage} & l_{GSS} & V_{DS} = 0 \ V, \ V_{GS} = 20 \ V & -1 & 1 & 1 \\ \hline \mbox{VD} = 100 \ V, \ V_{GS} = 0 \ V, \ T_{J} = 125 \ ^{\circ}C & -5 & 50 & -5 \\ \hline \mbox{VD} = 100 \ V, \ V_{GS} = 0 \ V, \ T_{J} = 125 \ ^{\circ}C & -5 & -5 & -5 & -5 & -5 & -5 & -5 & -$	Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{DS} = 0 V, I_{D} = 250 \mu A$	100			V
$ \begin{array}{ c c c c c } \hline V_{DS} = 100 \ V, \ V_{GS} = 0 \ V \\ \hline V_{DS} = 100 \ V, \ V_{GS} = 0 \ V, \ U_{J} = 125 \ ^{\circ}C \\ \hline V_{DS} = 100 \ V, \ V_{GS} = 0 \ V, \ U_{J} = 125 \ ^{\circ}C \\ \hline V_{DS} = 100 \ V, \ V_{GS} = 0 \ V, \ U_{J} = 175 \ ^{\circ}C \\ \hline V_{DS} = 100 \ V, \ V_{GS} = 0 \ V, \ U_{J} = 175 \ ^{\circ}C \\ \hline V_{DS} = 100 \ V, \ V_{GS} = 0 \ V, \ U_{J} = 175 \ ^{\circ}C \\ \hline V_{DS} = 100 \ V, \ V_{DS} = 10 \ V, \ U_{DS} = 10 \ V, \ U_{D$	Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2		4	v
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V			± 100	nA
$ \begin{array}{ c c c c c c } \hline \begin{tabular}{ c c c c c } \hline \begin{tabular}{ c c c c c c c } \hline \begin{tabular}{ c c c c c c c c c c c c c c c c c c c$			$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	μA
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$			$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$			250	
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 V$ , $V_{GS} = 10 V$	120			А
$\begin{tabular}{ c c c c c c } \hline $V_{GS} = 10 \ V, \ I_D = 30 \ A, \ T_J = 175 \ ^{\circ}\ C & 0.037 & 0 & 0.037 & 0 & 0.037 & 0 & 0.037 & 0 & 0.037 & 0 & 0.037 & 0 & 0.037 & 0 & 0.037 & 0 & 0.037 & 0 & 0.037 & 0 & 0.037 & 0 & 0.037 & 0 & 0& 0& 0& 0& 0& 0& 0& 0& 0& 0& 0& $			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.017		Ω
$ \begin{array}{c c c c c c c c } \hline Forward Transconductance^a & g_{fs} & V_{DS} = 15 \ V, \ I_{D} = 30 \ A & 25 & & & S \\ \hline \mbox{Dynamic}^b & & & & & & & & & & & & & & & & & & &$	Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C		0.023		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C		0.037		
$ \begin{array}{c c c c c c c } \hline \mbox{Input Capacitance} & C_{1SS} & & & & & & & & & & & & & & & & & & $	Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	25			S
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Dynamic <sup>b</sup>						
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Input Capacitance	C <sub>iss</sub>			1800		
$ \begin{array}{c c c c c c c c } \hline Total Gate Charge^{c} & Q_{g} & & & & & & & & & & & & & & & & & & &$	Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		210		pF
$ \begin{array}{c c c c c c c } \hline Gate-Source Charge^{C} & $Q_{gd}$ & $V_{DS} = 100 \ V, \ V_{GS} = 10 \ V, \ V_{DS} = 58 \ A & $23$ & $100 \ V, \ V_{DS} = 58 \ A & $34$ & $100 \ V, \ V_{DS} = 58 \ A & $34$ & $100 \ V, \ V_{DS} = 58 \ A & $34$ & $100 \ V, \ V_{DS} = 58 \ A & $34$ & $100 \ V, \ V_{DS} = 58 \ A & $0.5$ & $1.3$ & $3.1$ & $\Omega$ \\ \hline Gate Resistance & $R_{g}$ & $0.5$ & $1.3$ & $3.1$ & $\Omega$ \\ \hline Turn-On Delay Time^{C} & $t_{d(on)}$ & $V_{DD} = 100 \ V, \ R_{L} = 1.5 \ \Omega & $220$ & $330$ & $100 \ V, \ R_{D} = 58 \ A, \ V_{GEN} = 10 \ V, \ R_{g} = 2.5 & $100 \ V, \ R_{g} = 2.5$ & $115 \ V$ & $115 \ V$ & $115 \ V$ & $100 \ R_{g} = 1.5$ & $115 \ V$ & $100 \ R_{g} = 1.5$ & $100$	Reverse Transfer Capacitance	C <sub>rss</sub>			110		
$ \begin{array}{c c c c c c } \hline Gate-Drain Charge^{C} & Q_{gd} & & & & & & & & & & & & & & & & & & &$	Total Gate Charge <sup>c</sup>	Qg			90		
$ \begin{array}{c c c c c c c } \hline Gate Resistance & R_g & 0.5 & 1.3 & 3.1 & \Omega \\ \hline Turn-On Delay Time^{C} & t_{d(on)} & & & & & & & & & & & & & & & & & & &$	Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = 100 V, $V_{GS}$ = 10 V, $I_{D}$ = 58 A		23		nC
$\begin{tabular}{ c c c c c c } \hline Turn-On Delay Time^{C} & t_{d(on)} \\ \hline Rise Time^{C} & t_{r} \\ \hline Turn-Off Delay Time^{C} & t_{d(off)} \\ \hline Turn-Off Delay Time^{C} & t_{d(off)} \\ \hline Fall Time^{C} & t_{f} \\ \hline $	Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			34		
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Gate Resistance	R <sub>g</sub>		0.5	1.3	3.1	Ω
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			24	35	
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Rise Time <sup>c</sup>	t <sub>r</sub>			220	330	ns
Fail Time* $I_{f}$ $I_{c}$ $200$ $300$ Source-Drain Diode Ratings and Characteristics $T_{C} = 25 \ ^{\circ}C^{b}$ Continuous Current $I_{S}$ $70$ $70$ Pulsed Current $I_{SM}$ $115$ $70$ Forward Voltage <sup>a</sup> $V_{SD}$ $I_{F} = 58 \ A, V_{GS} = 0 \ V$ $1.0$ $1.5$ $V$ Reverse Recovery Time $t_{rr}$ $I_{F} = 30 \ A, di/dt = 100 \ A/\mus$ $8$ $12$ $A$	Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	- 5		45	70	
$\begin{tabular}{ c c c c c c } \hline Continuous Current & I_S & & & & & & & & & & & & & & & & & & &$	Fall Time <sup>c</sup>	t <sub>f</sub>	Ω		200	300	
Pulsed Current I I A   Forward Voltage <sup>a</sup> V <sub>SD</sub> I <sub>F</sub> = 58 A, V <sub>GS</sub> = 0 V 1.0 1.5 V   Reverse Recovery Time $t_{rr}$ 130 200 ns   Peak Reverse Recovery Current I <sub>RM(REC)</sub> I <sub>F</sub> = 30 A, di/dt = 100 A/µs 8 12 A	Source-Drain Diode Ratings and Cha	aracteristics 7	$\Gamma_{\rm C} = 25 \ {}^{\circ}{\rm C}^{\rm b}$				
Pulsed CurrentI I SM115115Forward Voltage <sup>a</sup> $V_{SD}$ $I_F = 58 \text{ A}, V_{GS} = 0 \text{ V}$ 1.01.5VReverse Recovery Time $t_{rr}$ 130200nsPeak Reverse Recovery Current $I_{RM(REC)}$ $I_F = 30 \text{ A}, di/dt = 100 \text{ A/µs}$ 812A	Continuous Current	۱ <sub>S</sub>				70	^
Reverse Recovery Time $t_{rr}$ 130200nsPeak Reverse Recovery Current $I_{RM(REC)}$ $I_F = 30 \text{ A}$ , di/dt = 100 A/µs812A	Pulsed Current	I <sub>SM</sub>			115		А
Reverse Recovery Time $t_{rr}$ 130200nsPeak Reverse Recovery Current $I_{RM(REC)}$ $I_F = 30 \text{ A}$ , di/dt = 100 A/µs812A	Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{F} = 58 \text{ A}, V_{GS} = 0 \text{ V}$		1.0	1.5	V
	Reverse Recovery Time	t <sub>rr</sub>			130	200	ns
Reverse Recovery Charge Q <sub>rr</sub> 0.52 1.2 μC	Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 30 A, di/dt = 100 A/µs		8	12	А
	Reverse Recovery Charge	Q <sub>rr</sub>			0.52	1.2	μC

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

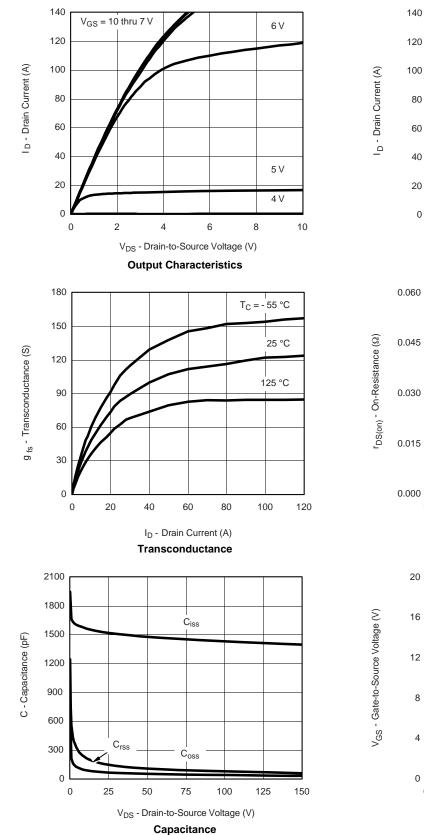
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

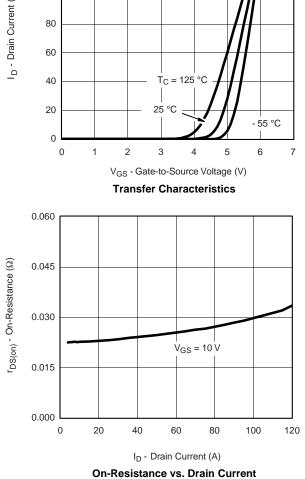
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

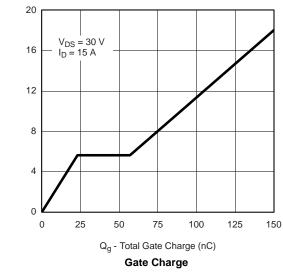
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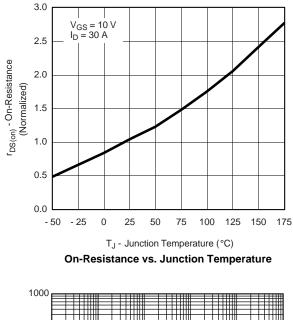
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

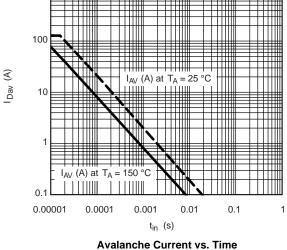


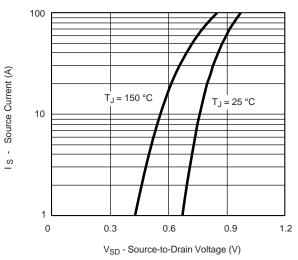




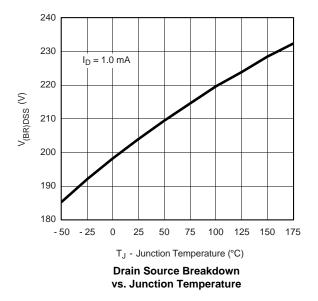
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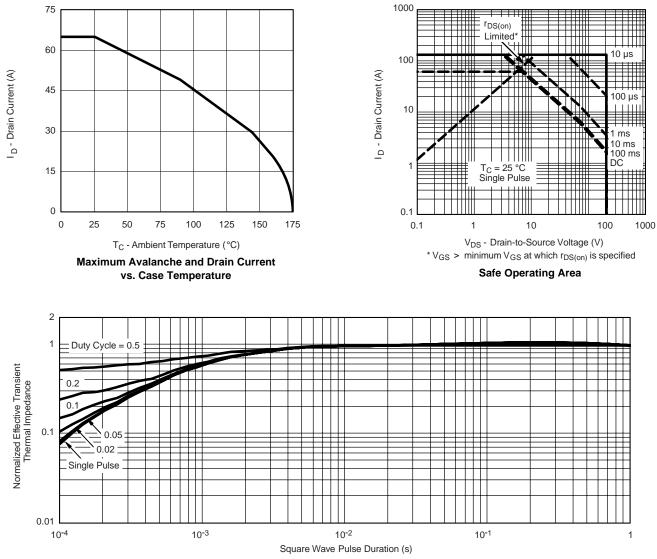


Source-Drain Diode Forward Voltage





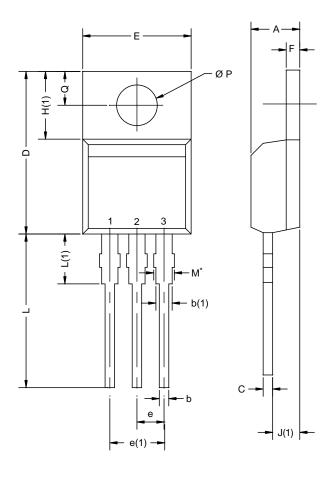
#### **THERMAL RATINGS**



Normalized Thermal Transient Impedance, Junction-to-Case



## **TO-220AB**



	MILLIN	IETERS	INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: X12- DWG: 547	0208-Rev. N, 1	08-Oct-12		

#### Notes

\* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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