



RECOMMENDED P.C.B LAYOUT
TOLERANCE IS: ±0.05

PAD AREA
 KEEP OUT AREA
 NO COPPER AREA (NO TRACE & VIA GND)
 GND PATTERN ONLY

PIN DESIGN		
13	GND	Ground
12	PETp0	PCIe Gen3 Tx singal
11	PETn0	PCIe Gen3 Tx singal
10	GND	Ground
9	PERp0	PCIe Gen3 Rx singal
8	PERn0	PCIe Gen3 Rx singal
7	GND	Ground
6	REFCLK+	Reference Clock input
5	REFCLK-	Reference Clock input
4	INS#	Media detection and power control
3	CLKREQ#	Indication of REFCLK to Host
2	+3.3V	3.3V Power Supply
1	PERST#	PCIe Express function reset

SPECIFICATION

- Material:
 - Housing: LCP S475, Rated UL94V-0, Color: Black.
 - Contacts: C7025 TM03
 - Matte Tin 80u"MIN. at Solder Tail,
 - Plating 15U" on Contact Area. Over Nickle 50u"MIN.
 - Shell: SUS304 1/2H.
 - Goldflash at Solder Tail, Over Nickel 30u"MIN. Plated.
 - LOCK PIN: SUS304 3/4H. Not Plating.
 - SPRING: SWP-B. Not Plating.
 - CAM ROD: SUS304H. Not Plating.
- Electrical Characteristics:
 - Rated voltage : 500Vrms AC for 1 min.
 - Current rating : 1.0 A.Min.per pin.
 - Operating Temperature: -25°C~+85°C.
 - Insulation resistance: 1000M ohms Min. at 500VDC for 1ms.
 - Dielectric withstanding voltage: 500 VAC/1minute.
 - Contact resistance: Initial: 55m ohms max. Final: Δ40m ohms max.
 - Insertion Force: 25N Max.
 - Removal Force: 0.5N Min. 20N Max.
 - Inverse Insertion: 50N Min.
 - Mating cycle: 10000 cycles, Spring is not breaking.

深圳市华宇创精密电子有限公司

TOLERANCE: XX ±0.30 X.XX ±0.25 X.XXX ±0.15 X° ±2° X.X° ±0.5°	DRAWN BY : 陈一鸣	DATE : 2014-02-23	PART NAME: CF Express Type A PUSH-CONN.
	CHECKED BY: 马跃	DATE : 2014-02-23	PART NO. HYCW29-CF13-375B
UNIT: mm [inch] SCALE: 1:1 SIZE: A4	APPROVED BY: 邱敏	DATE : 2014-02-23	DRAW NO.: HYC-2304241121
			SHEET NO. 1 OF 1