

Description

The FDN86246 is the high cell density trenched N-ch MOSFETs, which provides excellent RDSON and efficiency for most of the small power switching and load switch applications.

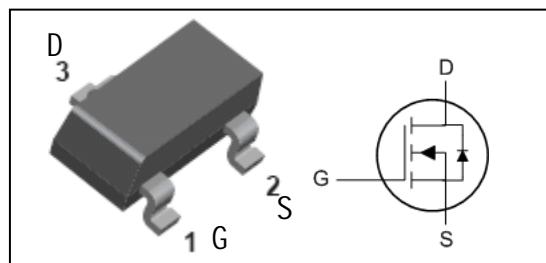
The FDN86246 meet the RoHS and Green Product requirement with full function reliability approved.

Product Summary

V _{DS}	150	V
R _{DS(ON),max}	280	mΩ
I _D	1.8	A

- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

SOT23 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	150	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	1.8	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	1.4	A
I _{DM}	Pulsed Drain Current ²	7.2	A
P _D @T _A =25°C	Total Power Dissipation ³	1.3	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	100	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	80	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	100	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=10\text{V}$, $I_D=2\text{A}$	---	240	280	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=1\text{A}$	---	250	300	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1.0	2	2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=150\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=150\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	30	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Q_g	Total Gate Charge (10V)	$V_{\text{DS}}=75\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=2\text{A}$	---	18	---	nC
Q_{gs}	Gate-Source Charge		---	4.6	---	
Q_{gd}	Gate-Drain Charge		---	5.7	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=75\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=6\Omega$ $I_D=2\text{A}$	---	13	---	ns
T_r	Rise Time		---	19	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	26	---	
T_f	Fall Time		---	9	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=75\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	840	---	pF
C_{oss}	Output Capacitance		---	119	---	
C_{rss}	Reverse Transfer Capacitance		---	25	---	

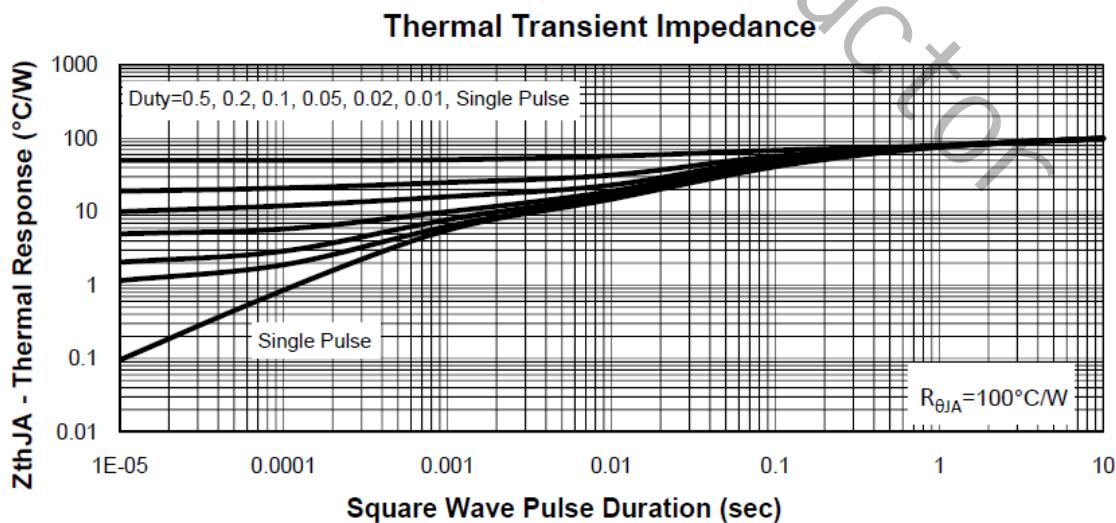
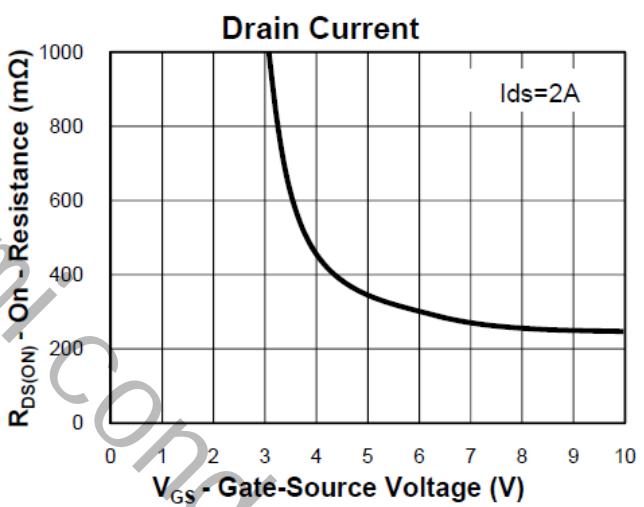
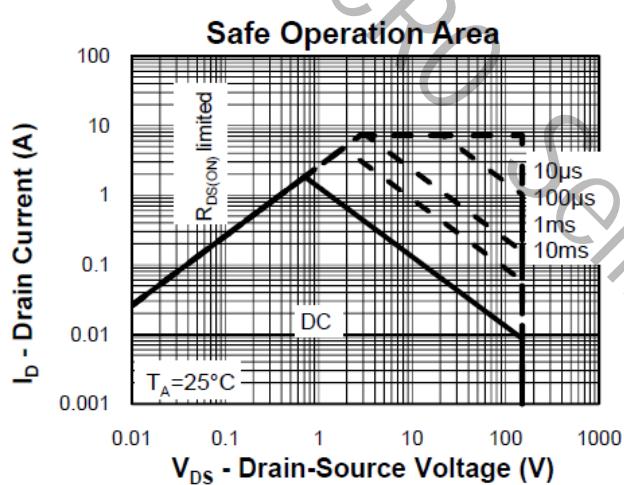
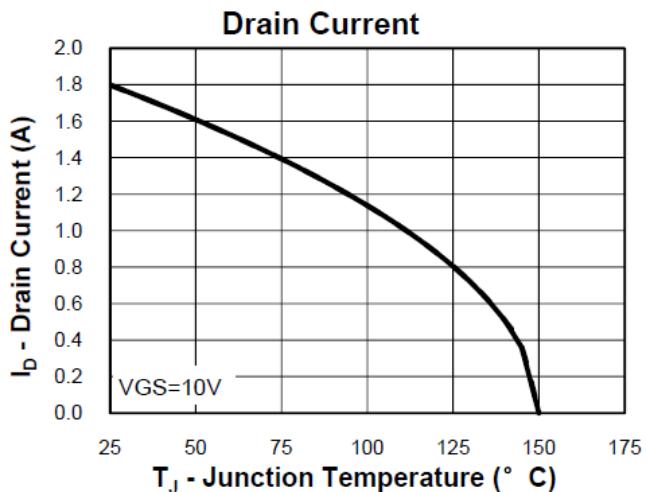
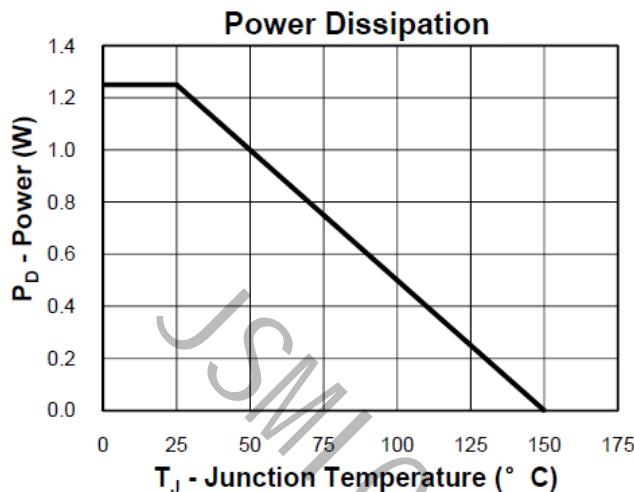
Diode Characteristics

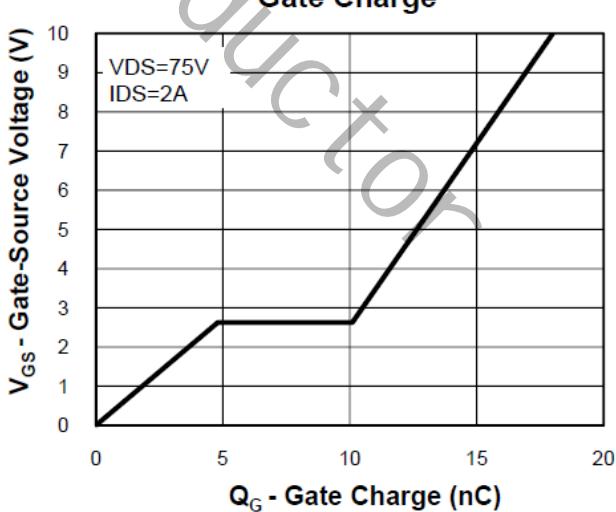
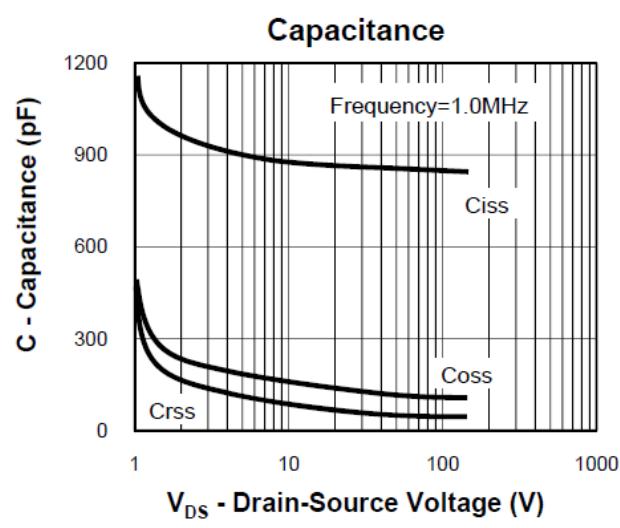
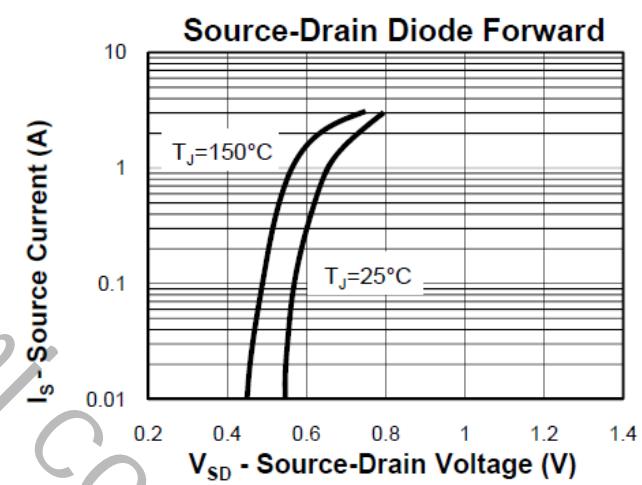
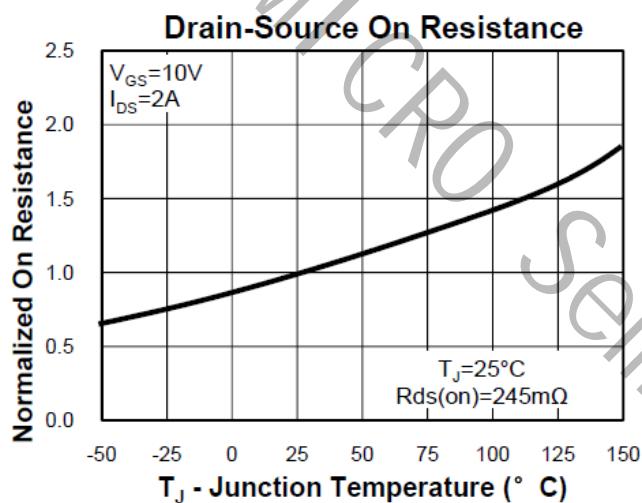
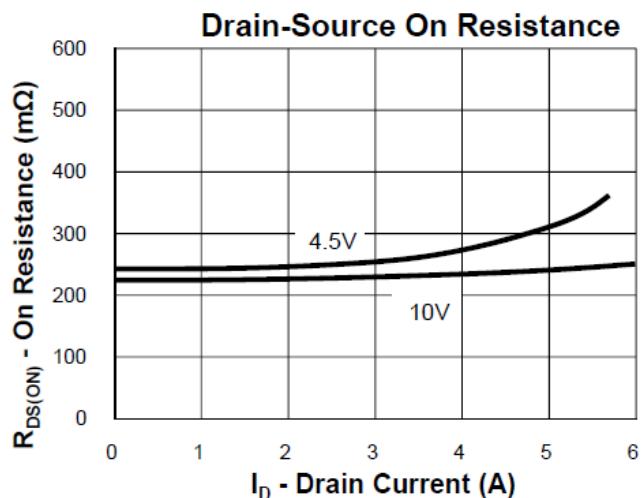
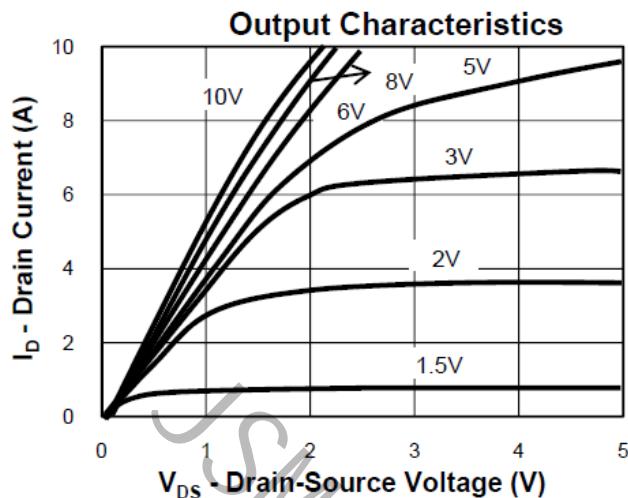
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,4}	$V_G=V_D=0\text{V}$, Force Current	---	---	1.8	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

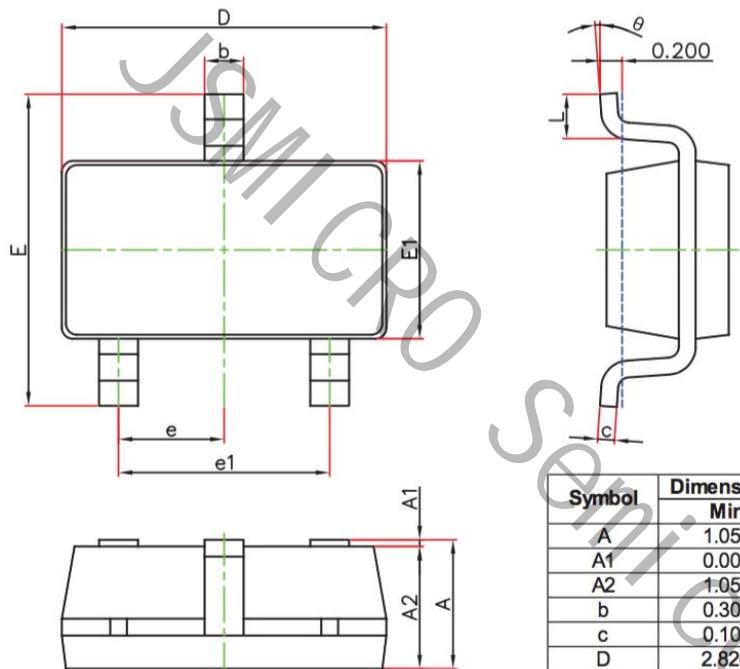
Typical Characteristics





Ordering Information

Part Number	Package code	Packaging
FDN86246	SOT-23	3000/Tape&Reel



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°