

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology
- ★ 100% EAS Guaranteed

### Product Summary

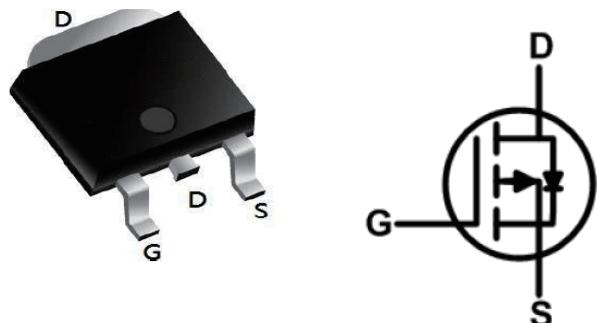
| BVDSS | RDSON | ID   |
|-------|-------|------|
| -30V  | 18mΩ  | -35A |

### Description

The 40P03 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The 40P03 meet the RoHS and Green Product requirement with full function reliability approved.

### TO 252 Pin Configuration



### Absolute Maximum Ratings

| Symbol                                | Parameter   | Rating     |              | Units |
|---------------------------------------|---|------------|--------------|-------|
|                                       |   | 10s        | Steady State |       |
| V <sub>DS</sub>                       | Drain-Source Voltage  | -30        |              | V     |
| V <sub>GS</sub>                       | Gate-Source Voltage   | ±20        |              | V     |
| I <sub>D</sub> @T <sub>c</sub> =25°C  | Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup> | -35        |              | A     |
| I <sub>D</sub> @T <sub>c</sub> =100°C | Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup> | -17        |              | A     |
| I <sub>DM</sub>                       | Pulsed Drain Current <sup>2</sup>                             | -80        |              | A     |
| EAS                                   | Single Pulse Avalanche Energy <sup>3</sup>                    | 35         |              | mJ    |
| I <sub>AS</sub>                       | Avalanche Current   | -10        |              | A     |
| P <sub>D</sub> @T <sub>A</sub> =25°C  | Total Power Dissipation <sup>4</sup>                          | 4          |              | W     |
| T <sub>STG</sub>                      | Storage Temperature Range                                     | -55 to 150 |              | °C    |
| T <sub>J</sub>                        | Operating Junction Temperature Range                          | -55 to 150 |              | °C    |

### Thermal Data

| Symbol           | Parameter  | Typ. | Max | Unit |
|------------------|--|------|-----|------|
| R <sub>θJA</sub> | Thermal Resistance Junction-Ambient <sup>1</sup> | ---  | 32  | °C/W |

Electrical Characteristics ( $T_J = 25^\circ\text{C}$  unless otherwise specified)

| Symbol  | Parameter  | Test condition  | Min. | Typ. | Max.      | Units            |
|---|--|---|------|------|-----------|------------------|
| <b>Off Characteristics</b>                                    |  |   |      |      |           |                  |
| $V_{(BR)DSS}$   | Drain-Source Breakdown Voltage                           | $V_{GS}=0\text{V}, I_D = -250\mu\text{A}$   | -30  | -    | -         | V                |
| $I_{DS(on)}$  | Zero Gate Voltage Drain Current                          | $V_{DS} = -30\text{V}, V_{GS} = 0\text{V}$  | -    | -    | -1        | $\mu\text{A}$    |
| $I_{GSS}$   | Gate to Body Leakage Current                             | $V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$                                     | -    | -    | $\pm 100$ | nA               |
| <b>On Characteristics</b>                                     |  |   |      |      |           |                  |
| $V_{GS(th)}$  | Gate Threshold Voltage                                   | $V_{DS} = V_{GS}, I_D = -250\mu\text{A}$  | -1   | -1.5 | -2.5      | V                |
| $R_{DS(on)}$<br>Note3   | Static Drain-Source on Resistance                        | $V_{GS} = -10\text{V}, I_D = -9\text{A}$  | -    | 18   | 25        | $\text{m}\Omega$ |
|   |  | $V_{GS} = -4.5\text{V}, I_D = -5\text{A}$   | -    | 27   | 38        |                  |
| <b>Dynamic Characteristics</b>                                |  |   |      |      |           |                  |
| $C_{iss}$   | Input Capacitance  | $V_{DS} = -15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$                     | -    | 1200 | -         | pF               |
| $C_{oss}$   | Output Capacitance                                       |   | -    | 155  | -         | pF               |
| $C_{rss}$   | Reverse Transfer Capacitance                             |   | -    | 139  | -         | pF               |
| $Q_g$   | Total Gate Charge  | $V_{DS} = -15\text{V}, I_D = -8\text{A}, V_{GS} = -10\text{V}$                    | -    | 52   | -         | nC               |
| $Q_{gs}$  | Gate-Source Charge                                       |   | -    | 9.8  | -         | nC               |
| $Q_{gd}$  | Gate-Drain("Miller") Charge                              |   | -    | 8.3  | -         | nC               |
| <b>Switching Characteristics</b>                              |  |   |      |      |           |                  |
| $t_{d(on)}$   | Turn-on Delay Time                                       | $V_{DD} = -15\text{V}, I_D = -1\text{A}, V_{GS} = -10\text{V}, R_{GEN} = 6\Omega$ | -    | 13   | -         | ns               |
| $t_r$   | Turn-on Rise Time  |   | -    | 15   | -         | ns               |
| $t_{d(off)}$  | Turn-off Delay Time                                      |   | -    | 198  | -         | ns               |
| $t_f$   | Turn-off Fall Time                                       |   | -    | 98   | -         | ns               |
| <b>Drain-Source Diode Characteristics and Maximum Ratings</b> |  |   |      |      |           |                  |
| $I_s$   | Maximum Continuous Drain to Source Diode Forward Current | -   | -    | -35  | -         | A                |
| $I_{SM}$  | Maximum Pulsed Drain to Source Diode Forward Current     | -   | -    | -80  | -         | A                |
| $V_{SD}$  | Drain to Source Diode Forward Voltage                    | $V_{GS} = 0\text{V}, I_s = -9\text{A}$  | -    | -0.8 | -1.2      | V                |

## Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition:  $T_J=25^\circ\text{C}, V_{DD}=-15\text{V}, V_G=-10\text{V}, R_G=25\Omega, L=0.5\text{mH}, I_{AS}=-10\text{A}$
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$

### Typical Performance Characteristics

Figure 1: Output Characteristics

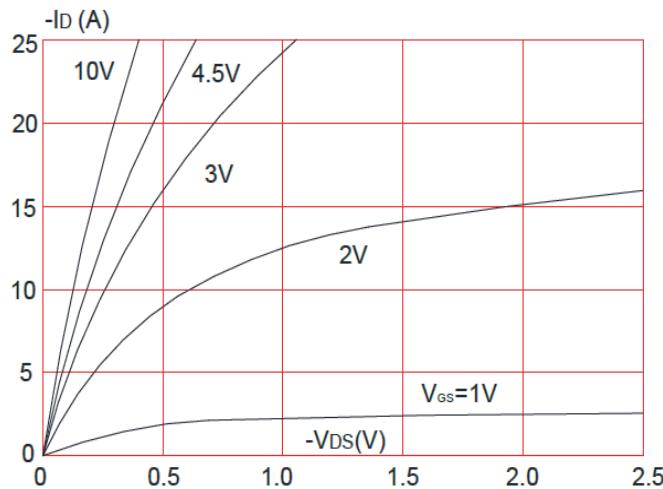


Figure 2: Typical Transfer Characteristics

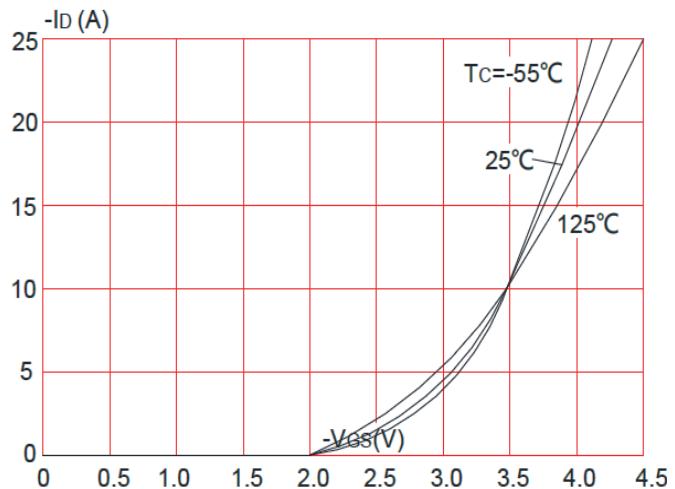


Figure 3: On-resistance vs. Drain Current

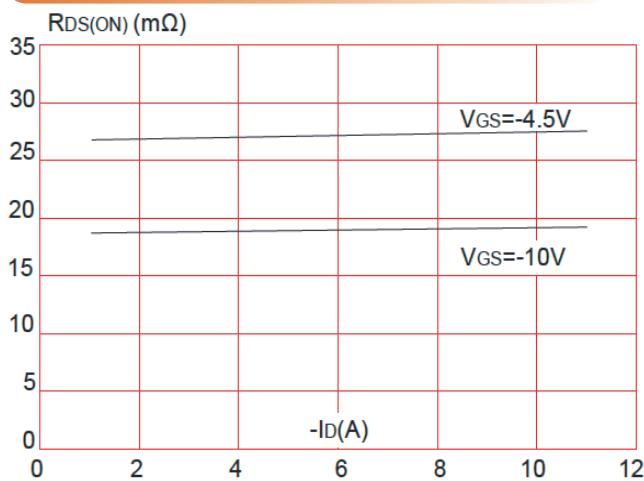


Figure 4: Body Diode Characteristics

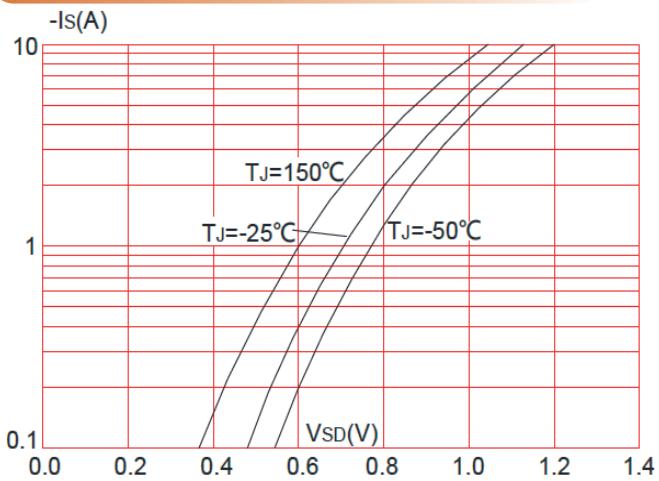


Figure 5: Gate Charge Characteristics

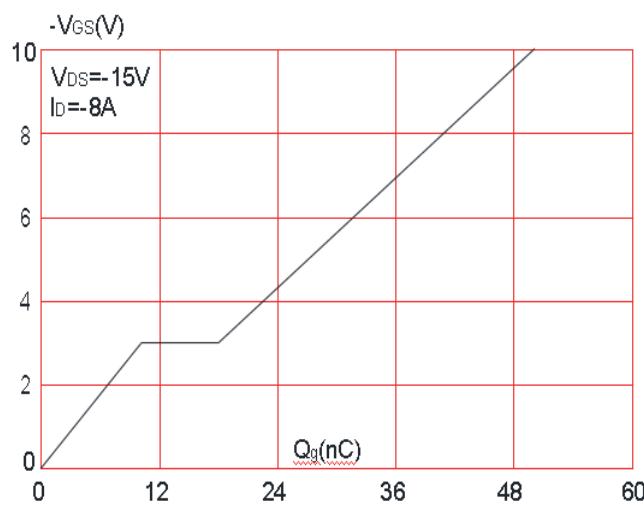
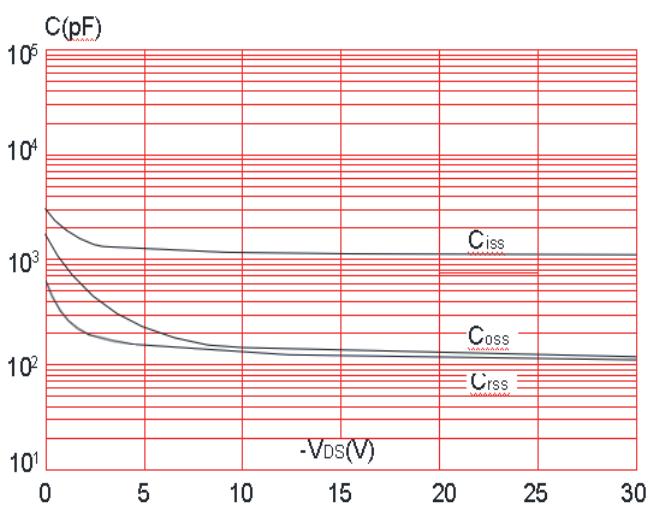


Figure 6: Capacitance Characteristics



### Typical Performance Characteristics

Figure 7: Normalized Breakdown Voltage

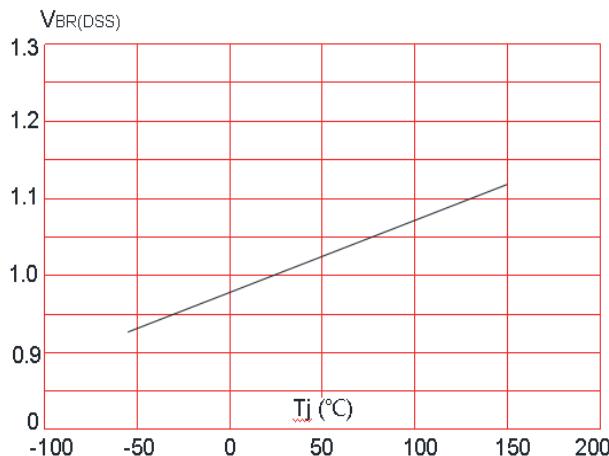


Figure 8: Normalized on Resistance vs. Junction Temperature

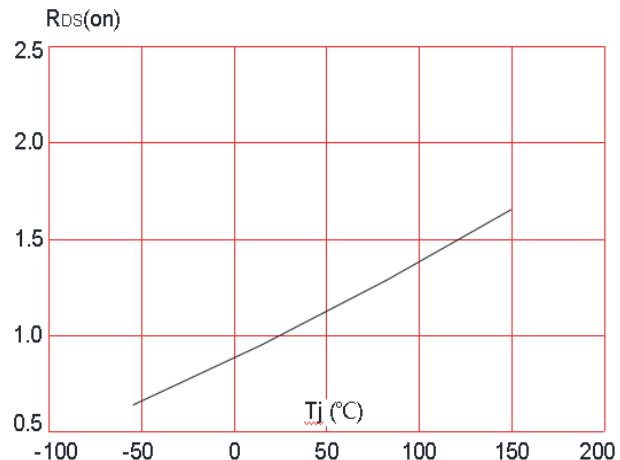


Figure 9: Maximum Safe Operating Area

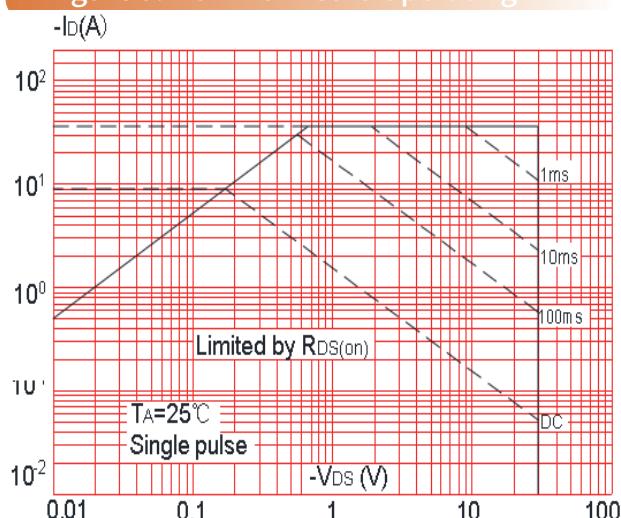


Figure 10: Maximum Continuous Drain Current

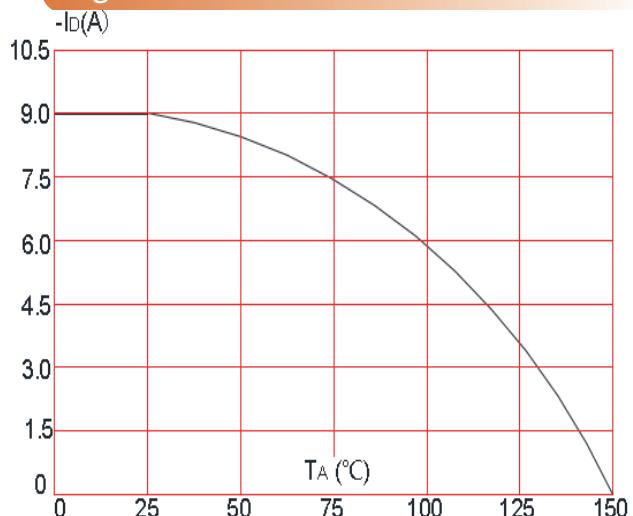
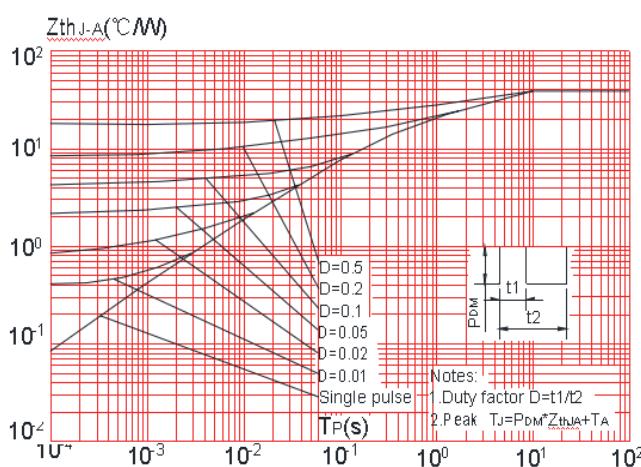
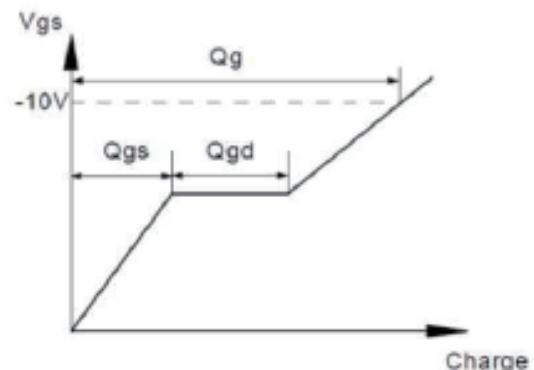
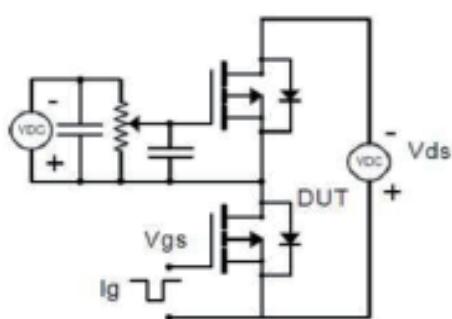


Figure 11: Maximum Effective Transient Thermal Resistance

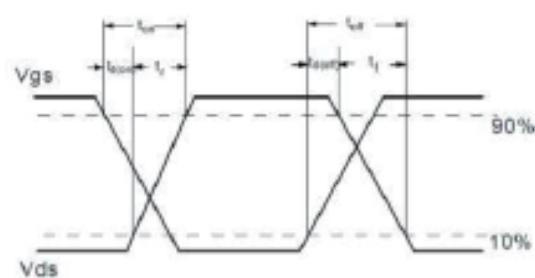
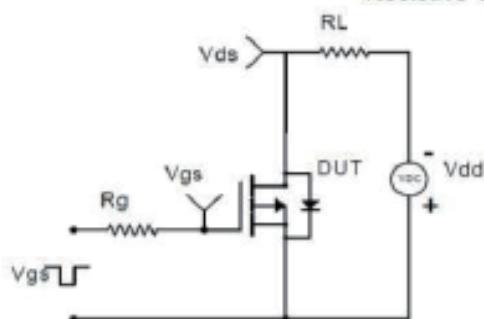


## Test Circuit

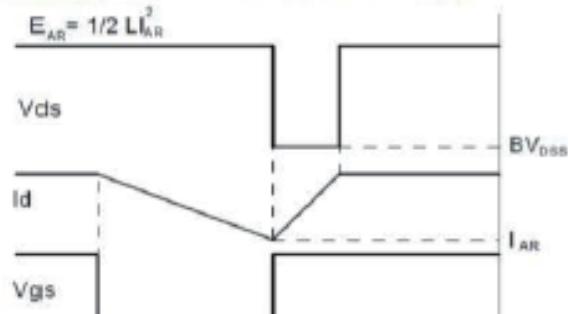
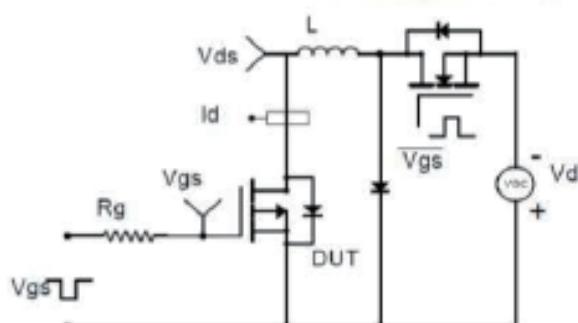
Gate Charge Test Circuit &amp; Waveform



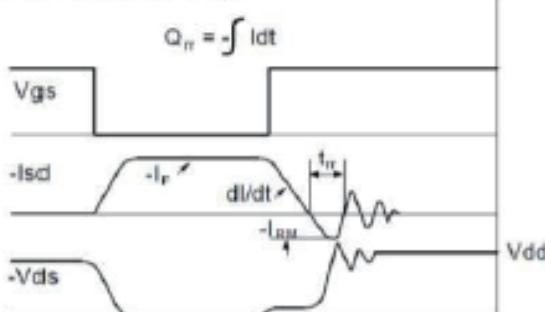
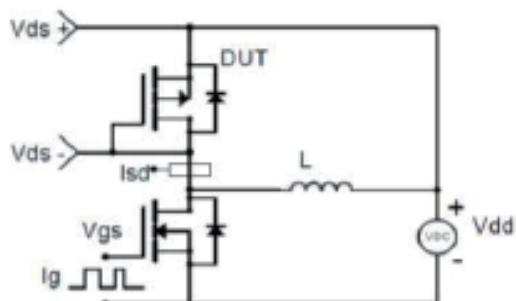
Resistive Switching Test Circuit &amp; Waveforms



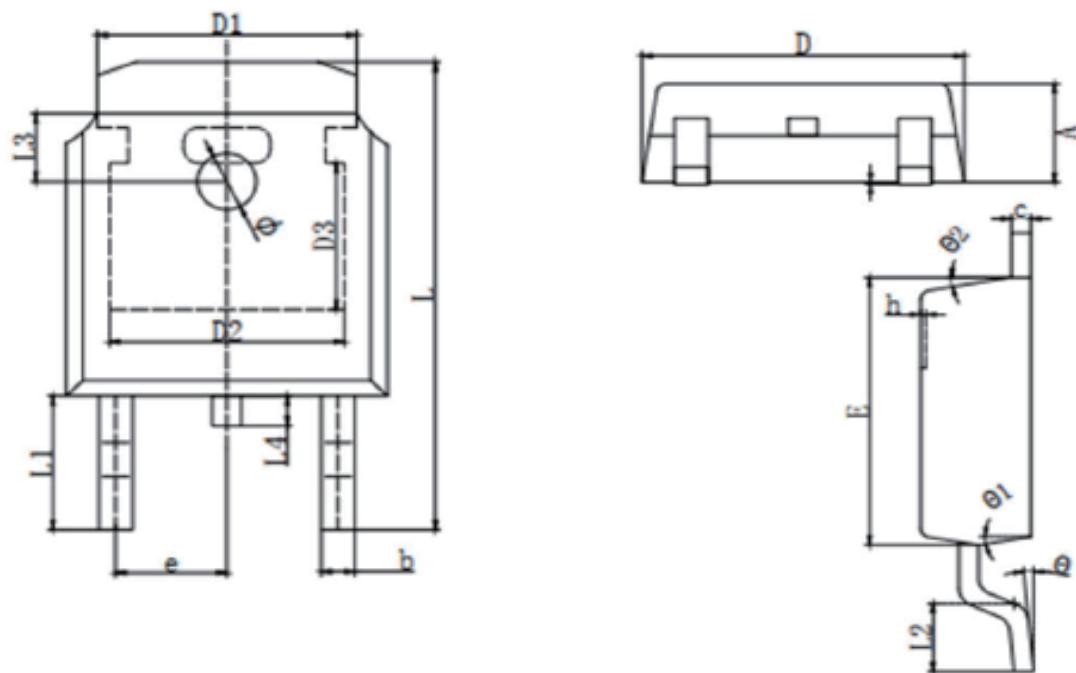
Unclamped Inductive Switching (UIS) Test Circuit &amp; Waveforms



Diode Recovery Test Circuit &amp; Waveforms



## TO-252 Package outline



| SYMBOL | MILLIMETER |       | SYMBOL | MILLIMETER |       |
|--------|------------|-------|--------|------------|-------|
|        | MIN        | MAX   |        | MIN        | MAX   |
| A      | 2.200      | 2.400 | h      | 0.000      | 0.200 |
| A1     | 0.000      | 0.127 | L      | 9.900      | 10.30 |
| b      | 0.640      | 0.740 | L1     | 2.888 REF  |       |
| c      | 0.460      | 0.590 | L2     | 1.400      | 1.700 |
| D      | 6.500      | 6.700 | L3     | 1.600 REF  |       |
| D1     | 5.334 REF  |       | L4     | 0.600      | 1.000 |
| D2     | 4.826 REF  |       | Φ      | 1.100      | 1.300 |
| D3     | 3.166 REF  |       | θ      | 0°         | 8°    |
| E      | 6.000      | 6.200 | θ1     | 9° TYP2    |       |
| e      | 2.286 TYP  |       | θ2     | 9° TYP     |       |